



**KLE** Technological  
University  
Creating Value  
Leveraging Knowledge

Earlier known as  
B. V. B. College of Engineering & Technology

## Department of Electrical and Electronics Engineering

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### BOS Meeting Details for the Last Five Years



**Structure of Board of Studies**  
**2<sup>nd</sup> BOS Meeting – 2<sup>nd</sup> April 2016**

The meeting of Board of Studies in E&E Engineering is convened on Saturday 2<sup>nd</sup> April 2016 to discuss & approve the following. The meeting will be held at Senate Hall of the department at 10.30am.

1. Approval of syllabi for 2016-20 batch of I /II semester Basic Electrical Engineering
2. Approval of Scheme for 2016-20 batch of 3<sup>rd</sup> to 8<sup>th</sup> semester
3. Approval of Scheme & syllabi for 2015-19 batch of 3<sup>rd</sup> & 4<sup>th</sup> semester

S. No.	Category	Nomination of the Committee		Name of the Person	Signature
1	Concerned Head of the Department/School/Centre	Chairperson	1	Dr. A B Raju	
2	One Professor, One Associate Professor and One Assistant Professor from the Department/School/ Centre, nominated by the Dean Academic Affairs	Members	1	Dr. S R Karnik	
			2	Smt. Rohini B Jyoti	
			3	Smt. Leah S Joshi	
3	Two Subject Experts from outside the college nominated by the Vice Chancellor	Members	1	Dr. D S Gaonkar, Professor, NITK, Surathkal.	
			2	Prof. B. N. Choudhari, Professor, Electrical Engineering & Dean, Quality Assurance College of Engineering, Pune.	 Bhushan Choudhari 2/4/16
4	TWO representative from industry corporate sector / allied area relating to placement nominated by the Vice - Chancellor	Member	1	Mr. V D Kulkarni, Consultant, Analog/Mixed Signal Circuits Design & Training, Entuple Technologies Bangalore.	
				Mr. Md. Nayeem Robert Bosch, Bangalore.	
5	One Post – graduate meritorious alumnus nominated by the Vice - Chancellor	Member	1	Mr. Allahbakhsh Asadullah, Infosys, Bangalore.	
6	One Student member representing each of the program offered by the Department / School / Centre	Invited Member	1	Ms. Radhika Shiralkar	
			2	Mr. Vivek Bhat	

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**Department of Electrical & Electronics Engineering**

**Minutes of BOS Meeting on 2<sup>nd</sup> April 2016**

1. For 3<sup>rd</sup> semester students use flipped class room approach for some topics of the syllabus of Circuit Analysis.
2. At the stage of 4<sup>th</sup> semester learning 32 bit microcontroller (ARM) with no proper background will be difficult.
3. Include as many tutorials as possible for difficult courses where problem solving is intensive.
4. Include memories in digital electronics course for 3<sup>rd</sup> semester.
5. Include one chapter on Energy conversion in electrical machines course.

Final 2015-16





**Structure of Board of Studies**  
**3<sup>rd</sup> BOS Meeting – 15<sup>th</sup> April 2017**

The meeting of Board of Studies in E&E Engineering is convened on Saturday 15<sup>th</sup> April 2017 to discuss & approve the following. The meeting will be held at Senate Hall of the department at 9.00am.

1. Approval for Scheme of 2016-20 batch for 3<sup>rd</sup> to 8<sup>th</sup> semester
2. Approval for syllabi of 2016-20 batch for 3<sup>rd</sup> & 4<sup>th</sup> semester
3. Approval for Scheme & syllabi of 2015-19 batch for 5<sup>th</sup> & 6<sup>th</sup> semester
4. Approval for syllabi of 2017-21 batch for I /II semester Basic Electrical Engineering

S. No.	Category	Nomination of the Committee		Name of the Person	Signature
1	Concerned Head of the Department/ School/ Center	Chairperson	1	Dr. A B Raju Prof. & Head, E&E Dept.,	
2	ONE Professor, ONE Associate Professor and ONE Assistant Professor from the Department/ School/ Center, nominated by the Dean Academic Affairs	Members	1	Dr. S R Karnik Professor, E&E Dept.,	
			2	Smt. Rohini B Jyoti Associate Professor, E&E Dept.,	
			3	Smt. Minal Salunke Assistant Professor, E&E Dept.,	
3	TWO Subject experts from outside the college nominated by the Vice-Chancellor	Members	1	Dr. Balaraman Power Research & Development Consultant, Bangalore	
			2	Dr. Dambhare Professor, E&E Dept., College of Engineering, Pune.	
4	TWO representative from industry corporate sector/ allied area relating to placement nominated by the Vice-Chancellor	Members	1	Mr. Allahbaksh Asadullah Product Technical Architect. Infosys, Bangalore.	
			2	Sri. Manjunath Dixit Vice President, General Electric Capital, Mumbai	
5	Co-opted Members	Members	1	Sri. S B Artal Associate Professor, E&E Dept.,	
			2	Smt. Jyoti C Pattanshetti Associate Professor, E&E Dept.,	
			3	Mr. Sachin Angadi Assistant Professor, E&E Dept.,	
6	ONE Post-graduate meritorious alumnus nominated by the Vice-Chancellor	Member	1	Dr. Savita Angadi, Scientist- Software Specialist – Analytics, SAS Research and Development (India) Pvt. Ltd. Pune.	
7	ONE Student Member representing each of the program offered by the Department/ School/ Center	Invited Member	1	Mr. Vinod Patil	

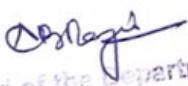


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## Department of Electrical & Electronics Engineering

### Minutes of BOS Meeting held on 15-04-2017

1. Scheme and syllabus of V/VI and VI/VII Semester's, KLE Tech. 2015-19, batch was reviewed. Following suggestions were made:
  - Machine Learning Lab - Can be connected through MOOCs.
  - Relay and High Voltage Engineering lab - Offer core course on relay and protection and HV can be elective. Some portion of protection can be included in PSA and/or PSOC.
  - HVAC and building management course can be included.
  - Waste management and power generation course can be included.
  - Curriculum Structure is very flexible with 6 electives.
  - Possible to have smart-grid with our own SCADA.
  - Projects on existing power plant renovation can be given for final year students—opportunity for multi-disciplinary projects.
2. Scheme and syllabus of III/IV Semester's, KLE Tech. 2016-20, and batch was reviewed. Following suggestions were made:
  - Engineering design – Problems can be taken from smart city initiatives or industry.
  - Microcontroller architecture & programming – possibly make L-T-P as 0-1-1.
  - Electrical Power Generation, Transmission & Distribution - Curriculum structure can be arranged as unit-I on generation, unit-II on transmission and unit-III as distribution. Power generation course can be offered through MOOCs.
  - ARM Microcontroller Lab - Introduction to RISC and CISC.
  - 3<sup>rd</sup> semester students must have induction program with exposure to global challenges for electrical engineers.Following change was made:
  - Electrical Machines – Only one electrical machine course is being introduced.
3. Syllabus of I/II Semester's, of Basic Electrical Engineering, KLE Tech. 2017-21, batch was reviewed and approved.

  
Head of the Department  
Electrical & Electronics Engineering  
KLE Technological University.,  
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**Vertical -1  
(Embedded Systems)**

Course	Suggestions	BOS Member	Actions Initiated
Computer Architectures	-		Newly introduced
Embedded Linux	-		
IOT	-		
Autosar & Infotainment	-		
Multi Core & Multiprocessor Architecture	-		

**Vertical -2  
(Power Electronics and Drives)**

Course	Suggestions	BOS Member	Actions Initiated
Digital Control Systems	-		Newly introduced
Advanced Electric Drive Vehicles	-		
Model Based Real Time Control Systems	-		
Power Quality and Ancillary services	This course can be shown in vertical 3 without Ancillary services in title.	KB	

  
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**Vertical -3  
(Modern Power and Energy Systems)**

Course	Suggestions	BOS Member	Actions Initiated
Modelling and Analysis of Hybrid Electrical Energy Systems	-		Newly introduced
Smart Grids	-		
Soft Computing applications to Power Systems	-		
Reliability Engineering	Power system reliability should be taught in this course	KB	

  
**Head of the Department**  
**Electrical & Electronics Engineering**  
**KLE Technological University.,**

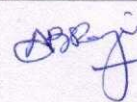
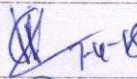
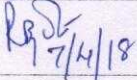
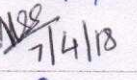
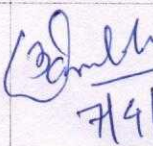

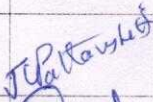




**Department of Electrical & Electronics Engineering**
**Structure of Board of Studies**
**4<sup>th</sup> BOS Meeting – 7<sup>th</sup> April 2018**

The meeting of Board of Studies in E&E Engineering is convened on Saturday 7<sup>th</sup> April 2018 to discuss & approve the following. The meeting will be held at EEE Department Computer Centre at 10.00am.

1. Approval for Scheme of 2017-21 batch for 3<sup>rd</sup> to 8<sup>th</sup> semester
2. Approval for syllabi of 2017-21 batch for 3<sup>rd</sup> & 4<sup>th</sup> semester
3. Approval for Scheme & syllabi of 2016-20 batch for 5<sup>th</sup> & 6<sup>th</sup> semester
4. Approval for Scheme & syllabi of 2015-19 batch for 7<sup>th</sup> & 8<sup>th</sup> semester
5. Approval for syllabi of 2018-22 batch for I /II semester Basic Electrical Engineering

**BOS Members**

S. No.	Category	Nomination of the Committee		Name of the Person	Signature
1	Concerned Head of the Department/ School/ Centre	Chairperson	1	Dr. A B Raju Prof. & Head, Electrical & Electronics Engg. Dept. Email: <a href="mailto:abraju@bvb.edu">abraju@bvb.edu</a>	
2	ONE Professor, ONE Associate Professor and ONE Assistant Professor from the Department/ School/ Centre, nominated by the Dean Academic Affairs	Members	1	Dr. S R Karnik Electrical & Electronics Engg. Dept.	
			2	Smt. Rohini B Jyoti Electrical & Electronics Engg. Dept.	
			3	Smt. Minal Salunke Electrical & Electronics Engg.	
3	TWO Subject experts from outside the college nominated by the Vice-Chancellor	Members	1	Dr. Dambhare Professor, E&E Dept., College of Engineering, Pune.	
4	TWO representative from industry corporate sector/ allied area relating to placement nominated by the Vice-Chancellor	Members	1	Mr. Allahbaksh Asadullah Product Technical Architect. Infosys, Bangalore. <a href="mailto:Allahbaksh_Asadullah@infosys.com">Allahbaksh_Asadullah@infosys.com</a>	
5	Co-opted Members	Members	1	Sri. S B Artal Electrical & Electronics Engg. Dept.	—
			2	Smt. Jyoti C Pattanshetti Electrical & Electronics Engg. Dept.	
			3	Mr. Sachin Angadi Electrical & Electronics Engg. Dept.	
6	ONE Post-graduate meritorious alumnus nominated by the Vice-Chancellor	Member	1	Mr. Girish Karamadi System Engineer, sDEmbed-SDG2, ALTAIR, Bangalore.	





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S. No.	Category	Nomination of the Committee		Name of the Person	Signature
7	ONE Student Member representing each of the program offered by the Department/ School/ Centre	Member	1	--	—
8	ONE Senior faculty member nominated by the concerned Head of the Department/ School/ Centre	Member	1	Mr. Anoopkumar Patil Electrical & Electronics Engg. Dept.	<i>Patil</i>

## Department of Electrical & Electronics Engineering

### Minutes of BOS Meeting held on 07-04-2018

1. Scheme & Syllabus of VII/VIII Semester's, KLE Tech. 2015-19, and batch was reviewed and approved.
2. Introduced Open Elective courses such as Artificial Neural Networks & Artificial Intelligence
3. Approved in Open elective courses i) Artificial Neural Networks & Artificial Intelligence
4. Scheme and syllabus of V/VI Semester's, KLE Tech. 2016-20, batch was reviewed.  
Following Changes are made:
  - Data Structure using C Lab is changed to 0-0-3 from 0-0-2 credits.
  - Linear Integrated Circuits is offered in 5<sup>th</sup> semester as a core course.
  - Control System lab is introduced in 5<sup>th</sup> semester with 0-0-1 credits to enhance laboratory component.
  - Power System Modelling, Operation & Control is shifted to 7<sup>th</sup> semester to reduce number of program electives in 7<sup>th</sup> semester.
  - One more program elective (Object Oriented Programming with C++) is offered in 6<sup>th</sup> semester.
  - Digital System Design using Verilog is introduced in 6<sup>th</sup> semester with 0-0-2 credits to enhance laboratory component.
5. Scheme and syllabus of III/IV Semester's, KLE Tech. 2017-21, and batch was reviewed.  
Following Changes are made:
  - Data Structure using C Lab is changed to 0-0-3 credits and offered in 3<sup>rd</sup> semester.
  - Digital System Design using Verilog lab is introduced in 4<sup>th</sup> semester with 0-0-2 credits to enhance laboratory component.
  - Electrical Machines laboratory is offered in 5<sup>th</sup> semester instead of 4<sup>th</sup> semester
6. Syllabus of I/II Semester's, of Basic Electrical Engineering, KLE Tech. 2018-22, batch was reviewed and approved. A topic on Power Converters has been introduced.

  
Head of the Department  
Electrical & Electronics Engineering  
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**Department of Electrical & Electronics Engineering**




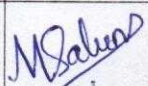



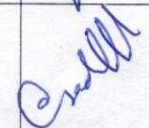

**Structure of Board of Studies**

**5<sup>th</sup> BOS Meeting – 13<sup>th</sup> April 2019**

The meeting of Board of Studies in E&E Engineering is convened on Saturday 13<sup>th</sup> April 2019 to discuss & approve the following. The meeting will be held at EEE Department Computer Centre at 10.00am.

1. Approval for Scheme & Syllabi of 2016-20 batch for 7<sup>th</sup> & 8<sup>th</sup> semester
2. Approval for Scheme & Syllabi of 2017-21 batch for 5<sup>th</sup> & 6<sup>th</sup> semester
3. Approval for Scheme & Syllabi of 2018-22 batch for 3<sup>rd</sup> & 4<sup>th</sup> semester
4. Approval for Scheme & Syllabi of 2019-23 batch for 3<sup>rd</sup> to 8<sup>th</sup> sem scheme & Syllabi of I /II semester Basic Electrical Engineering

**BOS Members**

S. No.	Category	Nomination of the Committee		Name of the Person	Signature
1	Concerned Head of the Department/ School/ Centre	Chairperson	1	Dr. A B Raju Prof. & Head, Electrical & Electronics Engg. Dept.	
2	ONE Professor, ONE Associate Professor and ONE Assistant Professor from the Department/ School/ Centre, nominated by the Dean Academic Affairs	Members	1	Dr. S R Karnik Professor, Electrical & Electronics Engg. Dept.	
			2	Smt. Rohini B Jyoti Associate Professor, Electrical & Electronics Engg. Dept.	
			3	Mrs. Minal Salunke Asst. Professor, Electrical & Electronics Engg. Dept.	
3	TWO Subject experts from outside the college nominated by the Vice-Chancellor	Members	1	Dr. Dambare Professor, E&E Dept., College of Engineering, Pune	
			2	Dr. Arjun Mudlapur Asst. Professor, E&E Dept., MIT, Manipal.	
4	TWO representative from industry corporate sector/ allied area relating to placement nominated by the Vice-Chancellor	Members	1	Dr. Pavana Kumar P Director and Chief Executive Officer Simlife Electric, Bangalore	
			2	Mr. Allahbaksh Asadullah Product Technical Architect. Infosys, Bangalore. Allahbaksh_Asadullah@infosys.com	
5	Co-opted Members	Members	1	Smt. Jyoti C Pattanshetti Associate Professor, Electrical & Electronics Engg. Dept.	





S. No.	Category	Nomination of the Committee		Name of the Person	Signature
			2	Mr. Anoopkumar Patil Asst. Professor, Electrical & Electronics Engg. Dept.	
			3	Mr. Sachin Angadi Asst. Professor, Electrical & Electronics Engg. Dept.	
6	ONE Post-graduate meritorious alumnus nominated by the Vice-Chancellor	Member	1	Dr. Savita Angadi, Scientist- Software Specialist – Analytics, SAS Research and Development (India) Pvt. Ltd. Pune.	—
7	ONE Student Member representing each of the program offered by the Department/ School/ Centre	Member	1	Shivakumar S Tariwal 8 <sup>th</sup> semester Electrical & Electronics Engg. Dept.	
8	ONE Senior faculty member nominated by the concerned Head of the Department/ School/ Centre	Member	1	Ms. Anupama R Itagi Asst. Professor, Electrical & Electronics Engg. Dept.	

(Dr P. G. Tewari)

DEAN ACADEMIC AFFAIRS

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## Department of Electrical & Electronics Engineering

### Minutes of BOS Meeting held on 13-04-2019

1. Scheme & Syllabus of VII/VIII Semester's, KLE Tech. 2016-20, and batch was reviewed.

Following suggestions were made:

- Switched Mode Power Conversion - Introduce Flux walking/flux resetting in DC power supplies.
- IoT - Change course title as "Introduction to IoT and its applications", introduce concept of node red & prolog in chapter 5.
- Power System Modelling, Operation and Control – offered as a core course.
- Flexible AC Transmission Systems (FACTS) - Offered course in Modern Power and Energy Systems vertical.
- Wind and PV Electrical Energy Systems - Offered as an open elective.
- Embedded Linux - Offered as an elective in Embedded Systems Vertical.

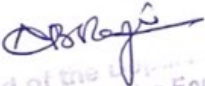
2. Scheme and syllabus of V/VI Semester's, KLE Tech. 2017-21, batch was reviewed.

Following suggestions were made:

- Linear Integrated Circuits - Introduce a lab on LIC, include Band Pass filter concepts in course & Hysteresis concepts, divide teaching hours of UNIT III chapter as 5+5, introduce PLL.
- Power Electronics - Introduce types of control strategies like space vector control.
- Operating Systems and Embedded Systems- Introduce implementation of semaphore in the lab, Visibility of the process, ways to communicate to process, concepts of threading.
- Electrical Machines Lab - Use Mini tab tool, Introduce DOE course.
- Electric Drives and control lab - Forward and Flyback DC DC converter concepts to be dealt before conduction of experiment, SCR based converter can be part of self study.
- MAHEES - Plan Co-teaching by experts.
- Machine Learning - Deep learning with electrical perspective, crack on wind turbines, wires etc.

Following changes were made:

- Battery management system – offered a course in e-mobility vertical.
- CMOS VLSI Circuits - offered course in embedded vertical. Suggested to refer book on CMOS VLSI Design by David Haris and Weste (Latest edition)

  
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Electrical & Electronics Engineering  
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3. Scheme and syllabus of III/IV Semester's, KLE Tech. 2018-22, and batch was reviewed.

Following suggestions were made:

- 8051 microcontroller/ ARM microcontroller - Introduce TI, infinion processor.
- Maths-I - Co-relate Mathematics with physical entities.
- 8051 microcontroller - Introduce RS 485 concepts.
- Electrical Machines - Discuss concepts of DC machines, Transformers and Induction machine concepts in detail, Introduction on Synchronous machines can be included, change teaching hours from 5hrs to 2hrs for Single phase Induction machines, change teaching hours from 5hrs to 8hrs for Synchronous machines, Credit structure from 3-0-0 to 4-0-0, Introduce a chapter on concepts of rotating electrical machines.
- OOPS with C++ - Change of title to OOPS using C++, Concept of headers, Smart pointers, Exception handling chapter to be dealt in earlier chapters.
- Analog Electronics - Introduce linear region, switching and cutoff region in MOSFET characteristics.
- Digital Electronics - Currently used memories to be introduced in Chapter 07.

Following change was made:

- Signals & Systems - Introduce this course for preparing competitive exams like GATE.
4. Syllabus of I/II Semester's, of Basic Electrical Engineering, KLE Tech. 2019-23, batch was reviewed and approved. Following suggestions were made: Introduce difference between IGBT, thyristor along with applications and when they can be used. Flyback converter introduce in power electronics.

  
**Head of the Department**  
**Electrical & Electronics Engineering**  
**KLE Technological University,**  
**HUBBALLI-31.**





**Department of Electrical & Electronics Engineering**  
**Structure of Board of Studies**  
**6<sup>th</sup> BOS Meeting 6<sup>th</sup> June 2020**

The meeting of Board of Studies in E&E Engineering is convened on Saturday 6<sup>th</sup> June 2020 to discuss & approve the following. The meeting will be held at EEE Department Computer Centre at 11.00am.

1. Approval for Scheme & Syllabi of 2017-21 batch for 7<sup>th</sup> & 8<sup>th</sup> semester
2. Approval for Scheme & Syllabi of 2018-22 batch for 5<sup>th</sup> & 6<sup>th</sup> semester
3. Approval for Scheme & Syllabi of 2019-23 batch for 3<sup>rd</sup> & 4<sup>th</sup> semester
4. Approval for Scheme & Syllabi of 2020-24 batch for 3<sup>rd</sup> to 8<sup>th</sup> sem scheme & Syllabi of I /II semester Basic Electrical Engineering

**BOS Members**

S. No.	Category	Nomination of the Committee		Name of the Person	Signature
1	Concerned Head of the Department/ School/ Centre	Chairperson	1	Dr. A B Raju Prof. & Head, Electrical & Electronics Engg. Dept.	
2	ONE Professor, ONE Associate Professor and ONE Assistant Professor from the Department/ School/ Centre, nominated by the Dean Academic Affairs	Members	1	Dr. S R Karnik Professor, Electrical & Electronics Engg. Dept.	
			2	Smt. Jyoti C Pattanshetti Associate Professor, Electrical & Electronics Engg. Dept.	
			3	Mr. Sachin Angadi Asst. Professor, Electrical & Electronics Engg. Dept.	
3	TWO Subject experts from outside the college nominated by the Vice-Chancellor	Members	1	Dr. Sandeep Asst. Professor, E&E Dept., MNIT, Jaipur	
			2	Dr. Shamin Dudu RBEI, Bangalore.	
4	TWO representative from industry corporate sector/ allied area relating to placement nominated by the Vice-Chancellor	Members	1	Dr. Yashomani Yashodhan. Kolhatkar RBEI, Bangalore.	
			2	Mr. Allahbaksh Asadullah Product Technical Architect. Infosys, Bangalore. Allahbaksh_Asadullah@infosys.com	





S. No.	Category	Nomination of the Committee		Name of the Person	Signature
5	Co-opted Members	Members	1	Mr. Anoopkumar Patil Asst. Professor, Electrical & Electronics Engg. Dept.	
			2	Mrs. Leah S Joshi Asst. Professor, Electrical & Electronics Engg. Dept.	
			3	Mr. Gurubasu Hombal Asst. Professor, Electrical & Electronics Engg. Dept.	
6	ONE Post-graduate meritorious alumnus nominated by the Vice-Chancellor	Member	1	Dr. Savita Angadi, Scientist- Software Specialist – Analytics, SAS Research and Development (India) Pvt. Ltd. Pune.	
7	ONE Student Member representing each of the program offered by the Department/ School/ Centre	Member	2	Ms. Divya Badiger 8 <sup>th</sup> semester E&E Dept. Mr. Chinmay Kulkarni 8 <sup>th</sup> semester E&E Dept	
8	ONE Senior faculty member nominated by the concerned Head of the Department	Member	1	Mrs. Minal Salunke Asst. Professor, Electrical & Electronics Engg. Dept.	

(Dr P. G. Tewari)

DEAN ACADEMIC AFFAIRS

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## Department of Electrical & Electronics Engineering

### Minutes of BOS Meeting held on 06-06-2020

1. Scheme & Syllabus of VII/VIII Semester's, KLE Tech. 2017-21, and batch was reviewed.

Following suggestions were made:

- Advanced Power Electronics - For dc-dc converter use PSPICE / LTSPICE. (Christopher Basso) (High power converters by Bin Wu) Add PWM rectifiers, Multi-pulse converters/active power-factor correction.
- Artificial Intelligence / Artificial Neural Network - Optimization algorithms/genetic algorithms to be included. AI/ML/DL comparison.

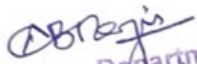
Following changes were made:

- Traction System for Electric Vehicles - Include an exclusive course on electric drives for EVs.
- Powertrain Control Lab - Include a laboratory course on integration of battery management systems and electric drives for EVs.
- Capstone Project Phase I - Include a capstone project phase 1 for 7th semester.

2. Scheme and syllabus of V/VI Semester's, KLE Tech. 2018-22, batch was reviewed.

Following suggestions are made:

- Machines Lab - Is it possible to have machines lab and theory in one semester? Rename speed control of induction motor experiment.
- OS & Embedded Systems - Threads and processes in process management (OS). Algorithms / projects with small C-programs on memory management (OS).
- Digital Signal Processing - Complicated to connect further with instrumentation, EMI and others / introduce some content in course (DSP).
- Machine Learning - Try to adopt some real time applications/seminars/connect with Electrical application in course (ML). Include matrices and operations. Ex: machine life prediction. Rename the Machine Learning course (Basics of ML).
- RTOS Lab - Modularize the programs in RTOS Lab.
- Power Electronic & Drives Lab - Include a course project with integration of modules as the course progress. PI controller design for Drives Lab. Sessions on Oscilloscopes in Drives lab.
- Battery Management System - Battery chemistry and broad spectrum of batteries to be discussed.

  
Head of the Department  
Electrical & Electronics Engineering  
KLE Technological University,  
HUBBALLI-31.

3. Scheme and syllabus of III/IV Semester's, KLE Tech. 2019-23, and batch was reviewed.

Following suggestions are made:

- Analog Electronic Circuits - Can we combine both digital and analog laboratories? Practical in 8051 need to be strengthened. Transistor fundamentals to be strengthened in Analog Electronics Laboratory. Improve connectivity between Analog and Digital Electronics subjects.
- Electrical Machines - Electrical machines syllabus seems to be very heavy. Tutorial has to be introduced in Electrical machines. Differential equations applied to electrical machines.
- Power Electronics - Tutorial for Power Electronics to impart practical links with other subjects.
- ARM Processor & Applications - OS and embedded systems instead of ARM in 4 sem.

4. Syllabus of I/II Semester's, of Basic Electrical Engineering, KLE Tech. 2020-24, batch was reviewed and approved.

  
Head of the Department  
Electrical & Electronics Engineering  
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