

Date: 15-03-2021

Change Summary Document – M.Tech (VDES)

Change summary between 2015-16 and 2016-17 admitted batches
(i.e. 2015 to 17 batch 2016 to 18 batch)

Semester	Course Name	Total Hours	Revision Hrs	% Change	Year of Implementation
I	Advanced processor Architectures	40	10	25	2016
II	Principles and Practices of Engineering Education	40	40	100	2016
III					
IV					
Total			50	(125/2) 62.5	

Evidence enclosed:

Schemes of 2015-19 batch and 2016-20 batch with changes shown in different colour

Signature of HoS/HoD



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KLE Technological University
HUBBALLI-580 031

Date: 15-03-2021


Change Summary Document – M.Tech (VDES)

Change summary between 2016-17 and 2017-18 admitted batches
(i.e. 2016 to 18 batch 2017 to 19 batch)

Semester	Course Name	Total Hours	Revision Hrs	% Change	Year of Implementation
I	Data Structures using C	24	24	100	2017
	Analog and Digital Circuits	48	48	100	2017
	Principles of Embedded Systems	48	48	100	2017
	CMOS VLSI Design	60	10	17	2017
	RISC Architectures	60	60	100	2017
	IC Fabrication Technology	40	10	25	2017
	Electronic System Design	72	72	100	2017
II	Automotive Electronics	60	60	100	2017
	Real Time Embedded Systems	60	60	100	2017
	Advanced Digital Logic Design	72	72	100	2017
	Testing and IC Characterization	60	60	100	2017
	Elective1				
	Image and Video Processing	50	50	100	2017
		50	50	100	2017
	Digital Control Systems				
	Standard Cell Design and Layout	50	50	100	2017
	Low Power VLSI Circuits	50	50	100	2017
Analog and Mixed Mode VLSI Circuits	50	50	100	2017	
III	Embedded Software Design	72	72	100	2018
	Advanced Digital Logic Verification	72	72	100	2018
	Elective2				
	Internet of Things	50	50	100	2018
	AUTOSAR	50	50	100	2018
	ASIC Design	50	50	100	2018
	MEMS	50	50	100	2018
IV	No Change				
Total			1118	(2042/22) 92.8	

Evidence enclosed:

Schemes of 2015-19 batch and 2016-20 batch with changes shown in different colour


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Change summary between 2017-18 and 2018-19 admitted batches
(i.e. 2017 to 19 batch 2018 to 20 batch)

Semester	Course Name	Total Hours	Revision Hrs	% Change	Year of Implementation
I	Machine Learning	40	40	100	2018
II					
III	Advanced Computer Architecture and Programming	50	50	100	2019
	Elective 2				
	System Simulation and Modelling	50	50	100	2019
	System on Chip	50	50	100	2019
IV	No Change				
Total			190	100	

Evidence enclosed:

Schemes of 2015-19 batch and 2016-20 batch with changes shown in different colour

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Change summary between 2018-19 and 2019-20 admitted batches
(i.e. 2018 to 20 batch 2019 to 21 batch)

Semester	Course Name	Total Hours	Revision Hrs	% Change	Year of Implementation
I					
II	Automotive Electronics and Communication	60	60	100	2019
	AUTOSAR and Infotainment	50	50	100	2019
III					
IV					
		Total	230	100	

Evidence enclosed:

Schemes of 2018-20 batch and 2019-21 batch with changes shown in different colour


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Change Summary Document – M.Tech (VDES)

Change summary between 2019-20 and 2020-21 admitted batches
(i.e. 2019 to 21 batch 2020 to 22 batch)

Semester	Course Name	Total Hours	Revision Hrs	% Change	Year of Implementation
I					
II					
III					
IV					
Total			NIL	NIL	

Evidence enclosed:


Signature of HoS/HoD


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PG-VDES

	Revision Hours	No. of Changed Subjects	Total No. Of Courses
2016-2018	50	2	33
2017-2019	1118	22	28
2018-2020	190	4	30
2019-2021	230	2	25
2020-2022	-	-	-
Total		30	116
%		$30/116 = 25.86\%$	


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