

School of Electronics and Communication Engineering

Date: 15-03-2021

Change Summary Document

Change summary between 2015-16 and 2016-17 admitted batches
(i.e. 2015 to 19 batch 2016 to 20 batch)

Semester	Course Name	Total Hours	Revision Hrs	% Change	Year of Implementation
I	Physics Lab 16EPHP101	20	4	20	2016
	Social Innovation 16EHSP101	64	60	94	2016
II	Engineering Exploration	78	56	72	2016
III	Digital Circuits	50	10	20	2017
	Engineering Design	72	72	100	2017
IV	Linear Integrated Circuit	50	10	20	2017
	Product Realization	48	48	100	2017
V	No Change				
VI	Embedded Intelligence Systems	72	72	100	2018
VII	Biosensors	40	20	50	2019
	Information Theory and Coding	40	12	24	2019
	CMOS ASIC Design	72	72	100	2019
	Physical Design-Analog	72	72	100	2019
VIII	Introduction to Deep Learning	40	40	100	2019
Total			448	900/13 =69.23	

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**Change summary between 2016-17 and 2018-19 admitted batches
(i.e. 2016 to 20 batch 2017 to 21 batch)**

Semester	Course Name	Total Hours	Revision Hrs	% Change	Year of Implementation
I					
II					
III	C Programming (Diploma)	48	48	100	2018
IV	Data Structure Applications Lab	48	48	100	2018
V	No Change				
VI	OOPS using C++	40	8	20	2019
VII	No Change				
VIII	No Change				
Total			104	220 / 3 =73	

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**Change summary between 2018-19 and 2019-20 admitted batches
(i.e. 2017 to 21 batch 2018 to 22 batch)**

Semester	Course Name	Total Hours	Revision Hrs	% Change	Year of Implementation
I	Single Variable Calculus 18EMAB101	60	40	67	2018
	Basic Electrical Engineering 18EEEF101	40	6	15	2018
II	Basic Electronics 18EECF102	50	5	10	2018
	Multivariable Calculus 18EMAB102	60	32	53	2018
	Problem Solving with Data Structures 18ECSP102	78	78	100	2018
III	Signals and Systems	40	24	60	2019
IV	No Change				
V	No Change				
VI	No Change				
VII	No Change				
VIII	No Change				
Total			180	305/ 6=51	

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Batch	Revised Hrs	No. of Courses
2015-19 to 2016-20	448	13
2016-20 to 2017-21	104	3
2017-21 to 2018-22	180	6
2018-22 to 2019-23	296	2
Total	1028	24

Total Courses: 63

$$24(16+8(\text{FY})) / 63 = 38.09\%$$

Evidence enclosed:

Schemes of All batch with changes shown in different colour


Signature of HoS/HoD


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