

School of Electronics & Communication Engineering

BOS Meeting Details for the Last Five Years



Minutes

1st Board of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

11th July 2015

KLE Technological University

(Established under Karnataka Act No.22, 2013)

KLE Technological University HUBBALLI-580 031

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The following are the minutes of the Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 11th July 2015 at 10:30 am at the Senate Hall of the University. **The following members were present.**

#	Name	Designation	Position	
1.	Dr. Uma Mudenagudi	Head of School, SoECE	Chairperson	
2.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member	
3.	Dr. Madhav Rao	Professor, Department of EC,IIIT Bangalore	Member	
4.	Dr. Lokesh Boregouda	Head Research, Samsung India, Bangalore	Member	
5.	Mr. Sanjeev Gondakar	Manager, RBEI, Bangalore	Member	
6.	Mr. Sumit Bhat Design Lead, Sankalp Semiconductors, Hubballi		Member	
7.	Dr. R. M. Banakar			
8.	Dr. Nalini Iyer			
9.	Dr. Priyatamkumar	Professor, SoECE	Member Member	
10.	Dr. Anil V. Nandi	Professor, SoECE	Member	
11.	Dr. R. B. Shettar	Professor, SoECE	Member	
12.	Dr. Saroja V S	Associate Professor, SoECE	Member	
13.	Mr. Sanjay Eligar	Assistant Professor, SoECE	Member	

CLNIA	Agenda	
SI No	Particulars	Page No.
1.1	To welcome the members of BoS	0
1.2	 To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same. a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams for (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch 	
1.3	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus I of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch	
1.4	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus I of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch To approve the Vision, Mission, POs and PSOs of School of ECE	
1.6	Any other subject with the permission of the Chair	<u> </u>

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BoS 1.1 To welcome the members of BoS

Resolution 1.1: The BoS members appreciated the work done towards recognition of KLE Technological University as a State private University effective from 2015.

BoS 1.2	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and
	approve the same.
	a) Scheme of I to VIII Semester (2015-19) batch
	 b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams for (2015-19) batch
	c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch
	Discussion: The verticals proposed for the undergraduate program B.E in Electronics & Communication were discussed. Based on the discussion, action points were listed. Members recommended industry specific elective courses to be offered, and also to explore the possibility of co-design and co-delivery with industry experts. <i>Action Item No.1: Strengthen the verticals by identifying appropriate core courses for B.E.(ECE)</i>
	Action Item No.2: Identify industry specific electives for the for B.E.(ECE)
51	Action Item No.3: Identify industry experts for co-design and co-delivery of the identified courses.

Resolution 1.2: Resolved to approve the following Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication Engineering subject to implementation of action points listed above:

- a) Scheme of I to VIII Semester (2015-19) batch.
- b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams for (2015-19) batch.
 c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch.

C/ 30	lente and synabilion withou degree in Electronics (2013-13) batch.		
BoS 1.3	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve		
	the same.		
	a) Scheme of I to IV Semester (2015-17) batch		
	b) Syllabus of I and II Semester (2015-17) batch		
	c) Scheme of III and IV Semester (2015-17) batch		
	a) Syllabus I of III and IV Semester (2015-17) batch		
	Discussion: The members proposed to identify industries in focus areas for the postgraduate program M.Tech in Digital Electronics. A suggestion to focus on internship & curriculum development in thrust areas. <i>Action Item No.4: Identify industries in focus areas.</i>		
	Action Item No.5: Identify industries for Internships.		

Resolution 1.3: Resolved to approve the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics:

- a) Scheme approval of I to IV Semester (2015-17)
- b) Syllabus approval of I and II Semester (2015-17)
- c) Scheme approval of III and IV Semester (2015-17)
- d) Syllabus approval of III and IV Semester (2015-17)

BoS 1.4	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems
	and approve the same.
	a) Scheme approval of I to IV Semester (2015-17)
	b) Syllabus approval of I and II Semester (2015-17)
	c) Scheme approval of III and IV Semester (2015-17)
	d) Syllabus approval of III and IV Semester (2015-17)
	Discussion: The members proposed to identify industries in focus areas for the postgraduate program M.Tech
	in VLSI Design & Embedded Systems. A suggestion to focus on internship & curriculum development in thrust
	areas.

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Action Item No.6: Identify industries in focus areas.

Action Item No.7: Identify industries for Internships.

Resolution 1.4: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems subject to implementation of action points listed above:

- a) Scheme of I to IV Semester (2015-17)
- b) Syllabus of I and II Semester (2015-17)
- c) Scheme of III and IV Semester (2015-17)
- d) Syllabus of III and IV Semester (2015-17)

 BoS 1.5
 To approve the Vision, Mission, POs and PSOs of School of ECE

 The Vision, Mission, POs and PSOs of School of ECE, which were aligned with the University's Vision and Mission, were presented.

Resolution 1.5: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE:

BoS 1.6	Any other subject with the permission of the Chair
	Nil.

The Chairperson thanked all the members for the fantastic contributions

Dr. Uma Mudenagudi Chairperson, BoS, SoECE

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Action Taken Report

1st Board of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

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(Established under Karnataka Act No.22, 2013)

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The following are the action items proposed during 1st Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 11th July 2015. The corresponding actions taken are also listed below.

	Description	Action Taken
	To welcome the members of BoS	Noted
BoS 1.1	Resolution 1.1: The BoS members appreciated the work done	
003 1.1	towards recognition of KLE Technological University as a State	
	private University effective from 2015.	
BoS 1.2	To consider the Schemes and Syllabi of the undergraduate	The BoS members noted the progress of
	program B.E in Electronics & Communication and approve the	the School and recommended action
	same.	items and timeline.
	a) Scheme of I to VIII Semester (2015-19) batch	
	b) Syllabus of I/II semester, Basic Electronics coursefor	Action Item No.1: Strengthen the
	Mechanical and Electrical streams for (2015-19) batch	verticals by identifying appropriate core
	c) Scheme and syllabi for Minor degree in Electronics (2015-	courses for B.E.(ECE)
	19) batch	
	Discussion : The verticals proposed for the undergraduate program	ATR: Identified the core courses for the 3
	B.E in Electronics & Communication were discussed. Based on the	verticals and will be presented during
	discussion, action points were listed. Members recommended	next BoS.
	industry specific elective courses to be offered, and also to explore	next 505.
	the possibility of co-design and co-delivery with industry experts.	Action Item No.2: Identify industry
	Action Item No.1: Strengthen the verticals by identifying	specific electives for B.E.(ECE)
	appropriate core courses for B.E.(ECE)	ATR: Identified the industry specific
	appropriate core courses for D.L.(ECE)	electives and and will be presented during
	Action Itom No. 2: Identify industry specific electives for B. E/E/E/	next BoS.
	Action Item No.2: Identify industry specific electives for B.E(ECE)	next Bos.
	Action Home No. 2. Identify industry superts for an desiry and a	Anting them. No 2. Identify industry
	Action Item No.3: Identify industry experts for co-design and co-	Action Item No.3: Identify industry
	delivery of the identified courses.	experts for co-design and co-delivery o
		the identified courses.
	Resolution 1.2: Resolved to approve the following Schemes and	ATR: Discussion with SMEs from RBEI
	Syllabi of B.E in Electronics & Communication Engineering	Samsung, Juniper, Cisco and othe
	subject to implementation of action points listed above:	industries for co-design and delivery o
	the second se	the identified courses
	a) Scheme of I to VIII Semester (2015-19) batch	
	b) Syllabus of I/II semester, Basic Electronics course for	
	Mechanical and Electrical streams (2015-19) batch	
	c) Scheme and syllabi for Minor degree in Electronics (2015-	
	19) batch.	
	To consider the Schemes and Syllabi of the postgraduate program	
BoS 1.3		The BoS members noted the progress o
BoS 1.3	M.Tech in Digital Electronics and approve the same.	
BoS 1.3		
BoS 1.3	M.Tech in Digital Electronics and approve the same.	the School and recommended action
BoS 1.3	 M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch 	the School and recommended action items and timeline.
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BoS 1.3	 M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch 	the School and recommended action items and timeline. Action Item No.4: Identify industries in focus areas.
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BoS 1.3	 M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch Discussion: The members proposed to identify industries in focus areas for the postgraduate program M.Tech in Digital Electronics. 	the School and recommended action items and timeline. Action Item No.4: Identify industries in focus areas.
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, a	 b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch e) Syllabus of III and IV Semester (2015-17) batch Discussion The members proposed to identify industries in focus areas for the postgraduate program M.Tech in VLSI Design & Embedded Systems. A suggestion to focus on internship & curriculum development in thrust areas. Action Item No.6: Identify industries for Internships. Resolution 1.4: Resolved to approve the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems subject to implementation of action points listed above: a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch 	Action Item No.6: Identify industries in focus areas. ATR: Discussion with SMEs from Sankalp Semiconductors and other VLSI companies. Action Item No.7: Identify industries for Internships. ATR: Identified about 10 industries, and discussions are at initial stage.
BoS 1.5	Approval of change of Vision, Mission, POs and PSOs of School of ECE Resolution 1.5: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE:	SoECE staff aligned to Vision, Mission, POs and PSOs
BoS 1.6	Any other subject with the permission of the Chair Nil.	

26 0 Dr. Uma Mudenagudi Chairperson, BoS, SoECE

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KLE Technological University HUBBALLI-580 031



Minutes

2nd Board of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

2nd April 2016

KLE Technological University

(Established under Karnataka Act No.22, 2013)

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The following are the minutes of the Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 2nd April 2016 at 09:00 am at the Senate Hall of the University. **The following members were present.**

SI No	Name Designation		Position	
1.	Dr. Uma Mudenagudi	Head of School, SoECE	Chairperson	
2.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member	
3.	Dr. Chetan Parikh	Professor, Department of EC,IIIT Bangalore	Member	
4.	Dr. Lokesh Boregouda	Head Research, Samsung India, Bangalore	Member	
5.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductors, Hubballi	Member	
6.	Dr. R. M. Banakar	Professor, SoECE	Member	
7.	Dr. Nalini Iyer	Professor, SoECE	Member	
8.	Dr. Priyatamkumar	Professor, SoECE	Member	
9.	Dr. R. B. Shettar	Professor, SoECE	Member	
10.	Mr. Sanjay Eligar	Assistant Professor, SoECE	Member	

The following members have sought leave of absence:

SI No	Name	Designation	Position
1	Mr. Sanjeev Gondakar	Manager, RBEI, Bangalore	Member
2.	Mr. Vivek G Pawar	Founder & CEO, Sankalp Semiconductors, Hubballi	Member

Agenda			
Particulars	Page No.		
To welcome the BoS Members and present department achievements & initiatives			
To read and confirm the minutes of 1 st BoS meeting held on 11 th July 2015			
To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics &			
a) Scheme approval of I to VIII Semester (2016-20) batch			
e) Scheme and Syllabi for Minor degree in Electronics (2015-19) batch			
To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and			
approve the same.			
c) Modification of Scheme of III/IV Semester (2015-17) batch			
d) Syllabus approval of III/IV Semester (2015-17) batch			
a) Scheme approval of I to IV Semester (2016-18) batch			
b) Syllabus approval of I/II Semester (2016-18) batch			
c) Modification of Scheme of III/IV Semester (2015-17)			
d) Syllabus approval of III/IV Semester (2015-17) batch			
Question Paper review			
Any other matter for discussion with the permission of the chair			
	Particulars To welcome the BoS Members and present department achievements & initiatives To read and confirm the minutes of 1st BoS meeting held on 11th July 2015 To consider the action taken report on the minutes of the previous meeting held on 11th July 2015 To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same. a) Scheme approval of I to VIII Semester (2016-20) batch b) Syllabus approval of I / II Semester, Basic Electronics course for Mechanical and Electrical streams (2016-20) batch c) Modification of Scheme of III to VIII Semester (2015-19) batch d) Syllabus approval of II / IV Semester (2015-19) batch e) Scheme and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme approval of I to IV Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme approval of I I/IV Semester (2015-17) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus approval of I I I V Semester (2016-18) batch b) Syllabus approval of I I I V Semester (2016-18) batch		

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To welcome the BoS Members and present department achievements & initiatives and discussed about the BoS 2.1 inputs from all stake holders. (Annexure 2.1)

Resolution 2.1: The BoS members appreciated the initiatives of SoECE and lauded its achievements.

BoS 2.2	The following are the minutes of the Board of Studies meeting of SoECE. KLE Technological University, Hubballi					
	which was held on 11 th July 2015 at 10:30 am at the Senate Hall of the University.					
	The fo	llowing members were pre	esent.			
	#	Name	Designation	Position		
	1.	Dr. Uma Mudenagudi	Head of School, SoECE	Chairperson		
	2.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member		
	3.	Dr. Madhav Rao	Professor, Department of EC,IIIT Bangalore	Member		
	4.	Dr. Lokesh Boregouda	Head Research, Samsung India, Bangalore	Member		
	5.	Mr. Sanjeev Gondakar	Manager, RBEI, Bangalore	Member		
	6.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductors, Hubballi	Member		
	7.	Dr. R. M. Banakar	Professor, SoECE	Member		
	8.	Dr. Nalini Iyer	Professor, SoECE	Member		
	9.	Dr. Priyatamkumar	Professor, SoECE	Member		
	10.	Dr. Anil V. Nandi	Professor, SoECE	Member		
	11.	Dr. R. B. Shettar	Professor, SoECE	Member		
	12.	Dr. Saroja V S	Associate Professor, SoECE	Member		
	13.	Mr. Sanjay Eligar	Assistant Professor, SoECE	Member		
BoS 1.1		come the members of BoS	are appreciated the work done towards many it's			
	Resolution 1.1: The BoS members appreciated the work done towards recognition of KLE Technologica University as a State private University effective from 2015.					
BoS 1.2	To cons	sider the Schemes and Sylla	bi of the undergraduate program B E in Electronice &			
	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same.					
	a)		ster (2015-19) batch			
	 a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus approval of I/II semester, Basic Electronics course for Mechanical and Electrical streams 					
	(2015-19) batch					
	c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch					
	Discussion : The verticals proposed for the undergraduate program B.E in Electronics & Communication were					
	discussed. Based on the discussion, action points were listed. Members recommended industry specific					
	elective courses to be offered, and also to explore the possibility of co-design and co-delivery with industry					
	experts.					
	Action Item No.1: Strengthen the verticals by identifying appropriate core courses for B.E.(ECE)					
	Action Item No.2: Identify industry specific electives for the for B.E.(ECE)					
	Action Item No.3: Identify industry experts for co-design and co-delivery of the identified courses.					
	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication Engineering subject to implementation of action points listed above:					
	a) Scheme of I to VIII Semester (2015-19) batch.					
	 b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams for (2015- 19) 					
	19) batch.					
	c)	Scheme and syllabi for N	linor degree in Electronics (2015-19) batch.			
30S 1.3	To consi	ider the Schemes and Syllab	oi of the postgraduate program M.Tech in Digital Electro	onics and approve the		
	same.					
	a)	Scheme of I to IV Semeste				
	b)	Syllabus of I and II Semes				
	c)	Scheme of III and IV Seme	ester (2015-17) batch			

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	d) Syllabus of III and IV Semester (2015-17) batch Discussion: The members proposed to identify industries in focus areas for the postgraduate program M.Tech in Digital Electronics. A suggestion to focus on internship & curriculum development in thrust areas. Action Item No.4: Identify industries in focus areas.
	Action Item No.5: Identify industries for Internships.
	Resolution 1.3: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics subject to implementation of action points listed above:
	 a) Scheme of I to IV Semester (2015-17) b) Syllabus of I and II Semester (2015-17) c) Scheme of III and IV Semester (2015-17) d) Syllabus of III and IV Semester (2015-17)
BoS 1.4	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems and approve the same. a) Scheme of I to IV Semester (2015-17) b) Syllabus of I and II Semester (2015-17) c) Scheme of III and IV Semester (2015-17)
	 d) Syllabus of III and IV Semester (2015-17) Discussion: The members proposed to identify industries in focus areas for the postgraduate program M.Tech in VLSI Design & Embedded Systems. A suggestion to focus on internship & curriculum development in thrust areas. Action Item No.6: Identify industries in focus areas.
	Action Item No.7: Identify industries for Internships.
	Resolution 1.4: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems subject to implementation of action points listed above:
	 a) Scheme of I to IV Semester (2015-17) b) Syllabus of I and II Semester (2015-17) c) Scheme of III and IV Semester (2015-17) d) Syllabus of III and IV Semester (2015-17)
BoS 1.5	Approval of change of Vision, Mission, POs and PSOs of School of ECE Resolution 1.5: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE as given in Annexure 1.5.
BoS 1.6	Any other subject with the permission of the Chair Nil.
Decolution	2.2. Desclored to confirm the minutes of its 15 DeC meeting held on 14th July 2015

Resolution 2.2: Resolved to confirm the minutes of its 1st BoS meeting held on 11th July 2015

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BoS 2.3 Item No	To confirm the action taken report on the minutes of the previou	
item NO	Description To welcome the members of BoS	Action Taken
,		Noted
BoS 1.1	Resolution 1.1: The BoS members appreciated the work done	
	towards recognition of KLE Technological University as a State	
BoS 1.2	private University effective from 2015.	
DU3 1.2	To consider the Schemes and Syllabi of the undergraduate	
	program B.E in Electronics & Communication and approve the same.	the School and recommended action
	a) Scheme of I to VIII Semester (2015-19) batch	items and timeline.
	b) Syllabus of I/II semester, Basic Electronics course for	
	Mechanical and Electrical streams for (2015-19)	
	batch	
	c) Scheme and syllabi for Minor degree in Electronics	for B.E.(ECE)
	(2015-19) batch	ATD Identified the series for the 2
	Discussion: The verticals proposed for the undergraduate	ATR: Identified the core courses for the 3 verticals and will be presented during next
	program B.E in Electronics & Communication were discussed.	Bos.
	Based on the discussion, action points were listed. Members	2003.
	recommended industry specific elective courses to be offered, and	Action Item No.2: Identify industry
	also to explore the possibility of co-design and co-delivery with	specific electives for B.E.(ECE)
	industry experts.	ATR: Identified the industry specific
		electives and and will be presented during
	Action Item No.1: Strengthen the verticals by identifying	next BoS.
	appropriate core courses for B.E.(ECE)	
		Action Item No.3: Identify industry
	Action Item No.2: Identify industry specific electives for B.E(ECE)	experts for co-design and co-delivery of
		the identified courses.
	Action Item No.3: Identify industry experts for co-design and co-	ATR: Discussion with SMEs from RBEI,
	delivery of the identified courses.	
	, , ,	Samsung, Juniper, Cisco and other
		industries for co-design and delivery of the
	Resolution 1.2: Resolved to approve the following Schemes and	
	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering	
	Resolution 1.2: Resolved to approve the following Schemes and	industries for co-design and delivery of the
	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above:	industries for co-design and delivery of the
	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch	industries for co-design and delivery of the
	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for	industries for co-design and delivery of the
	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch	industries for co-design and delivery of the
	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19)	industries for co-design and delivery of the
205 1 3	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch.	industries for co-design and delivery of the identified courses
305 1.3	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program	industries for co-design and delivery of the identified courses
3oS 1.3	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same.	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action
3oS 1.3	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch	industries for co-design and delivery of the identified courses
30S 1.3	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action items and timeline.
305 1.3	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.4: Identify industries in
3oS 1.3	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.4: Identify industries in focus areas.
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	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.4: Identify industries in focus areas.
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	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and Syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch biscussion: The members proposed to identify industries in focus areas for the postgraduate program M.Tech in Digital Electronics. A suggestion to focus on internship & curriculum development in thrust areas. Action Item No.4: Identify industries in focus areas. Action Item No.5: Identify industries for Internships. Resolution 1.3: Resolved to approve the Schemes and Syllabi of	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.4: Identify industries in focus areas. ATR: Discussion with SMEs from SiFive, RBEI and other industries. Action Item No.5: Identify industries for Internships.
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	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and Syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch biscussion: The members proposed to identify industries in focus areas for the postgraduate program M.Tech in Digital Electronics. A suggestion to focus on internship & curriculum development in thrust areas. Action Item No.4: Identify industries in focus areas. Action Item No.5: Identify industries for Internships. Resolution 1.3: Resolved to approve the Schemes and Syllabi of	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.4: Identify industries in focus areas. ATR: Discussion with SMEs from SiFive, RBEI and other industries. Action Item No.5: Identify industries for Internships. ATR: Identified about 10 industries, and
	Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) S	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.4: Identify industries in focus areas. ATR: Discussion with SMEs from SiFive, RBEI and other industries. Action Item No.5: Identify industries for Internships. ATR: Identified about 10 industries, and
	 Resolution 1.2: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2015-19) batch b) Syllabus of I/II semester, Basic Electronics course for Mechanical and Electrical streams (2015-19) batch c) Scheme and syllabi for Minor degree in Electronics (2015-19) batch. To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch 	industries for co-design and delivery of the identified courses The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.4: Identify industries in focus areas. ATR: Discussion with SMEs from SiFive, RBEI and other industries. Action Item No.5: Identify industries for Internships. ATR: Identified about 10 industries, and
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BoS 1.4	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems and approve the same. a) Scheme of I to IV Semester (2015-17) batch b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch e) Syllabus of III and IV Semester (2015-17) batch Discussion The members proposed to identify industries in focus areas for the postgraduate program M.Tech in VLSI Design & Embedded Systems. A suggestion to focus on internship & curriculum development in thrust areas. Action Item No.6: Identify industries in focus areas. Action Item No.7: Identify industries for Internships. Resolution 1.4: Resolved to approve the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems subject to implementation of action points listed above: a) Scheme of I to IV Semester (2015-17) batch	the School and recommended action items and timeline. Action Item No.6: Identify industries in focus areas. ATR: Discussion with SMEs from Sankalp Semiconductors and other VLSI companies. Action Item No.7: Identify industries for Internships. ATR: Identified about 10 industries, and discussions are at initial stage.
	 b) Syllabus of I and II Semester (2015-17) batch c) Scheme of III and IV Semester (2015-17) batch d) Syllabus of III and IV Semester (2015-17) batch 	
BoS 1.5	Approval of change of Vision, Mission, POs and PSOs of School of ECE Resolution 1.5: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE:	and PSOs
BoS 1.6	Any other subject with the permission of the Chair Nil.	

Resolution: 2.3 Resolved to confirm the action taken report on the minutes of its 1st BoS meeting held on 11th July 2015. The BoS members appreciated the new initiatives taken by SoECE

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BoS 2.4	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and
	approve the same.
	a) Scheme of I to VIII Semester (2016-20)
<i>.</i>	 a) Scheme of i to vin semester (2010 25) b) Syllabus of I / II Semester, Basic Electronics for Mechanical and Electrical stream course (2016-20)
	c) Modification of Scheme of III to VIII Semester (2015-19)
	d) Syllabus of III / IV Semester (2015-19)
	d) Synabus of M / W semiester (2019 15) Discussion: Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 02 nd April 2016. Persons responsible for these action items have already initiated the actions, which will be shared the next BoS meeting. The details of discussion are in <i>Annexure 2.4</i> .
	 Action Item No.1: Suggested for renaming of core courses for B.E.(ECE) 1) Electromagnetic Theory to Electromagnetic Fields and Waves 2) Embedded & IoT Lab to Embedded Lab (with focus on IoT)
	3) Sensor Networks & SDN to Sensor Networks
	Action Item No.2: Flipped mode of delivery for Circuit Analysis in III Sem
	Action Item No.2: Integrated approach: Introduction to hands-on 8051, ARM Microcontroller and programming
	(Embedded System Domain/Vertical courses in III Sem and IV Sem)
	Action Item No.4: Enhancing Programming Skill: using online Coding platform
	1) Data Structure using C
	Action Item No.5: Enhancing reasoning ability and experimental investigations: open ended Experiments
	1) Analog Electronics and Digital Circuits Lab.
	to the second seco

Resolution 2.4: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Comm implementation of action points listed above:

- a) Scheme of I to VIII Semester (2016-20) batch
- b) Syllabus of I / II Semester, Basic Electronics course for Mechanical and Electrical streams (2016-20) batch
- c) Modification of Scheme of III to VIII Semester (2015-19) batch
- d) Syllabus of III / IV Semester (2015-19) batch

To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the BoS 2.5 same. a) Scheme approval of I to IV Semester (2016-18) b) Syllabus approval of I/II Semester (2016-18) c) Modification of Scheme of III/IV Semester (2015-17) d) Syllabus approval of III/IV Semester (2015-17) Discussion: Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 02nd April 2016. Persons responsible for these action items have already initiated the actions, which will be shared the next BoS meeting. Action Item No.1: Suggested new core courses to strengthen design concepts 1) Principles and practices of Engineering Education 2) Fault Diagnosis and Testing for VLSI circuits 3) Real Time Embedded Systems Lab

Resolution 2.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics subject to implementation of action points listed above:

- a) Scheme I of I to IV Semester (2016-18) batch
- b) Syllabus of I/II Semester (2016-18) batch
- c) Modification of Scheme of III/IV Semester (2015-17) batch
- d) Syllabus of III/IV Semester (2015-17) batch

BoS 2.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems
	and approve the same.
	a) Scheme approval of I to IV Semester (2016-18)
	b) Syllabus approval of I/II Semester (2016-18)
	c) Modification of Scheme of III/IV Semester (2015-17)
	d) Syllabus approval of III/IV Semester (2015-17)

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Discussion: Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 02nd April 2016. Persons responsible for these action items have already initiated the actions, which will be shared the next BoS meeting.

Action Item No.1: Suggested new core course on pedagogy

 Principles and practices of Engineering Education

 Action Item No.2: Integrated approach: Emphasis on parallelism and protocols

1) Advanced Processor Architecture

Resolution 2.6: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems subject to implementation of action points listed above:

- a) Scheme of I to IV Semester (2016-18) batch
- b) Syllabus of I/II Semester (2016-18) batch
- c) Modification of Scheme of III/IV Semester (2015-17) batch
- d) Syllabus of III/IV Semester (2015-17)batch

BoS 2.7	Question Paper review
	Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI
	mapping was presented.

Resolution 2.7: Resolved to approve the Question Paper Pattern

BoS 2.8	Vision, Mission, POs and PSOs of School of ECE	
	Discussion: The Vision, Mission, POs and PSOs of School of ECE were presented.	

Resolution 2.8: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE

BoS 2.9	Any other matter for discussion with the permission of the chair
	Nil

The Chairperson thanked all the members for the fantastic contributions

Dr. Uma Mudenagudi Chairperson, BoS, SoECE

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Annexure 2.1 Discussion Item

Employers Feedback:

- Students lack ability to apply basic mathematical concepts and lack the rigor of practice.
- Enhance ability to identify and formulate problem in designing electronic system for real world applications.
- Enhance basic programming skills, to apply and realize real world problems.
- Students lack to engage in independent and lifelong learning.
- Students need to improve their ability to conduct investigations of technical issues with their level of knowledge and understanding.

Teachers Feedback (Pre-BoS MoM):

- Conceptual understanding of circuits and problem solving practice is essential.
- Focus on problem solving using programming skills and use of online platform.
- In circuit analysis course i.e. basic concepts of RLC, V-I and devices is required.

Students Feedback:

• Suggested for introduction of basic controller and its programming.

Alumni Feedback:

- Suggested for content delivery by industry experts.
- Recommended for inclusion of basic microcontroller under embedded system stream.
- Students lack to understand the basic of RISC architecture and its programming.
- Students ability to realize digital system design using concurrent programming is poor.

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Annexure 2.4

	Discussion Item	Course
	BE (ECE)	
1.	Flipped mode of teaching is introduced to provide rigor of problem solving practice and strengthening the basic circuit concepts for the course circuit analysis which engages students learning beyond class hours with access to content and interactive problem –solving interface	Circuit Analysis-15EECC201 Revised- Delivery and Assessment
2.	A course on 8051 microcontroller and programming is introduced to help the students to understand basic 8-bit controller architecture, its interfaces and programming for a given application.	Microcontroller architecture and programming-15EECF202 Added- New Course
3.	To enhance problem solving and programming skills online coding platform hacker rank is introduced in addition to regular assessment methods and IDE tools.	Data Structures using C-15EECF201 Revised- Delivery and Assessment
4.	Integrated theory and laboratory to facilitate more hands on with target platform which aids in timing and power analysis with area constraints for the given digital design	Digital system design using Verilog- 15EECC208 Revised- Delivery and Assessment
5.	Laboratory curriculum focusing on structured enquiry and open ended category of experiments to increase students reasoning ability and experimental investigations are enhanced	Analog Electronics and Digital electronics laboratory-15EECP202/15EECP201 Revised- Conduction and Assessment
	M.Tech Digital Electronics	
1.	To understand the basic principles of engineering education a new pedagogical course is introduced.	Principles and Practices of Engineering Education-15ECRC701
2.	A laboratory course on RTES is introduced to help the students to understand basic architecture and programming for a given application.	Real Time Embedded Systems Lab - 15EDEP706 Added- New Lab Course
3.	Integrated theory and laboratory to facilitate more hands on with target platform which aids fault diagnosis and characterization of digital circuits.	Fault Diagnosis and Testing for VLSI Circuits-15EDEC708 Revised- Delivery and Assessment
	M.Tech VLSI Design and Embedded Sy	stems
1.	To understand the basic principles of engineering education a new pedagogical course is introduced.	Principles and Practices of Engineering Education-15ECRC701

 The architecture of modern controller and bus protocols for embedded systems is introduced and application of the compiler techniques to exploit the instruction level parallelism is focused.
 Advanced Processor Architectures - 15EVEC704 Revised Course

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Action Taken Report

2nd Board of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

2nd April 2016

KLE Technological University

(Established under Karnataka Act No.22, 2013)

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The following are the action items proposed during 2nd Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 2nd April 2016. The corresponding actions taken are also listed below.

Item No	Description	Action Taken
	To welcome the BoS Members and present department	Noted
	achievements & initiatives and discussed about the inputs from	
D-C 2 1	all stake holders. (Annexure 2.1)	
BoS 2.1	Resolution 1.1: The BoS members appreciated the work done	
	towards recognition of KLE Technological University as a State	
	private University effective from 2015.	
	To read and confirm the minutes of 1 st BoS meeting held on 11 th	Noted
	July 2015	
BoS 2.2	Resolution 1.2: Minutes of the last meeting were read and	
	confirmed by BoS.	
	To confirm the action taken report on the minutes of the previous	Noted
	meeting held on 11 th July 2015	
BoS 2.3	Resolution 1.3: BoS confirmed the action taken report on the	
005 2.5	minutes of the previous meeting held on 11 th July 2015 and	
	suggestions were implemented.	
BoS 2.4		The BoS members noted the progress of
BOS 2.4	To consider the Schemes and Syllabi of the undergraduate	the School and recommended actio
	program B.E in Electronics & Communication and approve the	
	same.	items and timeline.
	a) Scheme of I to VIII Semester (2016-20) batch	A stimulture black Compared of Several S
	b) Syllabus of I / II Semester, Basic Electronics course for	Action Item No.1: Suggested for renamin
	Mechanical and Electrical streams (2016-20) batch	of core courses for B.E.(ECE)
	c) Modification of Scheme of III to VIII Semester (2015-19)	ATR: changes are made according to th
	batch	suggestions and will present during nex
	d) Syllabus of III / IV Semester (2015-19) batch	BoS.
	Discussion: Based on the discussions following action, items as	Action Item No.2: Flipped mode of deliver
	agreed upon by everyone were finalized and the same were	for Circuit Analysis in III Sem
	circulated to all the members on 02 nd April 2016. Persons	ATR: Flipped mode of teaching
	responsible for these action items have already initiated the	introduced to provide rigor of problem
	actions, which will be shared the next BoS meeting.	solving practice and strengthening th
	The details of discussion are in Annexure 2.4.	basic circuit concepts for the course Circu
	Action Item No.1: Suggested for renaming of core courses for	Analysis, which engages students learnin
	B.E.(ECE)	beyond class hours with access to conten
	1) Electromagnetic Theory to Electromagnetic Fields and	and interactive problem -solving interfac
	Waves	and will present during next BoS.
	2) Embedded & IoT Lab to Embedded Lab (with focus on	Action Item No.3: Integrated approach
	IOT)	Introduction to hands-on 8051, ARM
	3) Sensor Networks & SDN to Sensor Networks	
	Action Item No.2: Flipped mode of delivery for Circuit Analysis in	Microcontroller and programmin
		(Embedded System Domain/Vertice
	III Sem	courses in III Sem and IV Sem)
	Action Item No.3: Integrated approach: Introduction to hands-	ATR: Integrated theory and laborator
	on 8051, ARM Microcontroller and programming (Embedded	approach is adopted to bridge the ga
	System Domain/Vertical courses in III Sem and IV Sem)	
	Action Item No.4: Enhancing Programming Skill: using online	between understanding theoretics
		concepts and realiasing the same with
	Coding platform	programming in courses 8051 and ARI
		Controller
	1) Data Structure using C	Action Item No.4: Enhancin
	Action Item No.5: Enhancing reasoning ability and experimental	Programming Skill : using online Codin
	<i>investigations</i> : open ended Experiments 1) Analog	platform
	Electronics and Digital Circuits Lab.	ATR:
	Resolution 2.4: Resolved to approve the following Schemes and	To enhance problem solving an
	Syllabi of B.E in Electronics & Communication Engineering	programming skills online coding platform
	subject to implementation of action points listed above:	hacker rank is introduced in addition t
	a) Scheme of I to VIII Semester (2016-20) batch	and an and the second
	b) Syllabus of I / II Semester, Basic Electronics course for	regular assessment methods and IDE too
	Mechanical and Electrical streams (2016-20) batch	in the course Data Structures using C

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	 c) Modification of Scheme of III to VIII Semester (2015-19) batch d) Syllabus of III / IV Semester (2015-19) batch 	Action Item No.5: Enhancing reasoning ability and experimental investigations : open ended Experiments ATR : Laboratory curriculum focusing on structured enquiry and open ended category of experiments to increase students reasoning ability and experimental investigations are enhanced and introduced in AEC and DC labs Changes are made according to the suggestions and will be presented during the next BoS.
BoS 2.5	 To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion : Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 02 nd April 2016. Persons responsible for these action items have already initiated the actions, which will be shared the next BoS meeting. Action Item No.1: Suggested new core courses to strengthen design concepts 1) Principles and practices of Engineering Education 2) Fault Diagnosis and Testing for VLSI circuits 3) Real Time Embedded Systems Lab 	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested new core courses to strengthen design concepts ATR: Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and realiasing the same with programming in course Real Time Embedded Systems Lab and fault diagnosis and characterization of digital circuits in Fault Diagnosis and Testing for VLSI circuits A course on Principles and practices of
	 Resolution 2.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics subject to implementation of action points listed above: a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch 	Engineering Education is introduced to understand the basic principles of engineering education and pedagogy. Changes are made according to the suggestions and will be presented during the next BoS.
BoS 2.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 02 nd April 2016. Persons responsible for these action items have already initiated the actions, which will be shared the next BoS meeting.	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested new core course on pedagogy ATR: A course on Principles and practices of Engineering Education is introduced to understand the basic principles of engineering education and pedagogy Action Item No.2: Integrated approach: Emphasis on parallelism and protocols



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	Action Item No.1: Suggested new core course on pedagogy 1) Principles and practices of Engineering Education Action Item No.2: Integrated approach: Emphasis on parallelism and protocols 1) Advanced Processor Architecture Resolution 2.6: Resolved to approve the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems subject to implementation of action points listed above: a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch	ATR: Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and realiasing the same with programming in course Advanced Processor Architecture emphasis on parallelism and protocols. Changes are made according to the suggestions and will be presented during
	 c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch 	the next BoS.
BoS 2.7	Question Paper review Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI mapping was presented. Resolved to approve the Question Paper Pattern	QP Pattern is incorporated in all the courses.
BoS 2.8	Vision, Mission, POs and PSOs of School of ECE Discussion: The Vision, Mission, POs and PSOs of School of ECE were presented. Resolution 2.8: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE	and PSOs
BoS 2.9	Any other subject with the permission of the Chair Nil.	

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Dr. Uma Mudenagudi Chairperson, BoS, SoECE

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KLE Technological Univers



Minutes

3ndBoard of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

15th April 2017

KLE Technological University

(Established under Karnataka Act No.22, 2013)

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The following are the minutes of theBoard of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 15th April 2017 at 09:00 am at the Senate Hall of the University. **The following members were present.**

SI No	Name	Designation	Position
1.	Dr. NaliniC.lyer	Head of School, SoECE	Chairperson
2.	Dr. R M Bankar	Professor, SoECE	Member
3.	Ujwala Patil	Associate Professor, SoECE	Member
4.	Sanjay Eligar	Head Research, Samsung India, Bangalore	Member
5.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member
6.	Dr. Chetan Parekh	Professor, Department of EC,IIIT Bangalore	Member
7.	Dr. LokeshBoregouda	Professor, SoECE	Member
8.	Mr. Vivek G Pawar	Founder & CEO, Sankalp Semiconductor	Member
9.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductor	Member
10.	Mrs. PadminiNavalgund	RBEI, Bangalore	Member
11.	Prabha. C. Nissimgoudar	Associate Professor, SoECE	Member
12.	Dr. Uma Mudengudi	Professor, SoECE	Member
13.	Dr. Pritamkumar	Professor, SoECE	Member
14.	Dr. R B Shettar	Professor, SoECE	Member
15.	Rohini Hongal	Associate Professor, SoECE	Member
16.	Dr. G.B. Marali	Professor and Head of Mathematics	Member
17.	BE UG 1. AKSHAY M PANCHAMUKHI 2. MAITHILI NAIK PG1 : DE MADHURI SUMALATHA MARRI PG2: PG VDES DEEPIKA Wali k PhD: HeeraWali		Student Member's

The following members have sought leave of absence:

SI No	Name	Designation	Position
1.	Dr. R M Bankar	Professor, SoECE	Member

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	Agenda	Dege No.	
SI No	Particulars	Page No.	
3.1	To welcome the BoS Members and present department achievements & initiatives		
3.2	To read and confirm the minutes of 2 nd BoS meeting held on 2 nd April 2016		
3.3	To confirm the action taken report on the minutes of the previous meeting held on 2 nd April 2016		
3.4	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics &		
	Communication and approve the same.		
	a) Scheme approval of I to VIII Semester (2017-21) batch		
	b) Syllabus approval of I / II Semester, Basic Electronics course for Mechanical and Electrical		
	streams (2017-21) batch		
	c) Scheme approval of III to VIII Semester (2016-20) batch		
	d) Syllabus approval of III to VIII Semester (2016-20) batch		
	e) Modification of Scheme of V to VIII Semester (2015-19) batch		
	f) Syllabus approval of V and VI Semester (2015-19) batch		
	g) Scheme approval: Scheme 2016-20 batch of Minor Program		
	h) Syllabus approval: Scheme 2015-19 batch of Minor Program		
3.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and	1	
	approve the same.		
	a) Scheme approval of I to IV Semester (2017-19) batch		
	b) Syllabus approval of I/II Semester (2017-19) batch		
	c) Modification of Scheme of III/IV Semester (2016-18) batch		
	d) Syllabus approval of III/IV Semester (2016-18) batch		
3.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design &		
	Embedded Systems and approve the same.		
	a) Scheme approval of I to IV Semester (2017-19) batch		
	b) Syllabus approval of I/II Semester (2017-19) batch		
	c) Modification of Scheme of III/IV Semester (2016-18) batch		
	d) Syllabus approval of III/IV Semester (2016-18) batch		
3.7	Question Paper review		
3.8	Vision, Mission, POs and PSOs of School of ECE		
3.9	Any other matter for discussion with the permission of the chair		

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BoS 3.1	To welcome the BoS Members and present department achievements & initiativesand discussed about the
D03 3.1	inputs from all stake holders. (Annexure 3.1)

Resolution 3.1: The BoS members appreciated the initiatives of SoECE and lauded its achievements.

BoS 3.2	To read and confirm the minutes of 2 st BoS meeting held on 2 nd April 2016					
	The following are the action items proposed during 2 nd Board of Studies meeting of SoECE, KLE Technologic					
	University, Hubballi which was held on 2 nd April 2016. The corresponding actions taken are also listed below.					
	The following members were present.					
	Bosa	2.2 To read	and confirm the minutes of 2 nd BoS meeting held on 02 nd	April 2016		
	SI No	Name	Designation	Position		
	1.	Dr. Uma Mudenagudi	Head of School, SoECE	Chairperson		
	2.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member		
	3.	Dr. Chetan Parikh	Professor, Department of EC,IIIT Bangalore	Member		
	4.	Dr. Lokesh Boregouda		Member		
	5.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductors, Hubballi	Member		
	6.	Dr. R. M. Banakar	Professor, SoECE	Member		
	7.	Dr. Nalini Iyer	Professor, SoECE	Member		
	8.	Dr. Priyatamkumar	Professor, SoECE	Member		
	9.	Dr. R. B. Shettar	Professor, SoECE	Member		
	10.	Mr. Sanjay Eligar	Assistant Professor, SoECE	Member		
	. It a set	Nie				
	Item		Description oS Members and present department achievements & init			
	BoS 2	Resolution 2.1: Technological Uni	from all stake holders. <u>(Annexure 2.1)</u> The BoS members appreciated the work done towards versity as a State private University effective from 2015.	-		
	BoS 2	Resolution 2.2: N	To read and confirm the minutes of 2 nd BoS meeting held on 2 nd April 2016 Resolution 2.2: Minutes of the last meeting were read and confirmed by BoS.			
	BoS 2	To confirm the action taken report on the minutes of the previous meeting held on 2 nd April 2016BoS 2.3Resolution 2.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 2 nd April 2016 and suggestions were implemented.				
	BoS 2	Communication a a) Scheme or b) Syllabus o (2016-20) c) Modificati d) Syllabus o Discussion: Based	Schemes and Syllabi of the undergraduate program E nd approve the same. f I to VIII Semester (2016-20) batch f I / II Semester, Basic Electronics course for Mechanical a batch on of Scheme of III to VIII Semester (2015-19) batch f III / IV Semester (2015-19) batch l on the discussions following action, items as agreed upc ame were circulated to all the members on 02 nd April 2016.	nd Electrical stream		
		meeting. The details of disc Action Item No.1: 1) Electrom 2) Embedde 3) Sensor N Action Item No.2: Action Item No.3:	items have already initiated the actions, which will be s sussion are in <i>Annexure 2.4.</i> <i>Suggested for renaming of core courses for B.E.(ECE)</i> agnetic Theory to Electromagnetic Fields and Waves ed & IoT Lab to Embedded Lab (with focus on IoT) etworks & SDN to Sensor Networks <i>Flipped mode of delivery for Circuit Analysis in III Sem</i> <i>Integrated approach</i> : Introduction to hands-on 8051, ARM	1 Microcontroller and		
		programming (Em	bedded System Domain/Vertical courses in III Sem and IV.	Sem)		

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	1) Data Structure using C
	Action Item No.5: Enhancing reasoning ability and experimental investigations: open ended
	Experiments
	1) Applog Electronics and Digital Circuits Lab.
esolution	2.4. Resolved to approve the Schemes and Syllabi of the undergraduate program B.E in Electron
Commun	nication subject to implementation of action points listed above:
-) Cal	amo approval of Lto VIII Semester (2016-20)
	labus approval of I / II Semester, Basic Electronics for Mechanical and Electrical stream cour
(20	016-20) odification of Scheme of III to VIII Semester (2015-19)
c) MC	labus approval of III / IV Semester (2015-19)
u) 591	
BoS 2.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronic
	and approve the same.
	a) Scheme approval of I to IV Semester (2016-18)
	b) Syllabus approval of I/II Semester (2016-18)
	 c) Modification of Scheme of III/IV Semester (2015-17) d) Syllabus approval of III/IV Semester (2015-17)
	P: unique Based on the discussions following action, items as agreed upon by everyone we
	finalized and the same were circulated to all the members on 02 th April 2016. Persons responsible
	for these action items have already initiated the actions, which will be shared the next Be
	meeting
	Action Item No.1: Suggested new core courses to strengthen design concepts
	1) Principles and practices of Engineering Education
	2) Fault Diagnosis and Testing for VLSI circuits
	3) Real Time Embedded Systems Lab
	A second s
in Digital a) b) c)	n 2.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.T Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch
in Digital a) b) c)	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design
in Digital a) b) c) d)	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same.
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone w
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone w finalized and the same were circulated to all the members on 02 nd April 2016. Persons respons for these action items have already initiated the actions, which will be shared the next BoS meet
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone w finalized and the same were circulated to all the members on 02 nd April 2016. Persons response for these action items have already initiated the actions, which will be shared the next BoS meet Action Item No.1: Suggested new core course on pedagogy
in Digital a) b) c) d) BoS	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone w finalized and the same were circulated to all the members on 02 nd April 2016. Persons response for these action items have already initiated the actions, which will be shared the next BoS meet Action Item No.1: Suggested new core course on pedagogy 1) Principles and practices of Engineering Education
in Digital a) b) c) d) BoS	 Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone w finalized and the same were circulated to all the members on 02nd April 2016. Persons responsi for these action items have already initiated the actions, which will be shared the next BoS meet Action Item No.1: Suggested new core course on pedagogy Principles and practices of Engineering Education Action Item No.2: Integrated approach: Emphasis on parallelism and protocols
in Digital a) b) c) d) BoS 2.6	 Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2016-18) batch d) Syllabus of I/II Semester (2016-18) batch d) Syllabus of III/IV Semester (2015-17) batch d) Syllabus of I
in Digital a) b) c) d) BoS 2.6	Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone w finalized and the same were circulated to all the members on 02 nd April 2016. Persons responsi for these action items have already initiated the actions, which will be shared the next BoS meeti Action Item No.1: Suggested new core course on pedagogy 1) Principles and practices of Engineering Education Action Item No.2: Integrated approach: Emphasis on parallelism and protocols 1) Advanced Processor Architecture
in Digital a) b) c) d) BoS 2.6	 Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch Modification of Scheme of III/IV Semester (2015-17) batch Syllabus of III/IV Semester (2015-17) batch To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone w finalized and the same were circulated to all the members on 02nd April 2016. Persons responsi for these action items have already initiated the actions, which will be shared the next BoS meet Action Item No.1: Suggested new core course on pedagogy Principles and practices of Engineering Education Action Item No.2: Integrated approach: Emphasis on parallelism and protocols

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	b) Sy c) M d) Sy BoS 2.7 Resolution BoS 2.8	heme of I to IV Semester (2016-18) batch Ilabus of I/II Semester (2016-18) batch odification of Scheme of III/IV Semester (2015-17) batch Ilabus of III/IV Semester (2015-17) batch Question Paper review Discussion: The Question Paper along with assessment patte PO-PSO-PI mapping was presented. 2.7: Resolved to approve the Question Paper Pattern Vision, Mission, POs and PSOs of School of ECE Discussion: The Vision, Mission, POs and PSOs of School of E	CEwere presented.
	BoS 2.9	 2.8: Resolved to approve the Vision, Mission, POs and PSOs Any other matter for discussion with the permission of the one Nil 3.2: Resolved to confirm the minutes of its 2nd BoS meeting 	hair .
BoS 3.3		the action taken report on the minutes of the previous me	
	BoS 3.3	To confirm the action taken report on the minutes of th 2016	
	ltem No	Description	Action Taken
	BoS 2.1	To welcome the BoS Members and present department achievements & initiatives and discussed about the inputs from all stake holders. (Annexure 2.1) Resolution 2.1: The BoS members appreciated the work done towards recognition of KLE Technological University as a State private University effective from 2015.	Noted
	BoS 2.2	To read and confirm the minutes of 2 nd BoS meeting held on 2 nd April 2016 Resolution 2.2: Minutes of the last meeting were read and confirmed by BoS.	Noted
	BoS 2.3	To confirm the action taken report on the minutes of the previous meeting held on 2 nd April 2016 Resolution 2.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 2 nd April 2016 and suggestions were implemented.	Noted
	BoS 2.4	 To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same. a) Scheme of I to VIII Semester (2016-20) batch b) Syllabus of I / II Semester, Basic Electronics course for Mechanical and Electrical streams (2016-20) batch c) Modification of Scheme of III to VIII Semester (2015-19) batch d) Syllabus of II / IV Semester (2015-19) batch Discussion: Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 02nd April 	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested for renaming of core courses for B.E.(ECE) ATR: changes are made according to the suggestions and will present during next BoS. Action Item No.2: Flipped mode of delivery for Circuit Analysis in III Sem

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	 2016. Persons responsible for these action items have already initiated the actions, which will be shared the next BoS meeting. The details of discussion are in Annexure 2.4. Action Item No.1: Suggested for renaming of core courses for B.E.(ECE) Electromagnetic Theory to Electromagnetic Fields and Waves Embedded & Io Lab to Embedded Lab (with focus on IoT) Sensor Networks & SDN to Sensor Networks Action Item No.2: Flipped mode of delivery for Circuit Analysis in III Sem Action Item No.3: Integrated approach: Introduction to hands-on 8051, ARM Microcontroller and programming (Embedded System Domain/Vertical courses in III Sem and IV Sem) Action Item No.4: Enhancing Programming Skill: using online Coding platform Data Structure using C Action Item No.5: Enhancing reasoning ability and experimental investigations: open ended Experiments Analog Electronics and Digital Circuits Lab. Resolution 2.4: Resolved to approve the following Schemes and Syllabi of B.E in Electronics & Communication Engineering subject to implementation of action points listed above: Scheme of I to VIII Semester (2016-20) batch Syllabus of II / II Semester (2015-19) batch 	ATR: Flipped mode of teaching is introduced to provide rigor of problem solving practice and strengthening the basic circuit concepts for the course Circuit Analysis, which engages students learning beyond class hours with access to content, and interactive problem –solving interface and will present during next BOS. Action Item No.3: Integrated approach : Introduction to hands-on 8051, ARM Microcontroller and programming (Embedded System Domain/Vertical courses in III Sem and IV Sem) ATR: Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and realiasing the same with programming in courses 8051 and ARM Controller Action Item No.4: Enhancing Programming Skill : using online Coding platform ATR: To enhance problem solving and programming skills online coding platform hacker rank is introduced in addition to regular assessment methods and IDE tools in the course Data Structures using C Action Item No.5: Enhancing reasoning ability and experimental investigations: open ended Experiments ATR : Laboratory curriculum focusing on structured enquiry and open ended category of experiments to increase students reasoning ability and experimental investigations are enhanced and introduced in AEC and DC labs Changes are made according to the sungestions and will be presented
0-625	To consider the Schemes and Syllabi of the postgraduate	suggestions and will be presented during the next BoS.
BoS 2.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2016-18) batch	The bos memorie

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	 b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch Discussion: Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 02nd April 2016. Persons responsible for these action items have already initiated the actions, which will be shared the next BoS meeting. Action Item No.1: Suggested new core courses to strengthen design concepts Principles and practices of Engineering Education Fault Diagnosis and Testing for VLSI circuits Real Time Embedded Systems Lab Resolution 2.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics subject to implementation of action points listed above: Scheme of I to IV Semester (2016-18) batch Syllabus of I/II Semester (2016-18) batch 	Action Item No.1: Suggested new core courses to strengthen design concepts ATR: Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and realiasing the same with programming in course Real Time Embedded Systems Lab and fault diagnosis and characterization of digital circuits in Fault Diagnosis and Testing for VLSI circuits A course on Principles and practices of Engineering Education is introduced to understand the basic principles of engineering education and pedagogy.
	 c) Modification of Scheme of III/IV Semester (2015-17) batch d) Syllabus of III/IV Semester (2015-17) batch 	Changes are made according to the suggestions and will be presented during the next BoS.
BoS 2.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems and approve the same. a) Scheme of I to IV Semester (2016-18) batch b) Syllabus of I/II Semester (2016-18) batch c) Modification of Scheme of III/IV Semester (2015-17) batch	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested new core course on pedagogy
		The second s
	d) Syllabus of III/IV Semester (2015-17) batch	ATR: A course on Principles and
	Discussion : Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 02^{nd} April 2016. Persons responsible for these action items have already initiated the actions, which will be shared the	practices of Engineering Education is introduced to understand the basic principles of engineering education and pedagogy Action Item No.2: Integrated
	next BoS meeting.	
	Action Item No.1: Suggested new core course on	approach: Emphasis on parallelism and protocols
	1) Principles and practices of Engineering	ATR: Integrated theory and
	Education	laboratory approach is adopted to
	Action Item No.2: Integrated approach: Emphasis on	bridge the gap between
	parallelism and protocols	understanding theoretical concepts
	1) Advanced Processor Architecture	and realiasing the same with
	Resolution 2.6: Resolved to approve the Schemes and	programming in course Advanced
	Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems subject to implementation	Processor Architecture emphasis on parallelism and protocols.
	of action points listed above:	parateriori ana protocolo.
	a) Scheme of I to IV Semester (2016-18) batch	Changes are made according to the
	b) Syllabus of I/II Semester (2016-18) batch	suggestions and will be presented during the next BoS.

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		c) Modification of Scheme of III/IV Semester			
		(2015-17) batch	A		
		d) Syllabus of III/IV Semester (2015-17) batch			
	BoS 2.7	Question Paper review			
		Discussion: The Question Paper along with assessment	QP Pattern is incorporated in all the		
		patterns with respect to Bloom's Levels and PO-PSO-PI	courses.		
		mapping was presented.			
		Resolved to approve the Question Paper Pattern			
	BoS 2.8	Vision, Mission, POs and PSOs of School of ECE			
		Discussion: The Vision, Mission, POs and PSOs of School	SoECE staff aligned to Vision, Mission,		
		of ECE were presented.	POs and PSOs		
		Resolution 2.8: Resolved to approve the Vision, Mission,			
		POs and PSOs of School of ECE			
	BoS 2.9	Any other subject with the permission of the Chair			
	000 213	Nil.			
		Resolution 2.10: Resolved to approve the no other			
		subject has been discussed			
	Resolutio	n: 3.3 Resolved to confirm the action taken report on the	minutos of its 2 nd PoS mosting hold on		
	2 nd April	2016. The BoS members appreciated the new initiatives tal	timutes of its 2 Bos meeting held on		
	2 April 2	to to. The bos members appreciated the new initiatives ta	ken by soece.		
30S 3.4	To conside	er the Schemes and Syllabi of the undergraduate program E	F in Electronics & Communication an		
			S.E In Electronics & communication ar		
	approve the same. a) Scheme approval of I to VIII Semester (2017-21) batch				
			Machanical and Electrical streams source		
	b) Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrical stream course				
	(2017-21) batch c) Scheme approval of III to VIII Semester (2016-20) batch				
		Scheme approval of III to VIII Semester (2016-20) batch			
	d) Syllabus approval of III to VIII Semester (2016-20) batch				
	e) Modification of Scheme of V to VIII Semester (2015-19) batch				
	f) Syllabus approval of V and VI Semester (2015-19) batch				
	Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the				
	same were circulated to all the members on 15 th April 2017. Persons responsible for these action items have				
	already initiated the actions, which will be shared in the next BoS meeting.				
	The details of discussion are in Annexure 3.4.				
		n No.1: Action Item No.1: Suggested new core courses focus			
	1) M	n No.1: Action Item No.1: Suggested new core courses focu. Iachine learning			
	1) M 2) M	n No.1: Action Item No.1: Suggested new core courses focu. Iachine learning Iini Project			
	1) M 2) M 3) M	n No.1: Action Item No.1: Suggested new core courses focu. lachine learning lini Project linor project			
	1) M 2) M 3) M 4) Er	n No.1: Action Item No.1: Suggested new core courses focu. lachine learning lini Project linor project ngineering Design			
	1) M 2) M 3) M 4) Er 5) Pr	n No.1: Action Item No.1: Suggested new core courses focu. lachine learning lini Project linor project ngineering Design roduct Realization	sing on project based learning		
	1) M 2) M 3) M 4) Er 5) Pr	n No.1: Action Item No.1: Suggested new core courses focu. lachine learning lini Project linor project ngineering Design	sing on project based learning		
	1) M 2) M 3) M 4) Er 5) Pr <i>Action Iten</i>	n No.1: Action Item No.1: Suggested new core courses focu. lachine learning lini Project linor project ngineering Design roduct Realization	sing on project based learning		
	1) M 2) M 3) M 4) Er 5) Pr <i>Action Iten</i> 1) Au	n No.1: Action Item No.1: Suggested new core courses focus lachine learning lini Project linor project ngineering Design roduct Realization n No.2: Model based design approach for rapid prototyping	sing on project based learning		
	 1) M 2) M 3) M 4) Er 5) Pr Action Iten 1) Au 	n No.1: Action Item No.1: Suggested new core courses focu. lachine learning lini Project linor project ngineering Design roduct Realization In No.2: Model based design approach for rapid prototyping utomotive electronics laboratory In No.3: Integrated Approach: focus on hands on and code of	sing on project based learning		
	 1) M 2) M 3) M 4) Er 5) Pr Action Iten 1) Au Action Iten 1) Op 	n No.1: Action Item No.1: Suggested new core courses focu. lachine learning lini Project linor project ngineering Design roduct Realization n No.2: Model based design approach for rapid prototyping utomotive electronics laboratory	sing on project based learning n ptimization		
	1) M 2) M 3) M 4) Er 5) Pr Action Iten 1) Au Action Iten 1) Op Action Iten 1) Co	m No.1: Action Item No.1: Suggested new core courses focus lachine learning lini Project linor project oduct Realization m No.2: Model based design approach for rapid prototyping utomotive electronics laboratory m No.3: Integrated Approach: focus on hands on and code of perating system and Embedded system m No.4: Enhancing reasoning ability and investigation and i pommunication and Signal processing lab	sing on project based learning n ptimization		
	1) M 2) M 3) M 4) Er 5) Pr Action Iten 1) Au Action Iten 1) Op Action Iten 1) Cc 2) Cc	m No.1: Action Item No.1: Suggested new core courses focus lachine learning lini Project linor project oduct Realization m No.2: Model based design approach for rapid prototyping utomotive electronics laboratory m No.3: Integrated Approach: focus on hands on and code of perating system and Embedded system m No.4: Enhancing reasoning ability and investigation and i pommunication and Signal processing lab	sing on project based learning ptimization nterpretation : Open ended experiment		
	1) M 2) M 3) M 4) Er 5) Pr Action Iten 1) Au Action Iten 1) Op Action Iten 1) Co 2) Co Action Iten	m No.1: Action Item No.1: Suggested new core courses focus lachine learning lini Project linor project orduct Realization m No.2: Model based design approach for rapid prototyping utomotive electronics laboratory m No.3: Integrated Approach: focus on hands on and code of perating system and Embedded system m No.4: Enhancing reasoning ability and investigation and i pommunication and Signal processing lab omputer communication network lab m No.5: Flipped mode of delivery to strengthened system des	sing on project based learning ptimization nterpretation : Open ended experiment		
	1) M 2) M 3) M 4) Er 5) Pr Action Iten 1) Au Action Iten 1) Op Action Iten 1) Co 2) Co Action Iten	m No.1: Action Item No.1: Suggested new core courses focus lachine learning lini Project linor project oduct Realization m No.2: Model based design approach for rapid prototyping utomotive electronics laboratory m No.3: Integrated Approach: focus on hands on and code of perating system and Embedded system m No.4: Enhancing reasoning ability and investigation and i pommunication and Signal processing lab	sing on project based learning ptimization nterpretation : Open ended experiment		



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	1) Advanced Digital Logic				
	2) Advanced Digital Logic Verification				
	3) Analog Circuit Design				
	4) Internet of Things				
Re	olution 3.4: Resolved to approve the following Schemes and Syllabi of the B.E in Electronics munication subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2017-21) batch.				
	The second				
	 b) Syllabus of I / II Semester, Basic Electronics course for Wechanical and Electrical Streams (2011 – batch 				
	c) Scheme of III to VIII Semester (2016-20) batch.				
	 d) Syllabus of III to VIII Semester (2016-20) batch. 				
	e) Modification of Scheme of V to VIII Semester (2015-19) batch				
	f) Syllabus of V and VI Semester (2015-19) batch.				
	g) Scheme of Minor program of 2016-20 batch.				
	h) Scheme of Minor program of 2015-19 batch.				
oS 3.5 T	consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve t				
	ne.				
	a) Scheme approval of I to IV Semester (2017-19)				
	b) Syllabus approval of I/II Semester (2017-19)				
	c) Modification of Scheme of III/IV Semester (2016-18)				
	d) Syllabus approval of III/IV Semester (2016-18)				
D	cussion: Based on the discussions following action items as agreed upon by everyone were finalized and t				
S	same were circulated to all the members on 15 th April 2017. Persons responsible for these action items have				
a	already initiated the actions, which will be shared in the next BoS meeting. Action Item No.1: Suggested new core courses to strengthen basic concepts and programming				
A	ton Item No.1: Suggested new core courses to strengthen busic concepts and programming				
	1) Data Structures using C 2) Analog and Digital Circuits				
	3) Principles of Embedded systems				
	4) Fundamentals of signal processing				
	5) RISC Architectures				
	6) Machine learning				
	7) Electronics System Design				
	8) Embedded software design				
	9) Automotive communication				
	10) Automotive Electronics				
	11) Multimedia Signal processing				
,	tion Item No.2: Suggested new electives courses to build background with application perspective				
	1) Data Communication				
	2) Image and video processing				
	3) Digital Control System				
	4) Multi sensor data fusion				
	5) Internet of Things (IOT)				
	6) AUTOSAR				
	7) Multirate signal processing				
	solution 3.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.T				
	Digital Electronics subjected to implementation of action points listed above.				
	a) Scheme of I to IV Semester (2017-19) batch.				
	b) Syllabus of I/II Semester (2017-19) batch.				
	c) Modification of Scheme of III/IV Semester (2016-18) batch.				
	d) Syllabus of III/IV Semester (2016-18) batch.				
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BoS 3.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design and Embedded Systems				
	and approve the same.				
	a) Scheme approval of I to IV Semester (2017-19)				
	b) Syllabus approval of I/II Semester (2017-19)				
	c) Modification of Scheme of III/IV Semester (2016-18)				
	d) Syllabus approval of III/IV Semester (2016-18)				
	Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 15 th April 2017. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting.				
	Action Item No.1: Suggested new core courses to strengthen basic concepts and programming				
	1) Data Structures using C				
	2) Analog and Digital Circuits				
	3) Principals of Embedded systems				
	4) RISC Architectures				
	5) Electronics System Design				
	6) Real Time Embedded Systems				
	7) Advanced Digital Logic Design(ADLD) 8) VLSI Testing and IC Characterizing 9) Embedded software design				
	10) Automotive communication				
	11) Automotive Electronics				
	12) Advanced Digital Logic Verification(ADLV)				
	Action Item No.2: Suggested new electives courses to build background with application perspective				
	1) Data Communication				
	2) Image and video processing				
	3) Digital Control Systems				
	4) Standard cell design and Layout				
	5) Low power VLSI Circuits				
	6) Analog and Mixed Mode VLSI Circuits				
	7) Internet of Things				
	8) AUTOSAR				
	9) ASIC Design				
	10) MEMS				
	Resolution 3.6: Resolved to the Schemes and Syllabi of the postgraduate program M.Tech in in VLSI Design and Embedded Systems subjected to implementation of action points listed above.				
	a) Scheme of I to IV Semester (2017-19) batch.				
	b) Syllabus of I/II Semester (2017-19) batch.				
	c) Modification of Scheme of III/IV Semester (2016-18) batch.				
	d) Syllabus of III/IV Semester (2016-18) batch.				
BoS 3.7	Question Paper review				
003 5.7	Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-F				
	mapping presented.				
	Resolution 3.7: Resolved to approve the Question Paper Pattern				
Dec 2 0	Vision, Mission, POs and PSOs of School of ECE				
BoS 3.8					
	Discussion: The Vision, Mission, POs and PSOs of School of ECE were presented.				
D-C 2 0	Resolution 3.8: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE				
BoS 3.9	Any other subject with the permission of the Chair				
505 5.5	Nil.				

The Chairperson thanked all the members for the fantastic contributions

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Nalini C Iver Chairperson, BoS, SoECE

Annexure 3.1

Discussion Item

Employers Feedback:

- Enhance ability to identify and formulate problem in designing electronic system for real world applications.
- Enhance basic programming skills, to apply and realize real world problems.
- Students need to improve their ability to conduct investigations of technical issues with their level of knowledge and understanding,
- Students lack in Industry standard coding styles and algorithmic analysis.
- Understanding of professional engineering regulations, legislation and standards.
- Ability to apply the Code of Ethics and responsibilities. .
- Ability for effective communication, problem solving, conflict resolution and leadership skills. .
- Enhance ability to apply design principles in the development of hardware and software systems of varying complexity, using state of art tools for the development of VLSI/Embedded/Communication systems.

Teachers Feedback (Pre-BoS MoM):

- Conceptual understanding of circuits, system design and problem solving practice.
- Focus on problem solving using programming skills and use of online platform.
- Combinational logical reduction techniques with more number of variables in digital circuit is time consuming and . procedure based.
- Focus on embedded programing with and without optimization
- Collaborate with industry to get exposure to complete chip design flow, tools, courses etc.
- Understanding of Data converters is required for design of electronic system in real time nonlinear applications.

Students Feedback:

- Need more exposure to current trends and technologies.
- Hands on with Integrated Development Environment (IDEs)

Alumni Feedback:

- Recommended for co-delivery by industry experts.
- Industry specific skills for employability
- Recommended to focus on code optimization techniques in C/ Embedded C Programming/Data Structure Knowledge and projects.
- Suggested to have projects reviews with industry experts.
- Recommended to include elective courses in VLSI design domain to cover a complete chip design flow from netlist to GDSII.
- Connect with industry mentors for Chip design program.
- Recommended for Industry specific courses with collaboration and Co-teaching.
- Inclusion of Rapid prototyping using industry standard model based design (MBD) Approach for automotive industry

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Annexure 3.4

Discussion Item		Course
	BE (ECE)	15
1.	Introduction of machine learning course with Project-based learning which involves dynamic classroom approach in which students acquire a deeper knowledge through active investigation of real-world challenges and problems	Machine learning-17EECC307 Added-New Course
2.	Mini project focusing on code optimization using Embedded programming on an ARM based target hardware is introduced.	Mini Project-17EECW301 Added-New Course
3.	Theme based minor project in the domain of VLSI, Communication and Embedded with hardware and software integration for the desired functionality is introduced	Minor Project-17EECW302 Added-New Course
4.	Industry specific Model based design (MBD) for rapid prototyping of Automotive sub systems such as Speed control, PID control is introduced in automotive electronics laboratory	Automotive Laboratory- 17EECP302 Revised- Content
5.	Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts(Embedded operating) and realiasing the same with programming focusing on code optimization.	Operating systems and embedded system design- 17EECC304 Revised- Content
6.	Design of open ended Experiments using software defined radio (SDR) for design and testing of communication protocols with performance analysis	Communication and signal processing laboratory-17EECP301 Revised- Content
7.	Socket programming skills for point to point communication, point to multi point communication and open ended experiments for application of network devices such as switches and routers in collaboration with Juniper networks is introduced.	Computer communication network laboratory-17EECP303 Revised- Delivery and Assessment
8.	Network of physical objects that are embedded with sensors, software, and other communication protocols for connecting and exchanging data with other devices and systems over the internet is introduced in the course with experiential learning.	Internet of Things-17EECE307 Added-New Course
9.	Flipped mode of teaching is extended to strengthen electronic system design concepts for the course linear integrated circuits which engages students learning beyond class hours with access to content and interactive problem –solving interface with design experience	Linear integrated Circuits- 17EECC205 Revised- Delivery and Assessment
10	The course contents have been designed keeping in view the emerging trends in needs for skilled manpower. The course is highly practical oriented with 75 % of time spend on hands on practices by the candidates. The curriculum has been designed in consultation with industry and academic experts to map the skill sets and design methodologies.	Advanced Digital Logic Design- 17EECE302 Added-New Course
11.	. The course is intended to impart training in verifying using system Verilog incollabration with industry to develop skills required to become a verification engineers. Emphasis of the teaching curriculum is on design & Verification methodologies and on its practical applications. The course is highly practical oriented with 75 % of time spend on hands on practices by the candidates. The curriculum has been designed in consultation with industry and academic experts to map the skill sets and design methodologies, which is high in demand in VLSI & Embedded Systems industries.	Advanced Digital Logic Verification-18EECE418 Added-New Course
12	A course in the domain of analog VLSI in collaboration with industry to enhance teaching learning electronic circuit design and analysis. The course is highly practical oriented with 75 % of time spend on hands on practices by the candidates with emphasis on hands-on using industry standard tools	Analog Circuit design-17EECE301 Added-New Course



.,	Exclusive laboratory experience is introduced for design of Electronic Circuits.	Electronic System Design- 17EDEC707, Added-New Course
	Introduction of Data Structures using C for the enhancement of basic programming skills.	Data Structures using C - 17EDEC701 Added-New Course
5.	Introduction of machine learning course with Project-based learning.	Machine Learning-17EDEC705 Added-New Course
1 .	Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and practices.	Fundamentals of Signal Processing-17EDEC704, Added- New Course
5.	Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and practices.	Principles of Embedded Systems 17EDEC703, Added- New Course
6.	Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and practices.	Analog and Digital Circuits- 17EDEC702 Added- New Course
7.	Introduction of a new course focusing on how should new HLL programs be compiled and executed efficiently on a processor architecture.	RISC Architectures-17EDEC706 Added- New Course
8.	Introduction of Automotive courses based on the suggestion by industry.	Automotive Electronics- 17EDEC708, Added -New course
9.	Introduction of Automotive courses based on the suggestion by industry.	Automotive Communication- 17EDEC802, Added -New course
10). Introduction of Automotive courses based on the suggestion by industry.	AUTOSAR-17EDEE802 Added -New course
1:	1. Introduction of elective courses for getting in-lined with the industry needs.	Multi Sensor Data Fusion- 17EDEE703, Added -New course
1	2. Introduction of elective courses for getting in-lined with the industry needs.	Digital Control Systems- 17EDEE702, Added -New course
1	3. Introduction of elective courses for getting in-lined with the industry needs.	Image and Video Processing 17EDEE701, Added -New course
1	4. Introduction of elective courses for getting in-lined with the industry needs.	Internet of Things-17EDEE80 Added -New course
1	5. Introduction of elective courses for getting in-lined with the industry needs.	Multirate Signal Processing 17EDEE803, Added -New course



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	M.Tech VLSI Design and Embedded Systems	
1.	Exclusive laboratory experience is introduced for design of Digital Circuits.	Electronic System Design- 17EVEC707, Added-New Course
2.	Introduction of Data Structures using C for the enhancement of basic programming skills.	Data Structures using C - 17EVEC701 Added-New Course
3.	New process technology trends for integrated circuits is introduced.	IC Fabrication Technology- 17EVEC706 Revised
4.	Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and practices.	Principles of Embedded Systems 17EVEC703, Added- New Course
5.	Integrated theory and laboratory approach is adopted to bridge the gap between understanding theoretical concepts and practices.	Analog and Digital Circuits- 17EVEC702 Added- New Course
6.	Introduction of a new course focusing on how should new HLL programs be compiled and executed efficiently on a processor architecture.	RISC Architectures-17EVEC705 Added- New Course
7.	A course is introduced to visualize and analyze basic model of RTES and understand its characteristics, safety and reliability aspects to achieve highly reliable software.	Real Time Embedded System- 17EVEC709
8.	The course contents have been designed keeping in view the emerging trends in needs for skilled manpower. The course is highly practical oriented with 75 % of time spend on hands on practices by the candidates. The curriculum has been designed in consultation with industry and academic experts to map the skill sets and design methodologies.	Advanced Digital Logic Design- 17EVEC710 Added-New Course
9.	The course is intended to impart training using system Verilog in collaboration with industry to develop skills required to become testing and verification engineers. Emphasis of the teaching curriculum is on design & verification methodologies and on its practical applications. The course is highly practical oriented with 75 % of time spend on hands on practices by the candidates. The curriculum has been designed in consultation with industry and academic experts to map the skill sets and design methodologies, which is high in demand in VLSI & Embedded Systems industries.	Advanced Digital Logic Verification-17EVEC802, Added-New Course
10	The course is intended to impart training in system Verilog in collaboration with industry to develop skills required to become testing and verification engineers. Emphasis of the teaching curriculum is on testing and characterization methodologies which is high in demand in VLSI & Embedded Systems industries.	Testing and IC characterization 17EVEC711 Added-New Course
11	. Introduction of Automotive courses based on the suggestion by industry.	Automotive Electronics- 17EVEC708, Added -New course
12	. Introduction of Automotive courses based on the suggestion by industry.	AUTOSAR-17EVEE802 Added -New course
13	. Introduction of elective courses for getting in-lined with the industry needs.	Standard Cell Design and Layou 17EVEE703 Added-New Course
14	I. Introduction of elective courses for getting in-lined with the industry needs.	Digital Control Systems- 17EVEE702 Added-New Course

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15. Introduction of elective courses for getting in-lined with the industry needs.	Image and Video Processing- 17EVEE701 Added-New Course
16. Introduction of elective courses for getting in-lined with the industry needs.	Internet of Things-17EVEE801 Added-New Course
17. Introduction of elective courses for getting in-lined with the industry needs.	Low Power VLSI Circuits- 17EVEE704 Added-New Course
18. Introduction of elective courses for getting in-lined with the industry needs.	Analog and Mixed Mode VLSI Cicuits-17EVEE705 Added-New Course
19. Introduction of elective courses for getting in-lined with the industry needs.	ASIC Design-17EVEE803, MEMS- 17EVEE804 Added-New Course

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Action Taken Report

3ndBoard of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

15thApril 2017

KLE Technological University

(Established under Karnataka Act No.22, 2013)

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The following are the action items proposed during 3rd Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 15thApril 2017. The corresponding actions taken are also listed below.

tem No	Description	Action Taken
	To welcome the BoS Members and present department	Noted
	achievements & initiatives and discussed about the inputs from	
BoS 3.1	all stake holders. <u>(Annexure 2.1)</u>	
	Resolution 2.1: The BoS members appreciated the work done	
	towards recognition of KLE Technological University as a State	
	private University effective from 2015.	Nichard
	To read and confirm the minutes of 2 nd BoS meeting held on 2 nd	Noted
BoS 3.2	April 2016	
	Resolution 2.2: Minutes of the last meeting were read and confirmed by BoS.	
	To confirm the action taken report on the minutes of the previous	Noted
	meeting held on 2 nd April 2016	Noted
BoS 3.3	Resolution 2.3: BoS confirmed the action taken report on the	
	minutes of the previous meeting held on 2 nd April 2016 and	
	suggestions were implemented.	
BoS 3.4	To consider the Schemes and Syllabi of the undergraduate	The BoS members noted the progress of
	program B.E in Electronics & Communication and approve the	the School and recommended action
	same.	items and timeline.
	a) Scheme approval of I to VIII Semester (2017-21)	
	batch	Action Item No.1: Suggested new con
	b) Syllabus approval of I / II Semester, Basic Electronics	courses focusing on project based learnir
	for Mechanical and Electrical stream course (2017-	ATR:
	21) batch	Introduction of Machine learning cours
	c) Scheme approval of III to VIII Semester (2016-20)	with Project-based learning which involve
	batch	dynamic classroom approach in white
	d) Syllabus approval of III to VIII Semester (2016-20) batch	students acquire a deeper knowledg
		through active investigation of real-wor challenges and problems in collaboration
	e) Modification of Scheme of V to VIII Semester (2015- 19) batch	with Samsung R&D, Bangalore.
	f) Syllabus approval of V and VI Semester (2015-19)	Mini Project focusing on embedde
	batch	programming and optimization
	Discussion : Based on the discussions following action items as	Minor project theme based in the doma
	agreed upon by everyone were finalized and the same were	of VLSI, Communication and Embedde
	circulated to all the members on 15 th April 2017. Persons	systems.
	responsible for these action items have already initiated the	Engineering Design and Produ
	actions, which will be shared in the next BoS meeting.	Realization: Design process from conce
	The details of discussion are in <i>Annexure 3.4</i> .	to solution and Building prototypes.
	Action Item No.1: Action Item No.1: Suggested new core courses	Changes are made according to th
	focusing on project based learning	suggestions and will be presented durin
	1) Machine learning	the next BoS.
	2) Mini Project	Action Item No.2: Model based desig
	3) Minor project	approach for rapid prototyping.
	4) Engineering Design	ATR:
	5) Product Realization	Industry specific Model based desig
	Action Item No.2: Model based design approach for rapid	(MBD) for rapid prototyping of Automoti
	prototyping	sub systems is introduced in Automoti
		electronics laboratory in collaboration
	1) Automotive electronics laboratory	with Bosch, Bangalore.
	Antion How Ma 2: Interrupt I amount I	Action Itom No 2: Interested Annual
	Action Item No.3: Integrated Approach: focus on hands on and	Action Item No.3: Integrated Approac
	code optimization	focus on hands on and code optimization
		ATR:
	1) Operating system and Embedded system	Integrated theory and lab approach
		adapted to bridge the gap betwee
	Action Item No.4: Enhancing reasoning ability and investigation	understanding theoretical and realization
	and interpretation: Open ended experiments	the same with programming and al
	1) Communication and Signal processing lab	
	1) Communication and Signal processing lab	
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2) Computer communication network lab coding style and optimization in the course Action Item No.5: Flipped mode of delivery to strengthened Operating system and embedded system system design concepts Action Item No.4: Enhancing reasoning 1) Linear Integrated circuits ability and investigation and interpretation: Open ended experiments Action Item No.6: Suggested new elective courses with Industry ATR: Collaboration for design and delivery Laboratory curriculum focusing on 1) Advanced Digital Logic structured and open ended category of 2) Advanced Digital Logic Verification experiments to increase students 3) Analog Circuit Design reasoning ability and experimental 4) Internet of Things investigations enhanced. Resolution 3.4: Resolved to approve the following Schemes and Socket programming skills for point to Syllabi of the B.E in Electronics & Communication subject to point communication, point to multi point implementation of action points listed above: communication and open ended a) experiments for application of network Scheme of I to VIII Semester (2017-21) batch. b) Syllabus of I / II Semester, Basic Electronics course devices such as switches and routers in for Mechanical and Electrical streams (2017-21) collaboration with Juniper Networks, batch Bangalore is introduced in Computer c) Scheme of III to VIII Semester (2016-20) batch. Communication Network Lab. Syllabus of III to VIII Semester (2016-20) batch. d) e) Modification of Scheme of V to VIII Semester Design and testing of communication (2015-19) batch protocols with performance analysis using Syllabus of V and VI Semester (2015-19) batch. f) software defined radio (SDR) is introduced Scheme of Minor program of 2016-20 batch. g) in Communication and signal processing h) Scheme of Minor program of 2015-19 batch. lab. Action Item No.5: Flipped mode of delivery to strengthened system design concepts. ATR: Flipped mode of teaching is introduced is introduced to strengthened design concepts for Linear Integrated Circuits course which engages students learning beyond class hours with interactive problem solving interface. Action Item No.6: Suggested new elective courses with Industry Collaboration for design and delivery ATR: Elective courses (4) are introduced in in verticals (identified by SoECE) VLSI, Embedded and Communication Systems in collaboration with Industry in respective domain. Specialized Courses in VLSI domain, one of the vertical of SoECE, introduced are Advanced Digital Logic design

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	х.	and Advanced Digital Logic Verification in
		collaboration with SEER academy and
		IESA, Bangalore whereas Analog Circuit
		design in collaboration with Sankalp
		semiconductors Pvt. Ltd, hubli with focus
	·	
	×	on project based learning.
		Course on Internet of Things (IoT) is
		introduced in interaction with Bosch,
		Bangalore.
		Changes are made according to the
		suggestions and will be presented during
		the next BoS.
		The Defense is a stand the property of
BoS 3.5	To consider the Schemes and Syllabi of the postgraduate	The BoS members noted the progress of the School and recommended action
	program M.Tech in Digital Electronics and approve the same.	
	a) Scheme approval of I to IV Semester (2017-19)	items and timeline.
	b) Syllabus approval of I/II Semester (2017-19)	Artis Han No 4. Compared and some
	c) Modification of Scheme of III/IV Semester (2016-18)	Action Item No.1: Suggested new core
	d) Syllabus approval of III/IV Semester (2016-18)	courses to strengthen basic concepts and
	Discussion: Based on the discussions following action items as	programming
	agreed upon by everyone were finalized and the same were	ATR:
	circulated to all the members on 15 th April 2017. Persons	Fundamental concepts of analog and
	responsible for these action items have already initiated the	digital electronics with application
	actions, which will be shared in the next BoS meeting.	perspective is introduced in the courses
	Action Item No.1: Suggested new core courses to strengthen	Analog and Digital Circuits and Electronic
	basic concepts and programming	System Design.
	1) Data Structures using C	in a state of the second in
	2) Analog and Digital Circuits	Integrated theory and lab approach is
	3) Principles of Embedded systems	adopted to bridge the gap between
	4) Fundamentals of signal processing	understanding theoretical concepts and
	5) RISC Architectures	realization of the same with programming
	6) Machine learning	and also coding style and optimization in
	7) Electronics System Design	basic courses Data Structures using C,
	8) Embedded software design	Principles of Embedded systems,
	9) Automotive communication	Fundamentals of signal processing, RISC
	10) Automotive Electronics	Architectures, Embedded software
	11) Multimedia Signal processing	design, and Multimedia Signal
	Action Item No.2: Suggested new electives courses to build	processing.
	background with application perspective	Industry specific Model based design
		(MBD) for rapid prototyping of Automotive
	1) Data Communication	sub systems is introduced in courses
	2) Image and video processing	Automotive electronics and Automotive
	3) Digital Control System	communication in collaboration with
	4) Multi sensor data fusion	Bosch.
	5) Internet of Things (IoT)	Introduction of Machine Learning course
	6) AUTOSAR	with Project-based learning
	7) Multirate signal processing	Changes are made according to the
	// World are signal processing	suggestions and will be presented during
	Developing 2 F. Developing the following Schemes and	the next BoS.
	Resolution 3.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in Digital	the next bos.

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	Electronics subjected to implementation of action points listed	Action Item No.2: Suggested new
	above.	electives courses
	a) Scheme of I to IV Semester (2017-19) batch.	ATR:
	b) Syllabus of I/II Semester (2017-19) batch.	Elective courses (7) focusing on embedded
	c) Modification of Scheme of III/IV Semester (2016-18)	systems and Information processing are
	batch.	introduced
	d) Syllabus of III/IV Semester (2016-18) batch.	
		Specialized Courses introduced under
		Information processing domain are Data
		Communication, Image and Video
		processing, Multirate signal processing
		and under Embedded domain are IoT,
		Multisensor data fusion and Digital
		Control system with focus on hands-on.
		A standardized interface for software
		components in the application layer for
		building applications to support the vehicle
		functions is introduced in AUTOSAR in
		collaboration with Bosch, Bangalore
		Changes are made according to the
		suggestions and will be presented during
		the next BoS.
BoS 3.6	To consider the Schemes and Syllabi of the postgraduate	The BoS members noted the progress of
	program M.Tech in VLSI Design and Embedded Systems and	the School and recommended action
	approve the same.	items and timeline.
	a) Scheme approval of I to IV Semester (2017-19)	
	b) Syllabus approval of I/II Semester (2017-19)	Action Item No.1: Suggested new core
	c) Modification of Scheme of III/IV Semester (2016-18)	courses to strengthen basic concepts and
	d) Syllabus approval of III/IV Semester (2016-18)	programming
	Discussion: Based on the discussions following action items as	ATR:
	agreed upon by everyone were finalized and the same were	Fundamental concepts of analog and
	circulated to all the members on 15 th April 2017. Persons	digital electronics with application
	responsible for these action items have already initiated the	perspective is introduced in the courses
	actions, which will be shared in the next BoS meeting.	Analog and Digital Circuits, Real Time
		Embedded Systems and Electronic System
	Action Item No.1: Suggested new core courses to strengthen	Design.
	basic concepts and programming	
	1) Data Structures using C	Integrated theory and lab approach is
	2) Analog and Digital Circuits	adapted to bridge the gap between
	3) Principals of Embedded systems	understanding theoretical concepts and
	4) RISC Architectures	realization of the same with programming
	5) Electronics System Design	and also coding style and optimization in
	6) Real Time Embedded Systems	basic courses Principles of Embedded
	7) Advanced Digital Logic Design(ADLD)	systems, Fundamentals of signal
	8) VLSI Testing and IC Characterizing	processing, RISC Architectures, Embedded
	9) Embedded software design	software design.
	10) Automotive communication	
	11) Automotive Electronics	Industry specific Model based design
	12) Advanced Digital Logic Verification(ADLV)	(MBD) for rapid prototyping of Automotive
	Action Itom No 2: Suggested your stating sources to build	sub systems is introduced in courses
	Action Item No.2: Suggested new electives courses to build background with application perspective	Automotive electronics and Automotive
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	1) Data Communication	Communication in collaboration with Bosch.
	 2) Image and video processing 3) Digital Control Systems 	Advanced digital design concepts, their
	4) Standard cell design and Layout5) Low power VLSI Circuits	verification and testing is introduced in ADLD, ADLV and VLSI Testing and IC
	6) Analog and Mixed Mode VLSI Circuits7) Internet of Things	Characterization courses respectively with
	8) AUTOSAR	focus on hands-on.
	9) ASIC Design 10) MEMS	Changes are made according to the suggestions and will be presented during
	Resolution 3.6: Resolved to the Schemes and Syllabi of the	the next BoS. Action Item No.2: Suggested new
	postgraduate program M.Tech in in VLSI Design and Embedded Systems subjected to implementation of action points listed	electives courses
	above. a) Scheme of I to IV Semester (2017-19) batch.	ATR: Elective courses (10) focusing on
	 b) Syllabus of I/II Semester (2017-19) batch. c) Modification of Scheme of III/IV Semester (2016-18) 	embedded systems and VLSI design are introduced.
	batch. d) Syllabus of III/IV Semester (2016-18) batch.	Specialized Courses introduced under Embedded Systems domain are Data
		Communication, Image and Video processing, Digital Control system , IoT
		and under VLSI domain are ASIC Design,
		MEMS, Standard Cell design and Layout, Low power VLSI Circuits and Analog and
		Mixed mode VLSI with focus on hands-on.
		A standardized interface for software
		components in the application layer for building applications to support the vehicle
		functions is introduced in AUTOSAR in collaboration with Bosch, Bangalore.
		Changes are made according to the
		suggestions and will be presented during the next BoS.
BoS 3.7	Question Paper review Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI mapping presented. Resolved to approve the Question Paper Pattern	QP Pattern is incorporated in all the courses.
BoS 3.8	Vision, Mission, POs and PSOs of School of ECE Discussion: The Vision, Mission, POs and PSOs of School of ECE	SoECE staff aligned to Vision, Mission, POs and PSOs .
	were presented. Resolution 2.8: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE	
BoS 3.9	Any other subject with the permission of the Chair Nil.	

for REGISTRAR KLE Technological University HUBBALLI-580 031

NC 0 Dr.Nalini Clyer Chairperson, BoS, SoECE



Minutes

4th Board of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

7th April 2018

KLE Technological University

(Established under Karnataka Act No.22, 2013)

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The following are the minutes of the Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 7th April 2018 at 10:30 am at the ED Studio of the School. **The following members were present.**

SI No	Name	Designation	Position
1.	Dr. Nalini C.Iyer	Head of School, SoECE	Chairperson
2.	Dr. R M Bankar	Professor, SoECE	Member
3.	Dr. Uma Mudengudi	Professor, SoECE	Member
4.	Dr. Priyatamkumar	Professor, SoECE	Member
5.	Dr. Anil Nandi	Professor, SoECE	Member
6.	Dr. Saroja S	Professor, SoECE	Member
7.	Prof. Ujwala Patil	Associate Professor, SoECE	Member
8.	Prof. Sanjay Eligar	Assistant Professor, SoECE	Member
9.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member
10.	Dr. Chetan Parekh	Professor, Department of EC,IIIT Bangalore	Member
11.	Dr. Lokesh Boregouda	Head Research, Samsung India, Bangalore	Member
12.	Dr. P Subbanna Bhat	Professor Emirates, KLE Tech	Member
13.	Mr. Vivek G Pawar	Founder & CEO, Sankalp Semiconductor	Member
14.	Mrs. Padmini Navalgund	RBEI, Bangalore	Member
15.	Mr. Shiva Turmuri	Analog Devices, Bangalore	Member
16.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductor	Member
17.	Dr. Sujata S Kotabagi	Professor, SoECE	Member
18.	Dr. R B Shettar	Professor, SoECE	Member
19.	Prof. Suneeta V B	Professor, SoECE	Member
20.	Prof. P. C. Nissimgoudar	Associate Professor, SoECE	Member
21.	Prof, Rohini Hongal	Associate Professor, SoECE	Member
22.	Prof. R. M. Shet	Assistant Professor, SoECE	Member
23.	1. UG: Rohan D		Student Members
	2. UG: Sheetal		
	3. PG1 :Ravi		
	4. PG2: Pratima		
	5. PhD: Suhas Shirol		

The following members have sought leave of absence:

SI No	Name	Designation	Position
1.	Dr. Lokesh Boregouda	Head Research, Samsung India, Bangalore	Member
2.	Mr. Shiva Turmuri Analog Devices, Bangalore		Member
3.	Mr. Vivek G Pawar	Founder & CEO, Sankalp Semiconductor	Member

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SI No	Particulars	
4.1	To welcome the BoS Members and present department achievements & initiatives	
4.2	To read and confirm the minutes of 3 rd BoS meeting held on 15 th April 2017	
4.3	To confirm the action taken report on the minutes of the previous meeting held on 15 th April 2017	
4.4	 To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same. a) Scheme approval of I to VIII Semester (2018-22) b) Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrical stream course (2018-22) c) Scheme approval of III to VIII Semester (2017-21) d) Syllabus approval of III to VIII Semester (2017-21) e) Scheme approval of V to VIII Semester (2016-20) f) Syllabus approval of V to VIII Semester (2016-20) g) Modification of Scheme of VII and VIII Semester (2015-19) h) Syllabus approval of VII and VIII Semester (2015-19) i) Scheme approval: Scheme 2017-21 in Minor Program j) Scheme approval: Scheme 2016-20 in Minor Program k) Syllabus approval: Scheme 2016-20 in Minor Program 	
4.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme approval of I to IV Semester (2018-20) b) Syllabus approval of I/II Semester (2018-20) c) Modification of Scheme of III/IV Semester (2017-19) d) Syllabus approval of III/IV Semester (2017-19)	
4.6	 To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded Systems and approve the same. a) Scheme approval of I to IV Semester (2018-20) b) Syllabus approval of I/II Semester (2018-20) c) Modification of Scheme of III/IV Semester (2017-19) d) Syllabus approval of III/IV Semester (2017-19) 	
4.7	Question Paper review and Discussion on attainment of POs and PSOs	
4.8	Vision, Mission, POs and PSOs of School of ECE	
4.9	Any other matter for discussion with the permission of the chair	

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BoS 4.1 To welcome the BoS Members and present department achievements & initiatives

Resolution 4.1: The BoS members appreciated the initiatives of SoECE and lauded its achievements.

BoS 4.2	To read and	confirm the minutes of 3 rd BoS meeting held on 15 th April 2017
		To welcome the BoS Members and present department achievements & initiatives and discussed about the inputs from all stake holders (Annexure 4.1)
	BoS 3.1	Resolution 3.1: The BoS members appreciated the work done towards recognition of KLE
		Technological University as a State private University effective from 2015.
	BoS 3.2	To read and confirm the minutes of 3 rd BoS meeting held on 15 th April 2017 Resolution 3.2: Minutes of the last meeting were read and confirmed by BoS.
		To confirm the action taken report on the minutes of the previous meeting held on 15 th Apri 2017
	BoS 3.3	Resolution 3.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 15 th April 2017 and suggestions were implemented.
	BoS 3.4	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same.
		 a) Scheme approval of I to VIII Semester (2017-21) batch b) Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrica
		stream course (2017-21) batch c) Scheme approval of III to VIII Semester (2016-20) batch
		 d) Syllabus approval of III to VIII Semester (2016-20) batch e) Modification of Scheme of V to VIII Semester (2015-19) batch
		f) Syllabus approval of V and VI Semester (2015-19) batch
		Discussion : Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 15 th April 2017. Persons responsible for these action items have already initiated the actions, which will be shared in the
		next BoS meeting. The details of discussion are in <i>Annexure 3.4.</i>
		Action Item No.1: Action Item No.1: Suggested new core courses focusing on project based learning
		1) Machine learning
		2) Mini Project
		3) Minor project
		4) Engineering Design
		5) Product Realization Action Item No.2: Model based design approach for rapid prototyping
		1) Automotive electronics laboratory
		Action Item No.3: Integrated Approach: focus on hands on and code optimization
		1) Operating system and Embedded system
		Action Item No.4: Enhancing reasoning ability and investigation and interpretation: Open ended experiments
		1) Communication and Signal processing lab
		 2) Computer communication network lab Action Item No.5: Flipped mode of delivery to strengthened system design concepts 1) Linear Integrated circuits
		Action Item No.6: Suggested new elective courses with Industry Collaboration for design and

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	delivery
	1) Advanced Digital Logic
	2) Advanced Digital Logic Verification
	3) Analog Circuit Design
	4) Internet of Things
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	Resolution 3.4: Resolved to approve the following Schemes and Syllabi of the B.E in
	Electronics & Communication subject to implementation of action points listed above:
	a) Scheme of I to VIII Semester (2017-21) batch.
	b) Syllabus of I / II Semester, Basic Electronics course for Mechanical and Electr
	streams (2017-21) batch
	 c) Scheme of III to VIII Semester (2016-20) batch. d) Syllabus of III to VIII Semester (2016-20) batch.
	e) Modification of Scheme of V to VIII Semester (2015-19) batch
	f) Syllabus of V and VI Semester (2015-19) batch.
	g) Scheme of Minor program of 2016-20 batch.
	h) Scheme of Minor program of 2015-19 batch.
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BoS 3.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronic
	and approve the same. a) Scheme approval of I to IV Semester (2017-19)
	b) Syllabus approval of I/II Semester (2017-19)
	c) Modification of Scheme of III/IV Semester (2016-18)
	d) Syllabus approval of III/IV Semester (2016-18)
	Discussion: Based on the discussions following action items as agreed upon by everyone were
	finalized and the same were circulated to all the members on 15 th April 2017. Persons
	responsible for these action items have already initiated the actions, which will be shared in t
	next BoS meeting.
	Action Item No.1: Suggested new core courses to strengthen basic concepts and programming
	1. Data Structures using C
	2. Analog and Digital Circuits
	 Principals of Embedded systems Fundamentals of signal processing
	5. RISC Architectures
	6. Machine learning
	7. Electronics System Design
	8. Embedded software design
	9. Automotive communication
	10. Automotive Electronics
	11. Multimedia Signal processing
	Action Item No.2: Suggested new electives courses to build background with application
	perspective
	1 Data Communication
	 Data Communication Image and video processing
	 Image and video processing Digital Control System
	4. Multi sensor data fusion
	5. Internet of Things (IoT)
	6. AUTOSAR
1.1	
	7. Multirate signal processing

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~	 Resolution 3.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics subjected to implementation of action points listed above. a) Scheme of I to IV Semester (2017-19) batch. b) Syllabus of I/II Semester (2017-19) batch. c) Modification of Scheme of III/IV Semester (2016-18) batch. d) Syllabus of III/IV Semester (2016-18) batch.
BoS 3.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design and
	Embedded Systems and approve the same.
	a) Scheme approval of I to IV Semester (2017-19)
	b) Syllabus approval of I/II Semester (2017-19)
	c) Modification of Scheme of III/IV Semester (2016-18)
	d) Syllabus approval of III/IV Semester (2016-18)
	Discussion: Based on the discussions following action items as agreed upon by everyone were
	finalized and the same were circulated to all the members on 15 th April 2017. Persons responsible for these action items have already initiated the actions, which will be shared in the
	next BoS meeting.
	next bos meeting.
	Action Item No.1: Suggested new core courses to strengthen basic concepts and programming
	1. Data Structures using C
	2. Analog and Digital Circuits
	3. Principals of Embedded systems
	4. RISC Architectures
	5. Electronics System Design
	6. Real Time Embedded Systems
	7. Advanced Digital Logic Design(ADLD)
	8. Testing and IC Characterizing
	9. Embedded software design
	10. Automotive communication
	11. Automotive Electronics
	12. Advanced Digital Logic Verification(ADLV)
	Action Item No.2: Suggested new electives courses to build background with application perspective
	1. Data Communication
	2. Image and video processing
	3. Digital Control Systems
	4. Standard cell design and Layout
	5. Low power VLSI Circuits
	6. Analog and Mixed Mode VLSI Circuits
	7. Internet of Things
	8. AUTOSAR
	9. ASIC Design
	10. MEMS
	Resolution 3.6: Resolved to the Schemes and Syllabi of the postgraduate program M.Tech i
	in VLSI Design and Embedded Systems subjected to implementation of action points liste
	above.
	a) Scheme of I to IV Semester (2017-19) batch.
	b) Syllabus of I/II Semester (2017-19) batch.
	c) Modification of Scheme of III/IV Semester (2016-18) batch.
	d) Syllabus of III/IV Semester (2016-18) batch.
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BoS 3.7	Question Paper review

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	BoS 3.8 BoS 3.9 Resolution 4.	 Discussion: The Question Paper along with assessment p and PO-PSO-PI mapping presented. Resolved to approve the Question Paper Pattern Vision, Mission, POs and PSOs of School of ECE Discussion: The Vision, Mission, POs and PSOs of School Resolution 2.8: Resolved to approve the Vision, Mission Any other subject with the permission of the Chair Nil. 2: Resolved to confirm the minutes of its 3rd BoS meeting 	of ECE were presented. I, POs and PSOs of School of ECE
BoS 4.3	BoS 3.1	To welcome the BoS Members and present department achievements & initiatives and discussed about the inputs from all stake holders (Annexure 4.1) Resolution 3.1: The BoS members appreciated the work done towards recognition of KLE Technological University as a State private University effective from 2015.	Noted .
	BoS 3.2	To read and confirm the minutes of 3 rd BoS meeting held on 15 th April 2017 Resolution 3.2: Minutes of the last meeting were read and confirmed by BoS.	Noted
	BoS 3.3	To confirm the action taken report on the minutes of the previous meeting held on 15 th April 2017 Resolution 3.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 15 th April 2017 and suggestions were implemented.	Noted
	BoS 3.4	 To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same. a) Scheme approval of I to VIII Semester (2017-21) batch b) Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrical stream course (2017-21) batch c) Scheme approval of III to VIII Semester (2016-20) batch d) Syllabus approval of III to VIII Semester (2016-20) batch e) Modification of Scheme of V to VIII Semester (2016-20) batch f) Syllabus approval of V and VI Semester (2015-19) batch f) Syllabus approval of V and VI Semester (2015-19) batch Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 15th April 2017. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting. The details of discussion are in Annexure 3.4. Action Item No.1: Action Item No.1: Suggested new core courses focusing on project based learning 1) Machine learning 2) Mini Project 3) Minor project 	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested new core courses focusing on project based learning ATR: Introduction of Machine learning course with Project-based learning which involves dynamic classroom approach in which students acquire a deeper knowledge through active investigation of real-world challenges and problems in collaboration with Samsung R&D, Bangalore. Mini Project focusing on embedded programming and optimization Minor project theme based in the domain of VLSI, Communication and Embedded systems. Engineering Design and Product Realization: Design process from concept to solution and Building prototypes. Changes are made according to the suggestions and will be presented

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	4)	Engineering Design	during the next BoS.
		Product Realization	Action Item No.2: Model based
		tem No.2: Model based design approach for	design approach for rapid
	rapid pr	ototyping	prototyping. ATR:
10	1)	Automotive electronics laboratory	Industry specific Model based design (MBD) for rapid prototyping of
	Action I	tem No.3: Integrated Approach: focus on	Automotive sub systems is introduced
	hands o	n and code optimization	in Automotive electronics laboratory
			in collaboration with Bosch, Bangalore.
	1)	Operating system and Embedded system	
	Action I	tem No.4: Enhancing reasoning ability and	Action Item No.3: Integrated
	investig	ation and interpretation: Open ended	Approach: focus on hands on and
	experim	lents	code optimization
	1)	Communication and Signal processing lab	Integrated theory and lab approach
		Computer communication network lab	is adapted to bridge the gap between
		tem No.5: Flipped mode of delivery to	understanding theoretical and
	strengt	hened system design concepts	realization the same with
	1)	Linear Integrated circuits	
			programming and also coding style and optimization in the course
		tem No.6: Suggested new elective courses	Operating system and embedded
	with Ind	lustry Collaboration for design and delivery	system
	1)	Advanced Digital Logic	Action Item No.4: Enhancing
	2)	Advanced Digital Logic Verification	reasoning ability and investigation
	3)	Analog Circuit Design	and interpretation: Open ended
	4)	Internet of Things	experiments
	_		ATR:
		ion 3.4: Resolved to approve the following	Laboratory curriculum focusing on
		es and Syllabi of the B.E in Electronics & Inication subject to implementation of action	structured and open ended category
		isted above:	of experiments to increase students
	a)	Scheme of I to VIII Semester (2017-21)	reasoning ability and experimental
		batch.	investigations enhanced.
	b)	Syllabus of I / II Semester, Basic	
		Electronics course for Mechanical and	Socket programming skills for point
		Electrical streams (2017-21) batch Scheme of III to VIII Semester (2016-20)	to point communication, point to
	c)	batch.	multi point communication and open ended experiments for application of
	d)	Syllabus of III to VIII Semester (2016-20)	network devices such as switches and
		batch.	routers in collaboration with Juniper
	e)	Modification of Scheme of V to VIII Semester (2015-19) batch	Networks, Bangalore is introduced in Computer Communication Network
	f)	Syllabus of V and VI Semester (2015-19)	Lab.
	.,	batch.	
	g)	Scheme of Minor program of 2016-20	Design and testing of communication
		batch.	protocols with performance analysis
	h)	Scheme of Minor program of 2015-19 batch.	using software defined radio (SDR) is
		batch.	introduced in Communication and
			signal processing lab.
			Action Item No.5: Flipped mode of
			delivery to strengthened system
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		design concepts. ATR: Flipped mode of teaching is introduced is introduced to strengthened design concepts for Linear Integrated Circuits course which engages students learning beyond class hours with interactive problem solving interface. Action Item No.6: Suggested new elective courses with Industry Collaboration for design and delivery ATR:
		Elective courses (4) are introduced in in verticals (identified by SoECE) VLSI, Embedded and Communication Systems in collaboration with Industry in respective domain. Specialized Courses in VLSI domain,
		one of the vertical of SoECE, introduced are Advanced Digital Logic design and Advanced Digital Logic Verification in collaboration with SEER academy and IESA, Bangalore whereas Analog Circuit design in collaboration with Sankalp semiconductors Pvt. Ltd, hubli with
		focus on project based learning. Course on Internet of Things (IoT) is introduced in interaction with Bosch, Bangalore. Changes are made according to the suggestions and will be presented during the next BoS.
BoS 3.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme approval of I to IV Semester (2017-19) b) Syllabus approval of I/II Semester (2017-19) c) Modification of Scheme of III/IV Semester (2016-18) d) Syllabus approval of III/IV Semester (2016-18) Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested new core courses to strengthen basic concepts and programming ATR: Fundamental concepts of analog and digital electronics with application

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the same were circulated to all the members on 15th perspective is introduced in the April 2017. Persons responsible for these action items courses Analog and Digital Circuits have already initiated the actions, which will be shared and Electronic System Design. in the next BoS meeting. Action Item No.1: Suggested new core courses to Integrated theory and lab approach is strengthen basic concepts and programming adopted to bridge the gap between Data Structures using C understanding theoretical concepts 1. 2. Analog and Digital Circuits and realization of the same with 3. Principals of Embedded systems programming and also coding style 4. Fundamentals of signal processing and optimization in basic courses Principles of Embedded systems, 5. **RISC** Architectures Machine learning Fundamentals of signal processing, 6. RISC Architectures, Embedded Electronics System Design 7. software design, and Multimedia 8. Embedded software design 9 Automotive communication Signal processing. 10. Automotive Electronics Industry specific Model based design 11. Multimedia Signal processing (MBD) for rapid prototyping of Action Item No.2: Suggested new electives courses to Automotive sub systems is introduced build background with application perspective in courses Automotive electronics and Automotive communication in collaboration with Bosch. 1. Data Communication 2 Image and video processing Introduction of machine learning course with Project-based learning 3. Digital Control System 4. Multi sensor data fusion Changes are made according to the 5. Internet of Things (IoT) suggestions and will be presented 6. AUTOSAR during the next BoS. 7 Multirate signal processing Action Item No.2: Suggested new Resolution 3.5: Resolved to approve the following electives courses ATR: Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics subjected to Elective courses (7) focusing on implementation of action points listed above. embedded systems and Information Scheme of I to IV Semester (2017-19) a) processing are introduced batch. b) Syllabus of I/II Semester (2017-19) batch. Specialized Courses introduced under Modification of Scheme of III/IV Semester c) Information processing domain are (2016-18) batch. Data Communication, Image and d) Syllabus of III/IV Semester (2016-18) batch. Video processing, Multirate signal processing and under Embedded domain are IoT, Multisensor data fusion and Digital Control system with focus on hands-on. A standardized interface for software components in the application layer for building applications to support the vehicle functions is introduced in AUTOSAR in collaboration with Bosch, Bangalore Changes are made according to the suggestions and will be presented during the next BoS. BoS 3.6 To consider the Schemes and Syllabi of the The BoS members noted the

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postgraduate program M.Tech in VLSI Design and progress of the School and Embedded Systems and approve the same. recommended action items and Scheme approval of I to IV Semester a) timeline. (2017 - 19)b) Syllabus approval of I/II Semester (2017-19) Action Item No.1: Suggested new c) Modification of Scheme of III/IV Semester core courses to strengthen basic (2016 - 18)concepts and programming d) Syllabus approval of III/IV Semester (2016-18) ATR: Discussion: Based on the discussions following action Fundamental concepts of analog and items as agreed upon by everyone were finalized and digital electronics with application the same were circulated to all the members on 15th perspective is introduced in the April 2017. Persons responsible for these action items courses Analog and Digital Circuits, have already initiated the actions, which will be shared Real Time Embedded Systems and in the next BoS meeting. Electronic System Design. Action Item No.1: Suggested new core courses to Integrated theory and lab approach is strengthen basic concepts and programming adapted to bridge the gap between 1 understanding theoretical concepts Data Structures using C 2 Analog and Digital Circuits and realization of the same with 3 Principals of Embedded systems programming and also coding style 4 **RISC** Architectures and optimization in basic courses 5. **Electronics System Design** Principles of Embedded systems, Real Time Embedded Systems Fundamentals of signal processing, 6. 7. Advanced Digital Logic Design(ADLD) **RISC** Architectures, Embedded 8. Testing and IC Characterizing software design. 9. Embedded software design 10. Automotive communication Industry specific Model based design 11. Automotive Electronics (MBD) for rapid prototyping of 12. Advanced Digital Logic Verification(ADLV) Automotive sub systems is introduced in courses Automotive electronics Action Item No.2: Suggested new electives courses to and Automotive Communication in build background with application perspective collaboration with Bosch. 1. Data Communication Advanced digital design concepts, 2. Image and video processing their verification and testing is 3. **Digital Control Systems** introduced in ADLD, ADLV and 4. Standard cell design and Layout Testing and IC Characterization 5 Low power VLSI Circuits courses respectively with focus on 6. Analog and Mixed Mode VLSI Circuits hands-on. 7. Internet of Things 8. AUTOSAR Changes are made according to the 9 **ASIC** Design suggestions and will be presented 10. MEMS during the next BoS. Action Item No.2: Suggested new Resolution 3.6: Resolved to the Schemes and Syllabi electives courses of the postgraduate program M.Tech in in VLSI Design ATR: and Embedded Systems subjected to implementation Elective courses (10) focusing on of action points listed above. embedded systems and VLSI design Scheme of I to IV Semester (2017-19) a) are introduced. batch. Specialized Courses introduced under b) Syllabus of I/II Semester (2017-19) batch. Embedded Systems domain are Data Modification of Scheme of III/IV Semester c) Communication, Image and Video (2016-18) batch. d) Syllabus of III/IV Semester (2016-18) batch. processing, Digital Control system IoT and under VLSI domain are ASIC

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			Design, MEMS, Standard Cell design
			and Layout, Low power VLSI Circuits
			and Analog and Mixed mode VLSI
			with focus on hands-on.
			A standardized interface for software
			components in the application layer
			for building applications to support
			the vehicle functions is introduced in
			AUTOSAR in collaboration with Bosch,
			Bangalore.
			Changes are made according to the
			suggestions and will be presented
			during the next BoS.
	BoS 3.7	Question Paper review	
		Discussion: The Question Paper along with assessment	QP Pattern is incorporated in all the
	· · · · · ·	patterns with respect to Bloom's Levels and PO-PSO-PI	courses.
		mapping presented.	
		Resolved to approve the Question Paper Pattern	
	BoS 3.8	Vision, Mission, POs and PSOs of School of ECE	SoECE staff aligned to Vision, Mission,
		Discussion: The Vision, Mission, POs and PSOs of	POs and PSOs .
		School of ECE were presented.	
		Resolution 2.8: Resolved to approve the Vision,	
		Mission, POs and PSOs of School of ECE	
	BoS 3.9	Any other subject with the permission of the Chair Nil.	
	Resolution: 4	.3 Resolved to confirm the action taken report on the min	utes of its 3 rd BoS meeting held on 15 th
		he BoS members appreciated the new initiatives taken by	
BoS 4.4	To consider t	he Schemes and Syllabi of the undergraduate program B.E i	in Electronics & Communication and
	approve the s		
		Scheme approval of I to VIII Semester (2018-22)	
		Syllabus approval of I / II Semester, Basic Electronics for (2018-22)	Mechanical and Electrical stream course
		Scheme approval of III to VIII Semester (2017-21)	
		Syllabus approval of III to VIII Semester (2017-21)	
		Scheme approval of V to VIII Semester (2016-20)	
		Syllabus approval of V to VIII Semester (2016-20)	
		Modification of Scheme of VII and VIII Semester (2015-19)	
	100 100	Syllabus approval of VII and VIII Semester (2015-19)	
		Scheme approval: Scheme 2017-21 in Minor Program	
	j)	Scheme approval: Scheme 2016-20 in Minor Program	
	k)	Syllabus approval: Scheme 2016-20 in Minor Program	
	Diamatica		
		Based on the discussions following action items as agreed u	
		circulated to all the members on 7 th April 2018. Persons	
		ted the actions, which will be shared in the next BoS meeting for the section of	ng.
	The details of	r uiscussion are in Annexure 4.4	
	Action Item	No.1: Suggested new courses to strengthen basic conc	epts of Communication technology and
	programming		
	a)	Mobile and Wireless Communication	
	b)	Microwave and Antennas	· · · · · · · · · · · · · · · · · · ·

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	c)	Embedded Linux
	Action Ite	m No.2: Suggested new elective courses with Industry Collaboration for design and delivery
	a)	CMOS ASIC Design
	b)	Physical Design Analog
	c)	Embedded Intelligent Systems
		m No.3: Enhance programming skills: application to real world problem
	a)	Data structure applications lab
	b)	C programming (Diploma)
	a)	m No.4: Enhancing Research capabilities Research Experience for Undergraduates
	aj	Research Experience for ondergraduates
	Action Ite	m No.5: Enabling Industry Eco System
	a)	Institutional Research Project
	b)	Internship Training
	c)	Internship Project.
		n 4.4: Resolved to approve the Schemes and Syllabi of the undergraduate program B.E in Electronics
	& Commu	
	a) b)	Scheme approval of I to VIII Semester (2018-22) Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrical stream course
	57	(2018-22)
	c)	Scheme approval of III to VIII Semester (2017-21)
	d)	Syllabus approval of III to VIII Semester (2017-21)
	e)	Scheme approval of V to VIII Semester (2016-20)
	f)	Syllabus approval of V to VIII Semester (2016-20)
	g)	Modification of Scheme of VII and VIII Semester (2015-19)
	h)	Syllabus approval of VII and VIII Semester (2015-19)
	i)	Scheme approval: Scheme 2017-21 in Minor Program
	j)	Scheme approval: Scheme 2016-20 in Minor Program
	k)	Syllabus approval: Scheme 2016-20 in Minor Program
BoS 4.5	To conside	er the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the
		Scheme approval of I to IV Semester (2018-20)
		Syllabus approval of I/II Semester (2018-20)
	100 A 100	Modification of Scheme of III/IV Semester (2017-19)
		Syllabus approval of III/IV Semester (2017-19)
		n: Based on the discussions following action items as agreed upon by everyone were finalized and the
		e circulated to all the members on 7 th April 2018. Persons responsible for these action items have
		itiated the actions, which will be shared in the next BoS meeting.
	Action Ite	m No.1: Suggested new course to strengthen basic concepts and programming
		Advanced computer architecture and programming.
		Autosar and infotainment
	Action Ite	m No.2: Enabling Industry Eco System
		Project Phase –I/ Minor Project
	Resolutio	n 4.5: Resolved to the Schemes and Syllabi of the postgraduate program M.Tech in Digital
	Electronic	
		Scheme approval of I to IV Semester (2018-20) Syllabus approval of I/II Semester (2018-20)
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		KLE Technological University
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		1.1 Set set is a set of the set o



	c)	Modification of Scheme of III/IV Semester (2017-19)		
	d)	Syllabus approval of III/IV Semester (2017-19)		
BoS 4.6	To consi	der the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design and Embedded Systems		
	and app	rove the same.		
	a)	Scheme approval of I to IV Semester (2018-20)		
	b)	Syllabus approval of I/II Semester (2018-20)		
	c)	Modification of Scheme of III/IV Semester (2017-19)		
		Syllabus approval of III/IV Semester (2017-19)		
	Discussi	on: Based on the discussions following action items as agreed upon by everyone were finalized and the		
	same we	ere circulated to all the members on 7 th April 2018. Persons responsible for these action items have		
	already	initiated the actions, which will be shared in the next BoS meeting.		
	Action I	tem No.1: Suggested new core course to strengthen basic concepts and programming		
	1)	Machine learning		
	2)	Advanced computer architecture and programming.		
	Action I	tem No.2: Suggested new electives courses to build background with application perspective		
	1)	System simulation and modeling		
	2)	System on Chip		
	Resolut	ion 4.6: Resolved to the Schemes and Syllabi of the postgraduate program M.Tech in in VLSI Desig		
	and Embedded Systems			
	a)	Scheme approval of I to IV Semester (2018-20)		
	b)	Syllabus approval of I/II Semester (2018-20)		
	c)	Modification of Scheme of III/IV Semester (2017-19)		
	d)	Syllabus approval of III/IV Semester (2017-19)		
BoS 4.7	Questio	n Paper review		
	Discussi	on: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI		
	mappin	g was presented.		
BoS 4.8	Vision,	Mission, POs and PSOs of School of ECE		
	Discussi	on: The Vision, Mission, POs and PSOs of School of ECE were presented.		
BoS 4.9	Any oth	er subject with the permission of the Chair		
	Nil.			

The Chairperson thanked all the members for the fantastic contributions

Dr. Nalini Clyer Chairperson, BoS, SoECE

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Annexure 4.1

Discussion Item

Employers Feedback:

- Students should be able to evaluate the economic and financial performance of an engineering activity.
- Able to generate a diverse set of alternative design solutions for the given application.
- Enhance ability to identify and formulate problem in designing electronic system for real world applications.
- Enhance basic programming skills, to apply and realize real world problems.
- Understanding of professional engineering regulations, legislation and standards.
- Ability to apply the Code of Ethics and responsibilities.
- Enhance ability to apply design principles in the development of hardware and software systems of varying complexity, using state of art tools for the development of VLSI/Embedded/Communication systems.

Teachers Feedback (Pre-BoS MoM):

- Conceptual understanding of systems and their functional verification with hardware implementation is recommended.
- To focus on latest trends in communication technology.
- Formulation of application oriented examples
- Focus on problem solving using programming skills and use of online platform.
- Focus on embedded programing with and without optimization
- Collaborate with industry to get exposure to complete chip design flow, tools, courses etc.
- Lateral Entry Student's lack basic programming skills, to apply and realize real world problems and also rigor of practice.

Students Feedback:

- Focus on exposure to industries working in the domain of AI and ML.
- Hands on using EDA tools with Integrated Development Environment (IDEs).

Alumni Feedback:

- Knowledge of latest trends in mobile communication technology.
- Industry specific skills for employability.
- Depth of programming and analysis
- Recommended to include emulators for debugging and inclusion of Linux concepts for embedded systems.
- Recommended industry visits and internships.
- Need for Code profiling and introduction to online coding platform.
- Suggested to have projects reviews with industry experts and seek inputs.
- Recommended for Industry specific courses with collaboration and co-teaching.

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Annexure 4.4 **Discussion Item** Course BE (ECE) To understand how emerging technologies are changing the landscape, including 1. Mobile and Wireless enterprise personnel responsible for making cellular communications decisions(5G Communication-18EECC401 wireless communications), a wireless and mobile communication is added as a Added-New Course core course focusing on cellular communication methods, standards and cellular networks operation 2. To know in-depth know-how of microwave engineering and antennas for use in Microwave and Antennasfuture applications, like millimetre-wave 5G/beyond-5G wireless communications 18EECE411 . or automotive radar, that requires integrated antenna systems a course on Added-New Course Microwave and Antennas is introduced as an elective course. 3. To gain complete chip design flow knowledge CMOS ASIC design course in collaboration with industry is introduced with hands-on intensive. The course covers the Physical design flow of IC. Experiments explore complete digital design CMOS ASIC Design-18EECE420 flow of programmable ASIC through VLSI EDA tools. The course contents have been designed keeping in view the emerging trends in needs for skilled Added-New Course manpower. The course is highly practical oriented with 75 % of time spend on hands on practices by the candidates. Students work from design entry using Verilog code to GDSII file generation of an ASIC. 4. A course in the domain of analog vlsi in collaboration with industry to enhance teaching learning in Analog circuit design and layout, in turn build upon Physical Design Analog-18EECE419 competency through mini minor and capstone projects. The course is highly Added-New Course practical oriented with 75 % of time spend on hands on practices by the candidates with emphasis on hands-on using industry standard tools 5. Linux-based embedded systems are widely used in smartphones, in-vehicle infotainment systems, consumer electronics and for numerous industrial Embedded Linux-18EECE405 applications, resulting in the demand for qualified embedded system engineers with hands on in Linux. To make students acquire the practical skills involved in Added-New Course building an embedded Linux system, as well as debugging and profiling application an elective course on embedded Linux is introduced. 6. To get an industry like experience of deep learning technology on mobile devices Embedded Intelligent Systems-17EECE310 using Android, and enrich students understanding from concept development to Added-New Course model deployment. 7. A course on data structure application is introduced to enhance rigor in building Data structure applications labprogramming skills and to bridge the gap of applying the required data structures 18EECC210 and algorithmic skills to solve complex real world problems. Emphasis is on use of Added-New Course industry standard coding and online coding platform. C programming for Diploma-8. To impart programming skills for lateral entry students, a basic course on C 18EECF204 programming is introduced Added-New Course 9. A course on Institutional Research project (IRP) is introduced to provide students Institutional Research Projectan exposure for solving a real time projects involving current technologies using 17EECE491 Added-New Course KLETECH echo system as a live lab. 10. A course REU is introduced to enable students to take part in the research mission Research for UG-17EECE490 in their future career during and beyond their academia. It also helps them to experience and learn to identify, solve and evaluate Engineering solution for a real Added-New Course time problem.





11.	The minor in electronics is specially designed to enable the students from non- circuit discipline to develop their basic competency in electronic s and embedded systems relevant to their major disciplines, enhancing their carrier opportunities. Program engages students for 300 hours of experiential learning through 4 courses followed by a project.	Minor Programme Added-New Course
12.	Industry Internship training is introduced to enable students for the industry echo system while working on live projects.	Internship Training-18EECI493 Added-New Course
13.	Industry Internship project is introduced to enable students to work on live projects with industry skill sets and meeting deadlines	Internship Project- 18EECW494 Added-New Course
	M.Tech Digital Electronics	
1.	To evaluate the issues involved in choosing the proper instruction sets and to learn the concepts behind advanced pipelining techniques and to understand the qualitative and quantitative tradeoffs in the design of modern computer systems.	Advanced Compter Architecture and Programming-17EDEC801 Added-New Course
2.	Industry Internship training is introduced to enable students for the industry echo system while working on live projects.	17EDEW801 Revised
	M.Tech VLSI Design and Embedded Systems	
1.	To evaluate the issues involved in choosing the proper instruction sets and to learn the concepts behind advanced pipelining techniques and to understand the qualitative and quantitative tradeoffs in the design of modern computer systems.	Advanced Computer Architecture and Programming-17EVEC801 Added-New Course
2.	Industry Internship training is introduced to enable students for the industry echo system while working on live projects.	17EDEW801 Revised
3.	Introduction of machine learning course with Project-based learning.	Machine Learning-17EVEC708 Added-New Course
4.	Introduction of an elective course which helps the students to visualize the integration of multiple components of a system onto a single chip with interconnection structures (Buses and Networks) and accelerators.	System on Chip-17EVEE806 Added-New Course
5.	A course is introduced to give an insight on the concepts and classification of modelling and simulation and their architecture and application areas.	System Simulation and Modelling- 17EVEE804 Added-New Course

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Action Taken Report

4thBoard of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

7thApril 2018

KLE Technological University

(Established under Karnataka Act No.22, 2013)

VATSIL 80 031 XLE

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The following are the action items proposed during 3rd Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 15thApril 2017. The corresponding actions taken are also listed below.

tem No		Description	Action Taken
140	To welcor	ne the BoS Members and present department achievements	Noted
		ves and discussed about the inputs from all stake holders	Noted
	(Annexur		
BoS	(Annexan)		
4.1	Resolutio	n 3.1: The BoS members appreciated the work done towards	a.
		on of KLE Technological University as a State private	
		effective from 2015.	
		nd confirm the minutes of 3 rd BoS meeting held on 15 th April	Noted
BoS	2017	in commute minutes of 5 bos meeting held on 15 April	Noted
4.2		n 3.2: Minutes of the last meeting were read and confirmed	
4.2	by BoS.	1 3.2. Windles of the last meeting were read and commet	
		n the action taken report on the minutes of the previous	Noted
		eld on 15 th April 2017	Noted
BoS		n 3.3: BoS confirmed the action taken report on the	
4.3		of the previous meeting held on 15 th April 2017 and	
Def		ns were implemented.	
BoS 4.4		er the Schemes and Syllabi of the undergraduate program	The BoS members noted the progress of
4.4		tronics & Communication and approve the same.	the School and recommended action
	a)	Scheme of I to VIII Semester (2018-22) Batch	items and timeline.
	b)	Syllabus of I / II Semester, Basic Electronics for	
		Mechanical and Electrical stream course (2018-22) Batch	Action Item No.1: Suggested new course
	c)	Scheme of III to VIII Semester (2017-21) Batch	to strengthen basic concepts
	d)	Syllabus of III to VIII Semester (2017-21) Batch	Communication technology an
	e)	Scheme of V to VIII Semester (2016-20) Batch	programming
	f)	Syllabus of V to VIII Semester (2016-20) Batch	ATR:
	g)	Modification of Scheme of VII and VIII Semester (2015-	To understand how emerging technologi
		19) Batch	are changing the landscape responsible f
	h)	Syllabus of VII and VIII Semester (2015-19) Batch	making cellular communications decision
		a: Based on the discussions following action items as agreed	(5G wireless communications), a wirele
		everyone were finalized and the same were circulated to all	and mobile communication is added as
		pers on 7 th April 2018. Persons responsible for these action	core course focusing on cellulo
		e already initiated the actions, which will be shared in the	communication methods, standards an
	next BoS r		cellular networks operation.
	The detail	s of discussion are in Annexure 4.4	
			A course on Microwave and Antennas
	Action It	em No.1: Suggested new courses to strengthen basic	introduced as an elective course.
	concepts o	of Communication technology and programming	To know in-depth know-how of microway
	a)	Mobile and Wireless Communication	engineering and antennas for use in futur
	b)	Microwave and Antennas	applications, like millimeter-way
	c)	Embedded Linux	5G/beyond-5G wireless communications
			automotive radar, which require
	Action Iter	m No.2: Suggested new elective courses with Industry	integrated antenna systems.
		ion for design and delivery	
		, , ,	An elective course on Embedded Linux
	a)	CMOS ASIC Design	introduced to make students acquire th
	b)	Physical Design Analog	practical skills involved in building a
	c)	Embedded Intelligent Systems	Embedded Linux System, as well a
	0)	Embedded membern systems	debugging and profiling application.
	Action Iter	m No.3: Enhance programming skills: application to real	acougging and projimig approaction
	world prot		Action Item No.2: Suggested new elective
	a)	Data structure applications lab	<i>courses</i> with Industry Collaboration for
	b)	C programming (Diploma)	
	,	m No.4: Enhancing Research capabilities	design and delivery
	a)		470.
8	a)	Research Experience for Undergraduates	ATR:
	Action It-	n No Er Enghling Industry For Sustan	To gain complete chip design flo
		m No.5: Enabling Industry Eco System	knowledge CMOS ASIC design course
	a)	Institutional Research Project	collaboration with Sifive, Bangalore
		$\sqrt{\sqrt{2}}$	
		REGISTRAR	
		KLE Technological Un	



Internship Training introduced with hands-on to cover the b) Physical design flow of IC. Internship Project. c) A course in the domain of analog VLSI in collaboration with industry to enhance Resolution 4.4: Resolved to approve the Schemes and Syllabi of the teaching learning in Analog VLSI i.e. undergraduate program B.E in Electronics & Communication: Physical Design Analog and layout is Scheme of I to VIII Semester (2018-22) batch. a) introduced. Syllabus of I / II Semester, Basic Electronics for b) Mechanical and Electrical stream course (2018-22) To get an industry like experience of deep batch. learning technology on mobile devices Scheme of III to VIII Semester (2017-21) batch. c) using Android, and enrich students Syllabus of III to VIII Semester (2017-21) batch. d) understanding from concept development Scheme of V to VIII Semester (2016-20) batch. e) to model deployment a course Embedded f) Syllabus of V to VIII Semester (2016-20) batch. Intelligent System is introduced in Modification of Scheme of VII and VIII Semester (2015g) collaboration with Samsung R&D. 19). Syllabus of VII and VIII Semester (2015-19) batch. h) Scheme for Minor program in electronics for (2017-21) i) Action Item No.3: Enhance programming Batch skills: application to real world problem j) Scheme for Minor program in electronics for (2016-20) ATR: A course on Data Structure Batch application is introduced to enhance rigor k) Scheme for Minor program in electronics for (2016-20) in building programming skills and to Batch bridge the gap of applying the required data structures and algorithmic skills to solve complex real world problems. Emphasis is on use of industry standard coding and online coding platform. To impart programming skills for lateral entry(Diploma) students, a basic course on C programming is introduced. Action Item No.4: Enhancing Research capabilities. ATR: a course on Research Experience for Undergraduates is introduced to enable students to take part in the research mission in their future career during and beyond their academia Action Item No.5: Enabling Industry Eco System. ATR: A course on Institutional Research project (IRP) is introduced to provide students an exposure for solving a real involving current time projects technologies. Industry Internship training and project is introduced to enable students for the industry echo system while working on live projects. To consider the Schemes and Syllabi of the postgraduate program M. The BoS members noted the progress of BoS the School and recommended action 4.5 Tech in Digital Electronics and approve the same. items and timeline. a) Scheme of I to IV Semester (2018-20) batch. b) Syllabus of I/II Semester (2018-20) batch. Modification of Scheme of III/IV Semester (2017-19) Action Item No.1: Suggested new course c) to strengthen basic concepts and Syllabus of III/IV Semester (2017-19) batch. d)

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upon b the me items h next Bo Action and pro 1)	tion: Based on the discussions following action items as agreed y everyone were finalized and the same were circulated to all mbers on 15 th April 2017. Persons responsible for these action ave already initiated the actions, which will be shared in the oS meeting. Item No.1: Suggested new course to strengthen basic concepts ogramming Advanced computer architecture and programming.	programming ATR: A course on Advanced computer architecture and programming is introduced to give insights on the concepts of advance pipelining and trade off in design of modern computer systems for performance analysis.
Action 1) Resolut	Autosar and infotainment Item No.2: Enabling Industry Eco System Project Phase –I/ Minor Project tion 4.6: Resolved to the Schemes and Syllabi of the aduate program M. Tech in Digital Electronics: Scheme of I to IV Semester (2018-20) batch. Syllabus of I/II Semester (2018-20) batch. Modification of Scheme of III/IV Semester (2017-19) batch. Syllabus of III/IV Semester (2017-19) batch.	To acquire skills related to electrification, connectivity and infotainment to support the vehicle functions and A standardized interface for software components in the application layer, a course on Autosar and infotainment is introduced in collaboration with Bosch, Bangalore Action Item No.2: Enabling Industry Eco System Project Phase-I/ Minor Project is introduced to enable students for the industry echo system while working on live projects.
4.6 Tech in a) b) c) d) Discuss upon b the me items h next Bo Action	ider the Schemes and Syllabi of the postgraduate program M. VLSI Design and Embedded Systems and approve the same. Scheme of I to IV Semester (2018-20) Syllabus of I/II Semester (2018-20) Modification of Scheme of III/IV Semester (2017-19) Syllabus of III/IV Semester (2017-19) ion: Based on the discussions following action items as agreed y everyone were finalized and the same were circulated to all mbers on 15 th April 2017. Persons responsible for these action ave already initiated the actions, which will be shared in the S meeting.	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested new core course to strengthen basic concepts and programming ATR: Introduction of Machine learning course with Project-based learning which involves dynamic classroom approach in which students acquire a deeper knowledge through active investigation of real-world challenges and problems.
2) Action backgro 1)	Machine learning Advanced computer architecture and programming. Item No.2: Suggested new electives courses to build bund with application perspective System simulation and modeling System on Chip	To give insights on the concepts of advance pipelining and trade off in design of modern computer systems a course Advanced computer architecture and programming is introduced. Action Item No.2: Suggested new electives courses to build background with application perspective
	tion 4.7: Resolved to the Schemes and Syllabi of the aduate program M. Tech in in VLSI Design and Embedded s. Scheme of I to IV Semester (2018-20) batch Syllabus of I/II Semester (2018-20) batch Modification of Scheme of III/IV Semester (2017-19) Syllabus of III/IV Semester (2017-19) batch.	application perspective ATR: A course is introduced to give insights on the concepts and classification of modeling and simulation. To introduce students to the concepts of system integration on a single chip and their interconnections a course System on





		Chip is introduced.
BoS	Question Paper review	
4.7	Discussion: The Question Paper along with assessment patterns with	QP Pattern is incorporated in all the
	respect to Bloom's Levels and PO-PSO-PI mapping was presented.	courses.
	Resolution 4.7: Resolved to approve the Question Paper Pattern	
BoS	Vision, Mission, POs and PSOs of School of ECE	SoECE staff aligned to Vision, Mission, POs
4.8	Discussion: The Vision, Mission, POs and PSOs of School of ECE were presented.	and PSOs .
	Resolution 4.8: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE	
BoS	Any other subject with the permission of the Chair	
4.9	Nil.	

2 (Dr. Nalini Ciyer Chairperson, BoS, SoECE

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Minutes

5th Board of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

13th April 2019

KLE Technological University

(Established under Karnataka Act No.22, 2013)

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The following are the minutes of the Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 13th April 2019 at 9:30 am at the in Senate Hall of University. **The following members were present.**

SI No	Name	Designation	Position
1.	Dr. Nalini C.Iyer	Head of School, SoECE	Chairperson
2.	Dr. R M Bankar	Professor, SoECE	Member
3.	Dr. Uma Mudengudi	Professor, SoECE	Member
4.	Dr. Priyatamkumar	Professor, SoECE	Member
5.	Dr. Saroja S	Professor, SoECE	Member
6.	Prof. Ujwala Patil	Associate Professor, SoECE	Member
7.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member
8.	Dr. Chetan Parekh	Professor, Department of EC,IIIT Bangalore	Member
9.	Mr. Praveen B P	Samsung India, Bangalore	Member
10.	Mrs. Padmini Navalgund	RBEI, Bangalore	Member .
11.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductor	Member
12.	Mr. Shivakumar Turmari	Tessolve Semiconductors, Bangalore	Member
13.	Dr. Sujata S Kotabagi	Professor, SoECE	Member
14.	Dr. R B Shettar	Professor, SoECE	Member
15.	Prof. Suneeta V B	Professor, SoECE	
16.	Prof. P. C. Nissimgoudar	Associate Professor, SoECE	
17.	Prof. R. M. Shet	Assistant Professor, SoECE	
18.	1. UG: Pranav K		Student Members
	2. UG: Niveditha J	8	
	3. PG1 :Vijaylakshmi	5 S	
	4. PG2: Saiarpita		
	5. PhD: Suhas Shirol		

The following members have sought leave of absence:

SI No	Name	Designation	Position
1.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member
2.	Dr. Chetan Parekh	Professor, Department of EC,IIIT Bangalore	Member
3.	Mr. Shivakumar Turmari	Tessolve Semiconductors, Bangalore	Member
4.	Mr. Vivek Pawar	Sankalp Semiconductors, Hubballi	Member

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REGISTRAR



	Agenda					
SI No	Particulars	Page No.				
5.1	To welcome the BoS Members and present department achievements & initiatives					
5.2	To read and confirm the minutes of 4 th BoS meeting held on 7 th April 2018					
5.3	To confirm the action taken report on the minutes of the previous meeting held on 7 th April 2018					
5.4	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics &					
	Communication and approve the same.					
	a) Scheme approval of I to VIII Semester (2019-23)					
	b) Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrical					
	stream course (2019-23)					
	c) Scheme approval of III to VIII Semester (2018-22)					
	d) Syllabus approval of III to VIII Semester (2018-22)					
	e) Scheme approval of V to VIII Semester (2017-21)	•				
	f) Syllabus approval of V to VIII Semester (2017-21)					
	g) Scheme approval of VII and VIII Semester (2016-20)					
	h) Syllabus approval of V and VIII Semester (2016-20)					
	i) Scheme approval: Scheme 2018-22 in Minor Program					
	j) Scheme approval: Scheme 2017-21 in Minor Program					
	k) Syllabus approval: Scheme 2017-21 in Minor Program					
5.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and					
	approve the same.					
	a) Scheme approval of I to IV Semester (2019-21)					
	b) Syllabus approval of I/II Semester (2019-21)					
	c) Modification of Scheme of III/IV Semester (2018-20)					
	d) Syllabus approval of III/IV Semester (2018-20)					
5.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded					
	Systems and approve the same.					
	a) Scheme approval of I to IV Semester (2019-21)					
	b) Syllabus approval of I/II Semester (2019-21)					
	c) Modification of Scheme of III/IV Semester (2018-20)					
	d) Syllabus approval of III/IV Semester (2018-20)					
5.7	Question Paper review and Discussion on attainment of POs and PSOs					
5.8	Vision, Mission, POs, PSOs of School of ECE and CAM and PAM					
5.9	Any other matter for discussion with the permission of the chair					

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BoS 5.1 To welcome the BoS Members and present department achievements & initiatives

Resolution 4.1: The BoS members appreciated the initiatives of SoECE and lauded its achievements.

	The following are the minutes of the Board of Studies meeting of SoECE, KLE Technological University, Hubb which was held on 7 th April 2018 at 10:30 am at the Senate Hall of the University. The following members were present .					
	BoS 4.2	 BoS To read and confirm the minutes of 4th BoS meeting held on 7th April 2018 4.2 				
		The following are the minutes of the Board of Studies meeting of SoECE, KLE Technological University, Hubballi, which was held on 7 th April 2018 at 10:30 am at the ED Studio of the School. The following members were present.				
		SI No	Name	Designation	Position	
		1.	Dr. Nalini C.lyer	Head of School, SoECE	Chairperson	
		2.	Dr. R M Bankar	Professor, SoECE	Member	
		3.	Dr. Uma Mudengudi	Professor, SoECE	Member	
		4.	Dr. Priyatamkumar	Professor, SoECE	Member	
		5.	Dr. Anil Nandi	Professor, SoECE	Member	
		6.	Dr. Saroja S	Professor, SoECE	Member	
		7.	Prof. Ujwala Patil	Associate Professor, SoECE	Member	
		8.	Prof. Sanjay Eligar	Assistant Professor, SoECE	Member	
		9.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member	
		10.	Dr. Chetan Parekh	Professor, Department of EC,IIIT Bangalore	Member	
		11.	Dr. Lokesh Boregouda	Head Research, Samsung India, Bangalore	Member	
		12.	Dr. P Subbanna Bhat	Professor Emirates, KLE Tech	Member	
		13.	Mr. Vivek G Pawar	Founder & CEO, Sankalp Semiconductor	Member	
		14.	Mrs. Padmini Navalgund	RBEI, Bangalore	Member	
		15.	Mr. Shiva Turmuri	Analog Devices, Bangalore	Member	
		16.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductor	Member	
		17.	Dr. Sujata S Kotabagi	Professor, SoECE	Member	
		18.	Dr. R B Shettar	Professor, SoECE	Member	
		19.	Prof. Suneeta V B	Professor, SoECE	Member	
		20.	Prof. P. C. Nissimgoudar	Associate Professor, SoECE	Member	
		21.	Prof, Rohini Hongal	Associate Professor, SoECE	Member	
		22.	Prof. R. M. Shet	Assistant Professor, SoECE	Member	
		23.	1. UG: Rohan D 2. UG: Sheetal 3. PG1 :Ravi 4. PG2: Pratima		Student Members	
			5. PhD: Suhas Shirol			
	Item			Description		
	BoS	dis	scussed about the inputs	mbers and present department achievements from all stake holders (Annexure 5.1) nembers appreciated the work done towards re		

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BoS 4.2	To read and confirm the minutes of 4 th BoS meeting held on 7 th April 2018
B05 4.2	Resolution 4.2: Minutes of the last meeting were read and confirmed by BoS.
	To confirm the action taken report on the minutes of the previous meeting held on 7 th April
BoS 4.3	2018
005 4.5	Resolution 4.3: BoS confirmed the action taken report on the minutes of the previous
	meeting held on 7 th April 2018 and suggestions were implemented.
BoS 4.4	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics &
	Communication and approve the same.
	a) Scheme of I to VIII Semester (2018-22) Batch
	b) Syllabus of I / II Semester, Basic Electronics for Mechanical and Electrical stream
	course (2018-22) Batch
	c) Scheme of III to VIII Semester (2017-21) Batch
	d) Syllabus of III to VIII Semester (2017-21) Batch
	 e) Scheme of V to VIII Semester (2016-20) Batch f) Syllabus of V to VIII Semester (2016-20) Batch
	 g) Modification of Scheme of VII and VIII Semester (2015-19) Batch h) Syllabus of VII and VIII Semester (2015-19) Batch
	 h) Syllabus of VII and VIII Semester (2015-19) Batch Discussion: Based on the discussions following action items as agreed upon by everyone were
	finalized and the same were circulated to all the members on 7 th April 2018. Persons
	responsible for these action items have already initiated the actions, which will be shared in
	the next BoS meeting.
	The details of discussion are in Annexure 4.4
	Action Item No.1: Suggested new courses to strengthen basic concepts of Communication
	technology and programming
	a) Mobile and Wireless Communication
	b) Microwave and Antennas
	c) Embedded Linux
	Action Item No.2: Suggested new elective courses with Industry Collaboration for design and
	delivery
	a) CMOS ASIC Design
	b) Physical Design Analog
	c) Embedded Intelligent Systems
	Action Item No.3: Enhance programming skills: application to real world problem
	a) Data structure applications lab
	b) C programming (Diploma)
	Action Item No.4: Enhancing Research capabilities
	a) Research Experience for Undergraduates
	Action Item No.5: Enabling Industry Eco System
	a) Institutional Research Project
	b) Internship Training
	c) Internship Project.
	Resolution 4.4: Resolved to approve the Schemes and Syllabi of the undergraduate program
	B.E in Electronics & Communication:
	a) Scheme of I to VIII Semester (2018-22) batch.
	b) Syllabus of I / II Semester, Basic Electronics for Mechanical and Electrical
	stream course (2018-22) batch.
	c) Scheme of III to VIII Semester (2017-21) batch.
	d) Syllabus of III to VIII Semester (2017-21) batch.
	e) Scheme of V to VIII Semester (2016-20) batch.

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×	 f) Syllabus of V to VIII Semester (2016-20) batch. g) Modification of Scheme of VII and VIII Semester (2015-19). h) Syllabus of VII and VIII Semester (2015-19) batch. i) Scheme for Minor program in electronics for (2017-21) Batch j) Scheme for Minor program in electronics for (2016-20) Batch k) Scheme for Minor program in electronics for (2016-20) Batch
BoS 4.5	 To consider the Schemes and Syllabi of the postgraduate program M. Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2018-20) batch. b) Syllabus of I/II Semester (2018-20) batch. c) Modification of Scheme of III/IV Semester (2017-19) d) Syllabus of III/IV Semester (2017-19) batch. Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 15th April 2017. Personaresponsible for these action items have already initiated the actions, which will be shared in the next BoS meeting. Action Item No.1: Suggested new course to strengthen basic concepts and programming Advanced computer architecture and programming. Autosar and infotainment
	 Action Item No.2: Enabling Industry Eco System Project Phase –I/ Minor Project Resolution 4.6: Resolved to the Schemes and Syllabi of the postgraduate program M. Tech in Digital Electronics: Scheme of I to IV Semester (2018-20) batch. Syllabus of I/II Semester (2018-20) batch. Modification of Scheme of III/IV Semester (2017-19) batch. Syllabus of III/IV Semester (2017-19) batch.
BoS 4.6	 To consider the Schemes and Syllabi of the postgraduate program M. Tech in VLSI Design and Embedded Systems and approve the same. a) Scheme of I to IV Semester (2018-20) b) Syllabus of I/II Semester (2018-20) c) Modification of Scheme of III/IV Semester (2017-19) d) Syllabus of III/IV Semester (2017-19) Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 15th April 2017. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting.
	 Action Item No.1: Suggested new core course to strengthen basic concepts and programming Machine learning Advanced computer architecture and programming. Action Item No.2: Suggested new electives courses to build background with application perspective System simulation and modeling

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		Resolution 4.6: Resolved to the Schemes and Syllab	i of the postgraduate program M. Tech in				
		in VLSI Design and Embedded Systems.					
		a) Scheme of I to IV Semester (2018-20) batc	1				
		b) Syllabus of I/II Semester (2018-20) batch					
	8	c) Modification of Scheme of III/IV Semester	(2017-19)				
		d) Syllabus of III/IV Semester (2017-19) batch					
	BoS 4						
		Discussion: The Question Paper along with assessme	nt patterns with respect to Bloom's				
		Levels and PO-PSO-PI mapping was presented.					
		Resolution 4.7: Resolved to approve the Question P	aper Pattern				
	BoS 4	.8 Vision, Mission, POs and PSOs of School of ECE					
		Discussion: The Vision, Mission, POs and PSOs of Sch	pol of ECE were presented.				
		Resolution 4.8: Resolved to approve the Vision, Miss	ion, POs and PSOs of School of FCF				
	BoS 4	.9 Any other subject with the permission of the Chair					
		Nil.					
			6				
	Resolution	n 5.2: Resolved to confirm the minutes of its 4 th BoS meet	ing held on 7th April 2019				
oS 5.3	To confirm	the action taken report on the minutes of the previous m	eting held on 7 th April 2019				
		in the minutes of the previous m	April 2018				
	Resolution	1: 4.3 Resolved to confirm the action taken report on the	Resolution: 4.3 Resolved to confirm the action taken report on the minutes of its 4 th BoS meeting held on April 2018. The BoS members approximated the action taken report on the minutes of its 4 th .				
	Resolution	1: 4.3 Resolved to confirm the action taken report on the	minutes of its 4 th BoS meeting held on 3				
	Resolution	1: 4.3 Resolved to confirm the action taken report on the The BoS members appreciated the new initiatives taken	minutes of its 4 th BoS meeting held on 3 by SoECE.				
	Resolution	1: 4.3 Resolved to confirm the action taken report on the The BoS members appreciated the new initiatives taken	by SoECE.				
	April 2018	1: 4.3 Resolved to confirm the action taken report on the	minutes of its 4 th BoS meeting held on 5 by SoECE. Action Taken				
	April 2018	n: 4.3 Resolved to confirm the action taken report on the The BoS members appreciated the new initiatives taken Description	by SoECE.				
	April 2018	1: 4.3 Resolved to confirm the action taken report on the . The BoS members appreciated the new initiatives taken Description To welcome the BoS Members and present departme	Action Taken				
	Resolution April 2018 Item No	1: 4.3 Resolved to confirm the action taken report on the The BoS members appreciated the new initiatives taken Description To welcome the BoS Members and present departme achievements & initiatives and discussed about the input	Action Taken				
	April 2018	1: 4.3 Resolved to confirm the action taken report on the action taken report on the b. The BoS members appreciated the new initiatives taken Description To welcome the BoS Members and present departme achievements & initiatives and discussed about the input from all stake holders (Annexure 5.1)	hy SoECE. Action Taken nt Noted ts				
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	Resolution April 2018 Item No	12 4.3 Resolved to confirm the action taken report on the The BoS members appreciated the new initiatives taken Description To welcome the BoS Members and present departme achievements & initiatives and discussed about the inpu from all stake holders (Annexure 5.1) Resolution 4.1: The BoS members appreciated the wo done towards recognition of KLE Technological Universi	hy SoECE. Action Taken				
	Resolution April 2018 Item No	1: 4.3 Resolved to confirm the action taken report on the 5. The BoS members appreciated the new initiatives taken Description To welcome the BoS Members and present departme achievements & initiatives and discussed about the inpu from all stake holders (Annexure 5.1) Resolution 4.1: The BoS members appreciated the wo done towards recognition of KLE Technological Universi as a State private University effective from 2015.	by SoECE. Action Taken nt Noted ts Kk Y				
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	Resolution April 2018 Item No BoS 4.1 BoS 4.2 BoS 4.3	1: 4.3 Resolved to confirm the action taken report on the The BoS members appreciated the new initiatives taken Description To welcome the BoS Members and present departme achievements & initiatives and discussed about the input from all stake holders (Annexure 5.1) Resolution 4.1: The BoS members appreciated the wo done towards recognition of KLE Technological Universi as a State private University effective from 2015. To read and confirm the minutes of 4 th BoS meeting he on 7 th April 2018 Resolution 4.2: Minutes of the last meeting were reading and confirmed by BoS. To confirm the action taken report on the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 7 th April 2018 To consider the Schemes and Syllabi of the undergraduat program B.E in Electronics & Communication and approve the same.	by SoECE. Action Taken Action Taken Noted Noted Noted Noted Noted Noted The BoS members noted the progress of the School and recommended action items and				
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	Resolution April 2018 Item No BoS 4.1 BoS 4.2 BoS 4.3	1: 4.3 Resolved to confirm the action taken report on the 2: The BoS members appreciated the new initiatives taken 2: The BoS members appreciated the new initiatives taken 3: Description To welcome the BoS Members and present departme achievements & initiatives and discussed about the input from all stake holders (Annexure 5.1) Resolution 4.1: The BoS members appreciated the wo done towards recognition of KLE Technological Universi as a State private University effective from 2015. To read and confirm the minutes of 4 th BoS meeting he on 7 th April 2018 Resolution 4.2: Minutes of the last meeting were reading confirmed by BoS. To confirm the action taken report on the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 O consider the Schemes and Syllabi of the undergraduat program B.E in Electronics & Communication and approve the same. a) Scheme of I to VIII Se	Action Taken Action Taken Int Noted Its Its Its				
	Resolution April 2018 Item No BoS 4.1 BoS 4.2 BoS 4.3	A: 3 Resolved to confirm the action taken report on the confirm the BoS members appreciated the new initiatives taken Description To welcome the BoS Members and present departme achievements & initiatives and discussed about the input from all stake holders (Annexure 5.1) Resolution 4.1: The BoS members appreciated the wo done towards recognition of KLE Technological Universit as a State private University effective from 2015. To read and confirm the minutes of 4 th BoS meeting he on 7 th April 2018 Resolution 4.2: Minutes of the last meeting were read and confirme the action taken report on the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Oconsider the Schemes and Syllabi of the undergraduat program B.E in Electronics & Communication and approve the same. a) Scheme of I to VIII Semester (2018-22) Batch Syllab	Action Taken Action Taken Int Noted Its Its Its				
	Resolution April 2018 Item No BoS 4.1 BoS 4.2 BoS 4.3	1: 4.3 Resolved to confirm the action taken report on the 2: The BoS members appreciated the new initiatives taken 2: The BoS members appreciated the new initiatives taken 3: Description To welcome the BoS Members and present departme achievements & initiatives and discussed about the input from all stake holders (Annexure 5.1) Resolution 4.1: The BoS members appreciated the wo done towards recognition of KLE Technological Universi as a State private University effective from 2015. To read and confirm the minutes of 4 th BoS meeting he on 7 th April 2018 Resolution 4.2: Minutes of the last meeting were reading confirmed by BoS. To confirm the action taken report on the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 Resolution 4.3: BoS confirmed the action taken report of the minutes of the previous meeting held on 7 th April 2018 O consider the Schemes and Syllabi of the undergraduat program B.E in Electronics & Communication and approve the same. a) Scheme of I to VIII Se	Action Taken nt Noted d Noted e Noted n I il e e The BoS members noted the progress of the School and recommended action items and timeline. s Action Item No.1: Suggested new courses to strengthen basic concepts of Communication technology and programming n ATR: To understand how emerging technologies are changing the				

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	g)	Modification of Scheme of VII and VII	
	h)	Semester (2015-19) Batch Syllabus of VII and VIII Semester (2015-19)	(5G wireless communications), a wireless and mobile communication
	,	Batch	is added as a core course focusing on
	Discussion	n: Based on the discussions following action	cellular communication methods,
		agreed upon by everyone were finalized and the	
×	same wer	e circulated to all the members on 7 th April 2018.	operation.
	Persons r	esponsible for these action items have already	
	initiated t	he actions, which will be shared in the next BoS	A course on Microwave and
	meeting.		Antennas is introduced as an elective
	The detail	s of discussion are in Annexure 4.4	course.
			To know in-depth know-how of
		m No.1: Suggested new courses to strengthen	
		ncepts of Communication technology and	
	programn a)		millimeter-wave 5G/beyond-5G
	b)	Mobile and Wireless Communication Microwave and Antennas	wireless communications or
	c)	Embedded Linux	automotive radar, which requires
	٠,		integrated antenna systems.
	Action Ite	m No.2: Suggested new elective courses with	An elective course on Embedded
		ollaboration for design and delivery	Linux is introduced to make students
	industry c	endeerdeer jor design and denvery	acquire the practical skills involved in
	a)	CMOS ASIC Design	building an Embedded Linux System,
	b)	Physical Design Analog	as well as debugging and profiling
	c)	Embedded Intelligent Systems	application.
		tem No.3: Enhance programming skills:	Action Item No.2: Suggested new
		n to real world problem	elective courses with Industry
	a) b)	Data structure applications lab	Collaboration for design and delivery
		C programming (Diploma) m No.4: Enhancing Research capabilities	
	b)	Research Experience for Undergraduates	ATR:
	~/	Research experience for ondergraduates	To gain complete chip design flow
	Action Iter	m No.5: Enabling Industry Eco System	knowledge CMOS ASIC design course
	a)	Institutional Research Project	in collaboration with Sifive, Bangalore is introduced with hands-on to cover
	b)	Internship Training	the Physical design flow of IC.
	c)	Internship Project.	A course in the domain of analog VLSI
			in collaboration with industry to
		4.4: Resolved to approve the Schemes and	enhance teaching learning in Analog
	Syllabi of t	he undergraduate program B.E in Electronics &	VLSI i.e. Physical Design Analog and
	Communic		layout is introduced.
	a)	Scheme of I to VIII Semester (2018-22)	
		batch.	To get an industry like experience of
	b)	Syllabus of I / II Semester, Basic Electronics	deep learning technology on mobile
		for Mechanical and Electrical stream course	devices using Android, and enrich
		(2018-22) batch.	students understanding from concept
	c)	Scheme of III to VIII Semester (2017-21) batch.	development to model deployment a
	d)		course Embedded Intelligent System
	d)	Syllabus of III to VIII Semester (2017-21) batch.	is introduced in collaboration with
	e)	Scheme of V to VIII Semester (2016-20)	Samsung R&D.
		batch.	
	f)	Syllabus of V to VIII Semester (2016-20)	Action them the 2 5
	.,	batch.	Action Item No.3: Enhance
	g)	Modification of Scheme of VII and VIII	programming skills: application to
	5,	Semester (2015-19)	real world problem

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Svllabus of VII and VIII Semester (2015-19) h) ATR: A course on Data Structure batch. application is introduced to enhance i) Scheme for Minor program in electronics for rigor in building programming skills (2017-21) Batch and to bridge the gap of applying the Scheme for Minor program in electronics for j) required data structures and (2016-20) Batch algorithmic skills to solve complex k) Scheme for Minor program in electronics for real world problems. Emphasis is on (2016-20) Batch use of industry standard coding and online coding platform. To impart programming skills for lateral entry(Diploma) students, a basic course on C programming is introduced. Action Item No.4: Enhancing Research capabilities. ATR: a course on Research Experience for Undergraduates is introduced to enable students to take part in the research mission in their future career during and beyond their academia Action Item No.5: Enabling Industry Eco System. ATR: A course on Institutional Research project (IRP) is introduced to provide students an exposure for solving a real time projects involving current technologies. Industry Internship training and project is introduced to enable students for the industry echo system while working on live projects. **BoS 4.5** To consider the Schemes and Syllabi of the postgraduate BoS members noted The the program M. Tech in Digital Electronics and approve the progress of the School and same. recommended action items and Scheme of I to IV Semester (2018-20) batch. a) timeline. b) Syllabus of I/II Semester (2018-20) batch. c) Modification of Scheme of III/IV Semester (2017-Action Item No.1: Suggested new 19) course to strengthen basic concepts Syllabus of III/IV Semester (2017-19) batch. d) and programming Discussion: Based on the discussions following action ATR: items as agreed upon by everyone were finalized and the A course on Advanced computer same were circulated to all the members on 15th April architecture and programming is 2017. Persons responsible for these action items have introduced to give insights on the already initiated the actions, which will be shared in the concepts of advance pipelining and next BoS meeting. trade off in design of modern computer systems for performance Action Item No.1: Suggested new course to strengthen analysis. basic concepts and programming 1) Advanced computer architecture and programming. То acquire skills related to 2) Autosar and infotainment electrification, connectivity and infotainment to support the vehicle Action Item No.2: Enabling Industry Eco System functions and A standardized

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	 Project Phase –I/ Minor Project Resolution 4.5: Resolved to the Schemes and Syllabi of the postgraduate program M. Tech in Digital Electronics: a) Scheme of I to IV Semester (2018-20) batch. b) Syllabus of I/II Semester (2018-20) batch. c) Modification of Scheme of III/IV Semester (2017-19) batch. d) Syllabus of III/IV Semester (2017-19) batch. 	interface for software components in the application layer, a course on Autosar and infotainment is introduced in collaboration with Bosch, Bangalore Action Item No.2: Enabling Industry Eco System Project Phase-I/ Minor Project is introduced to enable students for the industry echo system while working on live projects.
BoS 4.6	 To consider the Schemes and Syllabi of the postgraduate program M. Tech in VLSI Design and Embedded Systems and approve the same. a) Scheme of I to IV Semester (2018-20) b) Syllabus of I/II Semester (2018-20) c) Modification of Scheme of III/IV Semester (2017-19) Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 15th April 2017. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting. Action Item No.1: Suggested new core course to strengthen basic concepts and programming 1) Machine learning 2) Advanced computer architecture and programming. Advanced computer architecture and programming. Action Item No.2: Suggested new electives courses to build background with application perspective System simulation and modeling System on Chip Resolution 4.6: Resolved to the Schemes and Syllabi of the postgraduate program M. Tech in in VLSI Design and Embedded Systems. a) Scheme of I to IV Semester (2018-20) batch b) Syllabus of I/II Semester (2018-20) batch c) Modification of Scheme of III/IV Semester (2017-19) 	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested new core course to strengthen basic concepts and programming ATR: Introduction of Machine learning course with Project-based learning which involves dynamic classroom approach in which students acquire a deeper knowledge through active investigation of real-world challenges and problems. To give insights on the concepts of advance pipelining and trade off in design of modern computer systems a course Advanced computer architecture and programming is introduced. Action Item No.2: Suggested new electives courses to build background with application perspective ATR: A course is introduced to give insights on the concepts and classification of modeling and simulation. To introduce students to the concepts of system integration on a single chip and their interconnections a course System on Chip is introduced.
BoS 4.7	d) Syllabus of III/IV Semester (2017-19) batch. Question Paper review Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI mapping was presented.	QP Pattern is incorporated in all the courses.
	Resolution 4.7: Resolved to approve the Question Paper Pattern	

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	BoS 4.8	Vision, Mission, POs and PSOs of School of ECE	SoECE staff aligned to Vision, Mission			
		Discussion: The Vision, Mission, POs and PSOs of School of ECE were presented.	POs and PSOs .			
		Resolution 4.8: Resolved to approve the Vision, Mission, POs and PSOs of School of ECE				
	BoS 4.9	Any other subject with the permission of the Chair Nil.				
	Pecelutian					
	April 2018	a: 5.3 Resolved to confirm the action taken report on the min . The BoS members appreciated the new initiatives taken by	utes of its 4 th BoS meeting held on 7 th SoECE			
BoS 5.4	To conside approve th	r the Schemes and Syllabi of the undergraduate program B.E in	n Electronics & Communication and			
	a)	Scheme approval of I to VIII Semester (2019-23)				
	b)	Syllabus approval of I / II Semester, Basic Electronics for N (2019-23)	Mechanical and Electrical stream cour			
	c)	Scheme approval of III to VIII Semester (2018-22)				
	d)	Syllabus approval of III to VIII Semester (2018-22)				
	e)	Scheme approval of V to VIII Semester (2017-21)				
	f)	Syllabus approval of V to VIII Semester (2017-21)				
	g)	Scheme approval of VII and VIII Semester (2016-20)				
	h)	Syllabus approval of V and VIII Semester (2016-20)				
	i)	Scheme approval: Scheme 2018-22 in Minor Program				
	j)	Scheme approval: Scheme 2017-21 in Minor Program				
	k)	Syllabus approval: Scheme 2017-21 in Minor Program				
	Discussion	Based on the discussions following action items as agreed up	on by everyone were finalized and the			
	same were circulated to all the members on 13 th April 2019. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting.					
	Action Item No.1: New courses added: OOPS using C++, Biosensor					
	Action Item No.2: Revised courses : CMOS VLSI Circuits, Internet of Things, Information Theory and coding and					
	Signals and System					
	Decelution					
2	& Commun	5.4: Resolved to approve the Schemes and Syllabi of the und ication:	ergraduate program B.E in Electronics			
2	& Commun	ication:	ergraduate program B.E in Electronics			
	& Commun a) b)	5.4: Resolved to approve the Schemes and Syllabi of the und ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23)				
	a)	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23)				
	& Commun a) b)	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22)				
	a) b) c)	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22)				
	a) b) c) d)	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V to VIII Semester (2017-21)				
	& Commun a) b) c) d) e) f) g)	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V to VIII Semester (2017-21) Scheme approval of VII and VIII Semester (2016-20)				
	& Commun a) b) c) d) e) f) g) h)	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V to VIII Semester (2017-21) Scheme approval of VII and VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20)				
	& Commun a) b) c) d) e) f) g) h) i)	Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V to VIII Semester (2017-21) Scheme approval of V III and VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20) Scheme approval: Scheme 2018-22 in Minor Program				
	& Commun a) b) c) d) e) f) g) h)	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V to VIII Semester (2017-21) Scheme approval of VII and VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20)				
3oS 5.5	& Commun a) b) c) d) e) f) g) h) i) j)	Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V to VIII Semester (2017-21) Scheme approval of V III and VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20) Scheme approval: Scheme 2018-22 in Minor Program	lechanical and Electrical stream cours			
3oS 5.5	& Commun a) b) c) d) e) f) g) h) i) j) To consider same.	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V to VIII Semester (2017-21) Scheme approval of V to VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20) Scheme approval: Scheme 2018-22 in Minor Program Scheme approval: Scheme 2017-21 in Minor Program	lechanical and Electrical stream cours			
BoS 5.5	<pre>& Commun a) b) c) d) e) f) g) h) i) j) To consider same. a) Sci</pre>	Ication: Scheme approval of I to VIII Semester (2019-23) Syllabus approval of I / II Semester, Basic Electronics for M (2019-23) Scheme approval of III to VIII Semester (2018-22) Syllabus approval of III to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V to VIII Semester (2017-21) Scheme approval of V III and VIII Semester (2016-20) Syllabus approval of V and VIII Semester (2016-20) Scheme approval: Scheme 2018-22 in Minor Program Scheme approval: Scheme 2017-21 in Minor Program	lechanical and Electrical stream cours			

REGISTRAR KLE Technological University HUBBALLI-580 031



	d) Syllabus approval of III/IV Semester (2018-20)
	Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the
	same were circulated to all the members on 13th April 2019. Persons responsible for these action items have
	already initiated the actions, which will be shared in the next BoS meeting.
	Action Item No.1: Suggested new core courses to strengthen basic concepts and programming
	1. Automotive electronics and Communication
	Resolution 5.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M. Tech
	in Digital Electronics subjected to implementation of action points listed above.
	a) Scheme approval of I to IV Semester (2019-21)
	b) Syllabus approval of I/II Semester (2019-21)
	c) Modification of Scheme of III/IV Semester (2018-20)
	d) Syllabus approval of III/IV Semester (2018-20)
BoS 5.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design and Embedded Systems
	and approve the same.
	a) Scheme approval of I to IV Semester (2019-21)
	b) Syllabus approval of I/II Semester (2019-21)
	c) Modification of Scheme of III/IV Semester (2018-20)
	d) Syllabus approval of III/IV Semester (2018-20)
	Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the
	same were circulated to all the members on 13th April 2019. Persons responsible for these action items have
	already initiated the actions, which will be shared in the next BoS meeting.
	, and the second s
	Action Item No.1: Suggested new core courses to strengthen basic concepts and programming
	1. Automotive electronics and Communication
	2. AUTOSAR and Infotainment
	Resolution 5.6: Resolved to the Schemes and Syllabi of the postgraduate program M. Tech in in VLSI Design
	and Embedded Systems subjected to implementation of action points listed above.
	a) Scheme approval of I to IV Semester (2019-21)
	b) Syllabus approval of I/II Semester (2019-21)
	d) Syllabus approval of III/IV Semester (2018-20)
BoS 5.7	Question Paper review
505 5.7	
	Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI
BoS 5.8	mapping were presented.
0.C CUU	Vision, Mission, POs, PSOs, CAM and PAM of School of ECE
Decro	Discussion: The Vision, Mission, POs, PSOs, CAM and PAM of School of ECE were presented.
BoS 5.9	Any other subject with the permission of the Chair
	Nil.

The Chairperson thanked all the members for the fantastic contributions

NLO Dr.Nalini C Iyer Chairperson, BoS, SoECE

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Annexure 5.1

Discussion Item

Employers Feedback:

- Able to generate a diverse set of alternative design solutions for the given application.
- Enhance ability to identify and formulate problem in designing electronic system for real world applications.
- Enhance basic programming skills, to apply and realize real world problems.

Teachers Feedback (Pre-BoS MoM):

- To focus on latest technological trends and development.
- Formulation of application oriented examples
- Focus on problem solving using programming skills and use of online platform.

Students Feedback:

- Focus on real time applications.
- Hands on using EDA tools with Integrated Development Environment (IDEs).

Alumni Feedback:

- Industry Specific Skills for employability.
- Depth of programming and analysis.

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	Discussion Item	Course
	BE (ECE)	
under	ated theory and laboratory approach is adopted to bridge the gap between standing theoretical concepts and realiasing the same using EDA Tools with ate credits for theory and labs.	CMOS-19EECC301 Revised- Delivery
	rse on Object Oriented Programming to aid the students for heterogeneous uting in real time context is introduced using C++.	OOPS using C++-19EECE302 Added- New course
respor	nphasis the basic principles of bio sensing in terms of photonic/ optical nses and demonstrate the same using simulation and modelling tools, a e on biosensor is introduced with experiential learning.	Biosensor-19EECE416 Added-New course
comm and sy	ork of physical objects that are embedded with sensors, software, and other unication protocols for connecting and exchanging data with other devices stems over the internet is introduced in the course with separate credits for project.	Internet of things-19EECE401 Revised- Delivery
techni simula separa	roduce the principles and applications of information theory with coding ques for performance analysis of communication channel, modelling and tion using MATLAB/Simulink followed by a course project is introduced with the credits for course project.	Information Theory and coding 19EECE402 Revised- Delivery
domai Mathe	xt based learning for the most fundamental course in communication n is introduced in the course Signals and Systems through Co-teaching. matical concepts are mapped with physical interpretation of signal ssing towards better learning.	Signals and Systems19EECC202 Revised- Delivery
	M.Tech Digital Electronics	
1.Electroni engineers t	cs and technology advances are changing the automotive industry forcing to acquire new skills in connectivity, electrification and infotainment	Automotive Electronics and Communication-19EDEC701, Added- New course
2.Electroni engineers t	cs and technology advances are changing the automotive industry forcing to acquire new skills in connectivity, electrification and infotainment .	AUTOSAR and Infotainment- 19EDEE702 Added- New course
3. IoT- A ur implication	nique technology transition that is impacting human lives and will have huge s for business of logistics.	Internet of Things-19EDEE703 Added- New course
	M.Tech VLSI Design and Embedded Systems	
1. Electro engine	onics and technology advances are changing the automotive industry forcing ers to acquire new skills in connectivity, electrification and infotainment .	Automotive Electronics and Communication-19EVEC701 Added- New course
2. Electro engine	nics and technology advances are changing the automotive industry forcing ers to acquire new skills in connectivity, electrification and infotainment .	AUTOSAR and Infotainment- 19EVEE707 Added- New course

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Action Taken Report

5th Board of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

13th April 2019

KLE Technological University

(Established under Karnataka Act No.22, 2013)

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The following are the action items proposed during 5th Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 13th April 2019. The corresponding actions taken are also listed below.

ltem No	Description	Action Taken
	To welcome the BoS Members and present department	Noted
	achievements & initiatives and discussed about the inputs from all	
	stake holders (Annexure 5.1)	
BoS 5.1	Resolution 4.1: The BoS members appreciated the work done	
	towards recognition of KLE Technological University as a State	
	private University effective from 2015.	
	To read and confirm the minutes of 4 th BoS meeting held on 7 th	Noted
	April 2018	Noted
BoS 5.2	Resolution 4.2: Minutes of the last meeting were read and	
	confirmed by BoS.	
	To confirm the action taken report on the minutes of the previous	Noted
	meeting held on 7 th April 2018	Noted
BoS 5.3	Resolution 4.3: BoS confirmed the action taken report on the	
	minutes of the previous meeting held on 7th April 2018 and	
	suggestions were implemented.	
3oS 5.4	·	The BoS members noted the progress of th
	To consider the Schemes and Syllabi of the undergraduate	School and recommended action items an
	program B.E in Electronics & Communication and approve the	timeline.
	same.	
	1. Scheme of I to VIII Semester (2019-23) Batch	Action Item No.1: Suggested new electiv
	2. Syllabus approval of I / II Semester, Basic Electronics for	courses: with Industry Collaboration for
	Mechanical and Electrical stream course (2019-23) Batch	design and delivery
	3. Scheme of III to VIII Semester (2018-22) Batch	ATR:
	4. Syllabus of III to VIII Semester (2018-22) Batch	Elective courses (3) are introduced in
	5. Scheme of V to VIII Semester (2017-21) Batch	state of a strand drawn with the second state and the state of the second strand s
	6. Syllabus of V to VIII Semester (2017-21) Batch	
	7. Scheme of VII and VIII Semester (2016-20) Batch	Embedded and Communication Systems
	8. Syllabus of VIII Semester (2016-20) Batch	collaboration with Industry in respectiv
	9. Scheme for Minor program in electronics for (2018-22)	domain.
	Batch	
	10. Scheme for Minor program in electronics for (2017-21)	Specialized Courses in VLSI domain, one o
	Batch	the vertical of SoECE, introduced are CMO
	11. Scheme for Minor program in electronics for (2017-21)	
	Batch	ASIC Design and Physical design analog
		in collaboration with Sifive Bangalore and
	Discussion: Based on the discussions following action items as	Sankalp semiconductors Pvt. Ltd, Hub
	agreed upon by everyone were finalized and the same were	respectively with focus on project base
	circulated to all the members on 13th April 2019. Persons	learning.
	responsible for these action items have already initiated the	curring.
	actions, which will be shared in the next BoS meeting.	To gain complete chip design flow
	The details of discussion are in Annexure 5.4	knowledge of CMOS ASIC design course wit
		hands-on intensive covers and explor
	Action Item No.1: Suggested new elective courses: with Industry	complete digital design flow o
	Collaboration for design and delivery	programmable ASIC through VLSI EDA tools.
	1. CMOS ASIC design	programmable ASIC through VLSI EDA (001s.
	2. Physical design analog	A course in the density of the bullet
	3. Introduction to deep learning	A course in the domain of analog VLSI to
		enhance teaching learning in Analog circui
	Action Item No.2: Integrated approach with hands on: Revised	design and layout, in turn build upon
	courses	competency through mini minor and
	1. CMOS VLSI Circuits,	capstone projects.
	2. Internet of Things,	
	3. Information Theory and coding	Course on Deep learning is introduced in
	4. Signals and System	interaction with SRIB, Bangalore. with
		Project-based learning which involves
	Resolution 5.4: Resolved to approve the Schemes and Syllabi of	dynamic classroom approach in which
	the undergraduate program B.E in Electronics & Communication	students acquire a deeper knowledge
	subject to implementation of action points listed above:	through active investigation of real-world
		challenges and problems.
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	No. J	
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		gical University
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	2. Sy M 3. Sc 4. Sy 5. Sc 6. Sy 7. Sc 8. Sy 9. Sc 8a 10. Sc 8a 11. M 19 12. Sy 13. Sc 8a 14. Sc 8a 14. Sc 8a 15. Sy	heme of I to VIII Semester (2019-23) Batch Allabus of I / II Semester, Basic Electronics course for echanical and Electrical streams for (2019-23) Batch heme of III to VIII Semester (2018-22) Batch Allabus of III to VIII Semester (2018-22) Batch Heme of V to VIII Semester (2017-21) Batch Allabus of V to VIII Semester (2017-21) Batch Heme of VII and VIII Semester (2016-20) Batch Ilabus of V and VIII Semester (2016-20) Batch Heme for Minor program in electronics for (2018-22) Atch Heme for Minor program in electronics for (2017-21) Atch Odification of Scheme of VII and VIII Semester (2015-19) Heme for Minor program in electronics for (2017-21) Atch Heme for Minor program in electronics for (2017-21) Atch Heme for Minor program in electronics for (2017-21) Atch Heme for Minor program in electronics for (2016-20) Atch Heme for Minor program in electronics for (2016-20) Atch	Action Item No.2: Integrated approach with hands on: Revised courses ATR: Integrated theory and lab approach is adapted to bridge the gap between understanding theoretical and realization the same with programming using EDA Tools and in the course CMOS VLSI circuits with separate credit structure Course on Internet of Things, (IoT) is introduced in interaction with Bosch, Bangalore. Focusing on hands on with separate credits for course project. An elective course on Information Theory and coding also focuses on hands on with various coding techniques for performance
			analysis of communication channel,
			modelling and simulation using
			MATLAB/Simulink followed by a course
			project with separate credits for course
			project.
			Context based learning for the most
			fundamental course in communication
			domain Signals and Systems is introduced
			through Co-teaching with hands on for
			mapping Mathematical concepts with
			physical interpretation of signal processing
BoS 5.5	To consider	the Schemes and Syllabi of the postgraduate program	<i>towards better learning .</i> The BoS members noted the progress of the
	M. Tech in D	Digital Electronics and approve the same.	School and recommended action items and
	a) Scl	neme of I to IV Semester (2019-21) Batch	timeline.
	b) Syl	labus of I/II Semester (2019-21) Batch	
	c) Mo	odification of Scheme of III/IV Semester (2018-20)	Action Item No.1: Suggested new core
	d) Syl	labus of III/IV Semester (2018-20) Batch	courses to strengthen basic concepts and
	Discussion:	Based on the discussions following action items as	programming ATR:
	agreed upo	n by everyone were finalized and the same were	A course on Automotive electronics and
	circulated t	o all the members on 13th April 2019. Persons	Communication is introduced with the focus
	responsible	for these action items have already initiated the	on industry specific model based design
	actions, which	ch will be shared in the next BoS meeting.	approach and necessary communication
			protocols for inter and intra vehicular
	Action Item	No.1: Suggested new core courses to strengthen	communication in collaboration with Bosch
		its and programming	B <mark>angalo</mark> re.
	I. AUT	omotive electronics and Communication	

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	Resolution 5.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M. Tech in Digital Electronics subjected to implementation of action points listed above. a) Scheme of I to IV Semester (2019-21) Batch b) Syllabus of I/II Semester (2019-21) Batch c) Modification of Scheme of III/IV Semester (2018-20) Batch d) Syllabus of III/IV Semester (2018-20) Batch	
BoS 5.6	To consider the Schemes and Syllabi of the postgraduate program M. Tech in VLSI Design and Embedded Systems and approve the same.	The BoS members noted the progress of the School and recommended action items and timeline.
	 a) Scheme of I to IV Semester (2019-21) Batch b) Syllabus of I/II Semester (2019-21) Batch c) Modification of Scheme of III/IV Semester (2018-20) d) Syllabus of III/IV Semester (2018-20) Batch 	Action Item No.1: Suggested new core courses to strengthen basic concepts and programming ATR:
	Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 13th April 2019. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting.	A course on Automotive electronics and Communication is introduced with the focus on industry specific model based design approach and necessary communication protocols for inter and intra vehicular communication in collaboration with Bosch Bangalore.
	 Action Item No.1: Suggested new core courses to strengthen basic concepts and programming 1. Automotive electronics and Communication 2. AUTOSAR and Infotainment 	A standardized interface for software components in the application layer for building applications including infotainment to support the vehicle functions is introduced
	Resolution 5.6: Resolved to the Schemes and Syllabi of the postgraduate program M. Tech in in VLSI Design and Embedded Systems subjected to implementation of action points listed	in AUTOSAR and Infotainment in collaboration with Bosch, Bangalore.
	 above. a) Scheme of I to IV Semester (2019-21) Batch b) Syllabus of I/II Semester (2019-21) Batch c) Modification of Scheme of III/IV Semester (2018-20) Batch d) Syllabus of III/IV Semester (2018-20) Batch 	
BoS 5.7	Question Paper review Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI mapping was presented.	QP Pattern is incorporated in all the courses.
BoS 5.8	Vision, Mission, POs, PSOs of School of ECE and CAM , PAM of 2015-19 Discussion: The Vision, Mission, POs, PSOs and CAM, PAM of 2015-19 of School of ECE were presented.	SoECE staff aligned to Vision, Mission, POs and PSOs .
BoS 5.9	Any other subject with the permission of the Chair Nil.	

Dr.Nalini e Iyer RECISTRAR Chairperson, BoS, SoECE KLE Technological Universit HUBBALLI-580 031



Minutes

6th Board of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

30th May 2020

KLE Technological University

(Established under Karnataka Act No.22, 2013)

KLE Technological University N HUBBALLI-580 031



The following are the minutes of the Board of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 30th May 2020 at 10:30 am in MS Teams.

The following members were present.

SI No	Name	Designation	Position
1.	Dr. Nalini C.lyer	Head of School, SoECE	Chairperson
2.	Dr. R M Bankar	Professor, SoECE	Member
3.	Dr. Uma Mudengudi	Professor, SoECE	Member
4.	Dr. Priyatamkumar	Professor, SOECE	Member
5.	Dr. Saroja S	Professor, SoECE	Member
6.	Dr. Ujwala Patil	Associate Professor, SoECE	Member
7.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member
8.	Dr. Mahadev prasanna	Professor, Department of EC,IIT Dharwad	Member
9.	Dr. Chetan Parekh	Professor, Department of EC,IIIT Bangalore	Member
10.	Mr. Praveen B P	Samsung India, Bangalore	Member
11.	Mr. Gurumurthy A	RBEI, Bangalore	Member
12.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductor	Member
13.	Dr. Sujata S Kotabagi	Professor, SoECE	Member
14.	Dr. R B Shettar	Professor, SoECE	Member
15.	Prof. Suneeta V B	Professor, SoECE	
16.	Prof. P. C. Nissimgoudar	Associate Professor, SoECE	
17.	Prof. R. M. Shet	Assistant Professor, SoECE	
18.	1. UG: Deepti H 2. UG: Aditya O		Student Members
	3.PG1 :Mandakini	and the second sec	
	4. PG2: Gangotri		
	5. PhD: Suhas Shirol		

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SI No	Particulars	Page No.
6.1	To welcome the BoS Members and present department achievements & initiatives	
6.2	To read and confirm the minutes of 5 th BoS meeting held on 13 th April 2019	
6.3	To confirm the action taken report on the minutes of the previous meeting held on 13 th April 2019	
6.4	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics &	
6.4	Communication and approve the same.	
	a) Scheme approval of I to VIII Semester (2020-24)	
	b) Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrical	
	stream course (2020-24)	
	c) Scheme approval of III to VIII Semester (2019-23)	
	d) Syllabus approval of III to VIII Semester (2019-23)	
	e) Scheme approval of V to VIII Semester (2018-22)	
	f) Syllabus approval of V to VIII Semester (2018-22)	
	g) Scheme approval of VII and VIII Semester (2017-21)	
	h) Syllabus approval of V and VIII Semester (2017-21)	
	i) Scheme approval: Scheme 2019-23 in Minor Program	•
	j) Scheme approval: Scheme 2018-22 in Minor Program	
	k) Syllabus approval: Scheme 2018-22 in Minor Program	
6.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and	
	approve the same.	
	a) Scheme approval of I to IV Semester (2020-22)	
	b) Syllabus approval of I/II Semester (2020-22)	
	c) Modification of Scheme of III/IV Semester (2019-21)	
	d) Syllabus approval of III/IV Semester (2019-21)	
6.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design & Embedded	
	Systems and approve the same.	
	a) Scheme approval of I to IV Semester (2020-22)	
	b) Syllabus approval of I/II Semester (2020-22)	
	c) Modification of Scheme of III/IV Semester (2019-21)	
	d) Syllabus approval of III/IV Semester (2019-21)	
6.7	Question Paper review and Discussion on attainment of POs and PSOs	
6.8	Vision, Mission, POs, PSOs of School of ECE and CAM and PAM	
6.9	Any other matter for discussion with the permission of the chair	

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BoS 6.1 To welcome the BoS Members and present department achievements & initiatives

Resolution 5.1: The BoS members appreciated the initiatives of SoECE and lauded its achievements.

BoS 6.2	The follo	wing are	the minutes of the Boar	oS meeting held on 13 th April 2019 d of Studies meeting of SoECE, KLE Techn	ological University, Hubba	
	which wa	as held o	n 13 th April 2019 at 10:30) am at the Senate Hall of the University.		
	The follo	wing me	embers were present.			
	BoS 5.2	To read and confirm the minutes of 5 th BoS meeting held on 13 th April 2019				
		Univer Univer	sity, Hubballi, which wa	es of the Board of Studies meeting of as held on 13 th April 2019 at 10:30 am present.	SoECE, KLE Technologica at the Senate Hall of th	
		SI	Name	Designation	Position	
		1.	Dr. Nalini C.lyer	Head of School, SoECE	Chairperson	
		2.	Dr. R M Bankar	Professor, SoECE	Member	
		3.	Dr. Uma Mudengudi	Professor, SoECE	Member	
		4.	Dr. Priyatamkumar	Professor, SoECE	Member	
		5.	Dr. Saroja S	Professor, SoECE	Member	
		6.	Prof. Ujwala Patil	Associate Professor, SoECE	Member	
		7.	Dr. D. Manjunath	Professor, Department of EC,IIT Bombay	Member	
		8.	Dr. Chetan Parekh	Professor, Department of EC,IIIT Bangalore	Member	
		9.	Mr. Praveen B P	Samsung India, Bangalore	Member	
		10.	Mrs. Padmini Navalgund	RBEI, Bangalore	Member	
		11.	Mr. Sumit Bhat	Design Lead, Sankalp Semiconductor	Member	
		12.	Mr. Shivakumar Turmari	Tessolve Semiconductors, Bangalore	Member	
		13.	Dr. Sujata S Kotabagi	Professor, SoECE	Member	
		14.	Dr. R B Shettar	Professor, SoECE	Member	
		15.	Prof. Suneeta V B	Professor, SoECE		
		16.	Prof. P. C. Nissimgoudar	Associate Professor, SoECE		
		17.	Prof. R. M. Shet	Assistant Professor, SoECE		
		18.	UG: Pranav K UG: Niveditha J		Student Members	
			PG1 :Vijaylakshmi PG2: Saiarpita			
			PhD: Suhas Shirol			
	Item			Description		
	BoS	5.1 d R	iscussed about the inputs esolution 5.1: The BoS	embers and present department achieved from all stake holders (Annexure 6.1) members appreciated the work done to s a State private University effective from	owards recognition of KL	
	BoS	5.2 T	o read and confirm the m esolution 5.2: Minutes of	inutes of 5 th BoS meeting held on 13 th Ap f the last meeting were read and confirm	ril 2019 ed by BoS.	
	BoS		o confirm the action take 019	en report on the minutes of the previous	meeting held on 13" Apr	

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BoS 5.4	
	To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same.
	1 Scheme of Lto VIII Semester (2019-23) Batch
	2. Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrica
10	stream course (2019-23) Batch
	3. Scheme of III to VIII Semester (2018-22) Batch
	4. Syllabus of III to VIII Semester (2018-22) Batch
	5. Scheme of V to VIII Semester (2017-21) Batch
	6. Syllabus of V to VIII Semester (2017-21) Batch
	7. Scheme of VII and VIII Semester (2016-20) Batch
	8. Syllabus of VIII Semester (2016-20) Batch
	9. Scheme for Minor program in electronics for (2018-22) Batch
	10. Scheme for Minor program in electronics for (2017-21) Batch
	11. Scheme for Minor program in electronics for (2017-21) Batch
	Discussion: Based on the discussions following action items as agreed upon by everyone we
	finalized and the same were circulated to all the members on 13 th April 2019. Person
	responsible for these action items have already initiated the actions, which will be shared
	the next BoS meeting.
	The details of discussion are in Annexure 6.4
	Action Item No.1: Suggested new elective courses: with Industry Collaboration for design an
	delivery
	1. CMOS ASIC design
	2. Physical design analog
	3. Introduction to deep learning
	Action Item No.2: Integrated approach with hands on: Revised courses
	1. CMOS VLSI Circuits,
	2. Internet of Things,
	3. Information Theory and coding
	4. Signals and System
	Resolution 5.4: Resolved to approve the Schemes and Syllabi of the undergraduate program
	B.E in Electronics & Communication subject to implementation of action points listed above
	1. Scheme of I to VIII Semester (2019-23) Batch
	2. Syllabus of I / II Semester, Basic Electronics course for Mechanical and Electri
	streams for (2019-23) Batch
	3. Scheme of III to VIII Semester (2018-22) Batch
	4. Syllabus of III to VIII Semester (2018-22) Batch
	5. Scheme of V to VIII Semester (2017-21) Batch
	6. Syllabus of V to VIII Semester (2017-21) Batch
	7. Scheme of VII and VIII Semester (2016-20) Batch
	8. Syllabus of V and VIII Semester (2016-20) Batch
	9. Scheme for Minor program in electronics for (2018-22) Batch
	10. Scheme for Minor program in electronics for (2017-21) Batch
	11. Modification of Scheme of VII and VIII Semester (2015-19)
	12. Syllabus approval of VII and VIII Semester (2015-19)
	13. Scheme for Minor program in electronics for (2017-21) Batch
	14. Scheme for Minor program in electronics for (2016-20) Batch
	15. Syllabus for Minor program in electronics for (2016-20) Batch

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BoS 5.5	To consider the Schemes and Syllabi of the postgraduate program M. Tech in Digital Electronics
000 0.0	and approve the same.
	a) Scheme of I to IV Semester (2019-21) Batch
	b) Syllabus of I/II Semester (2019-21) Batch
	c) Modification of Scheme of III/IV Semester (2018-20)
15	d) Syllabus of III/IV Semester (2018-20) Batch
	Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 13th April 2019. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting.
	Action Item No.1: Suggested new core courses to strengthen basic concepts and programming 1. Automotive electronics and Communication
	Resolution 5.5: Resolved to approve the following Schemes and Syllabi of the postgraduate program M. Tech in Digital Electronics subjected to implementation of action points listed above.
	a) Scheme of I to IV Semester (2019-21) Batch
	b) Syllabus of I/II Semester (2019-21) Batch
	 c) Modification of Scheme of III/IV Semester (2018-20) Batch d) Syllabus of III/IV Semester (2018-20) Batch
BoS 5.6	To consider the Schemes and Syllabi of the postgraduate program M. Tech in VLSI Design and
000 010	Embedded Systems and approve the same.
	a) Scheme of I to IV Semester (2019-21) Batch
	b) Syllabus of I/II Semester (2019-21) Batch
	c) Modification of Scheme of III/IV Semester (2018-20)
	d) Syllabus of III/IV Semester (2018-20) Batch
	Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 13th April 2019. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting.
	 Action Item No.1: Suggested new core courses to strengthen basic concepts and programming 1. Automotive electronics and Communication 2. AUTOSAR and Infotainment
	Resolution 5.6: Resolved to the Schemes and Syllabi of the postgraduate program M. Tech in in VLSI Design and Embedded Systems subjected to implementation of action points listed above.
	a) Scheme of I to IV Semester (2019-21) Batch
	b) Syllabus of I/II Semester (2019-21) Batch
	c) Modification of Scheme of III/IV Semester (2018-20) Batch
	d) Syllabus of III/IV Semester (2018-20) Batch
BoS 5.7	Question Paper review
003 5.7	Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels
	and PO-PSO-PI mapping was presented.
BoS 5.8	Vision, Mission, POs, PSOs of School of ECE and CAM , PAM of 2015-19
	Discussion: The Vision, Mission, POs, PSOs and CAM, PAM of 2015-19 of School of ECE were
D.050	presented.
BoS 5.9	Any other subject with the permission of the Chair

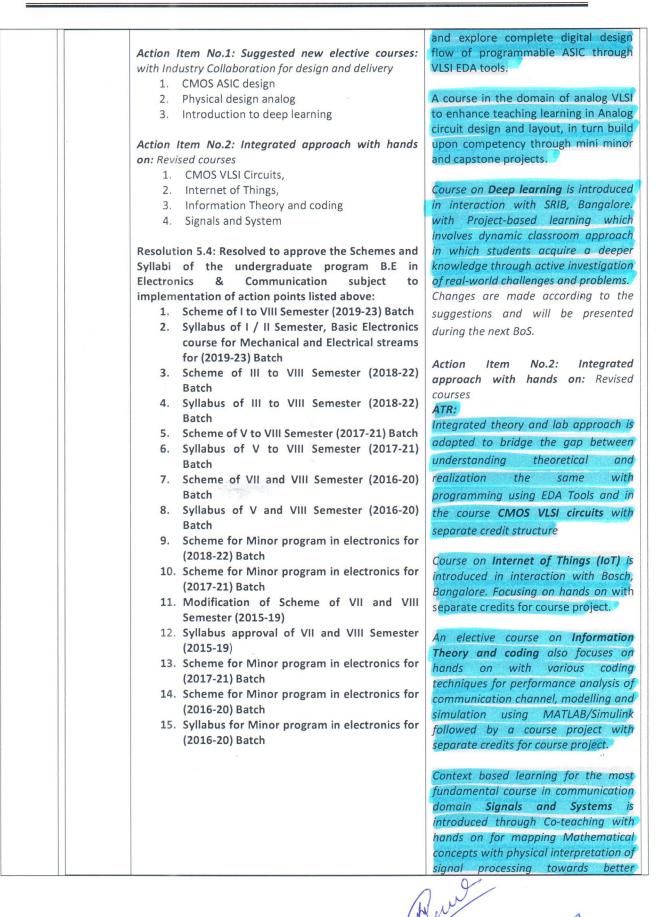
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		Nil.	
	Resolution	6.2: Resolved to confirm the minutes of its 5 th BoS meeti	ng held on 13 th April 2019
3oS 6.3	Resolution:	he action taken report on the minutes of the previous mee 5.3 Resolved to confirm the action taken report on the m The BoS members appreciated the new initiatives taken b	inutes of its 5 th BoS meeting held on 13
	Item No	Description	Action Taken
	BoS 5.1	To welcome the BoS Members and present department achievements & initiatives and discussed about the inputs from all stake holders (Annexure 6.1) Resolution 5.1: The BoS members appreciated the work done towards recognition of KLE Technological University as a State private University effective from 2015.	Noted .
	BoS 5.2	To read and confirm the minutes of 5 th BoS meeting held on 13 th April 2019 Resolution 5.2: Minutes of the last meeting were read and confirmed by BoS.	Noted
	BoS 5.3	To confirm the action taken report on the minutes of the previous meeting held on 13 th April 2019 Resolution 5.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 13 th April 2019 and suggestions were implemented.	Noted
	BoS 5.4	 To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same. 1. Scheme of I to VIII Semester (2019-23) Batch 2. Syllabus approval of I / II Semester, Basic Electronics for Mechanical and Electrical stream course (2019-23) Batch 3. Scheme of III to VIII Semester (2018-22) Batch 4. Syllabus of III to VIII Semester (2018-22) Batch 5. Scheme of V to VIII Semester (2017-21) Batch 6. Syllabus of V to VIII Semester (2017-21) Batch 7. Scheme of VII and VIII Semester (2016-20) Batch 8. Syllabus of VIII Semester (2016-20) Batch 9. Scheme for Minor program in electronics for (2018-22) Batch 10. Scheme for Minor program in electronics for (2017-21) Batch 11. Scheme for Minor program in electronics for (2017-21) Batch 	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested nee elective courses: with Indust Collaboration for design and delivery ATR: Elective courses (3) are introduced in verticals (identified by SoECE) VLS Embedded and Communication Systems in collaboration with Indust in respective domain. Specialized Courses in VLSI domain, on of the vertical of SoECE, introduced a CMOS ASIC Design and Physical design analog in collaboration with Sifive Bangalo and, Sankalp semiconductors Pvt. La
		Discussion : Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 13 th April 2019. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting.	Hubli respectively with focus on proje based learning.

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		learning .
BoS 5.5	To consider the Schemes and Syllabi of the	The BoS members noted the progress
	postgraduate program M. Tech in Digital Electronics	of the School and recommended
	and approve the same.	action items and timeline.
	a) Scheme of I to IV Semester (2019-21) Batch	
	b) Syllabus of I/II Semester (2019-21) Batch	Action Item No.1: Suggested new cor
	c) Modification of Scheme of III/IV Semester	courses to strengthen basic concept
e	(2018-20)	and programming
	d) Syllabus of III/IV Semester (2018-20) Batch	ATR:
		A course on Automotive electroni
	Discussion: Based on the discussions following action	and Communication is introduced wi
	items as agreed upon by everyone were finalized and	the focus on industry specific mod
	the same were circulated to all the members on 13th	based design approach and necessa
	April 2019. Persons responsible for these action items	communication protocols for inter an
	have already initiated the actions, which will be shared	intra vehicular communication
	in the next BoS meeting.	collaboration with Bosch Bangalore.
	Action Item No.1: Suggested new core courses to	
	strengthen basic concepts and programming	
	1. Automotive electronics and Communication	
	Resolution 5.5: Resolved to approve the following	
	Schemes and Syllabi of the postgraduate program M.	
	Tech in Digital Electronics subjected to	
	implementation of action points listed above.	
	a) Scheme of I to IV Semester (2019-21) Batch	
	b) Syllabus of I/II Semester (2019-21) Batch	
	c) Modification of Scheme of III/IV Semester	
	(2018-20) Batch d) Syllabus of III/IV Semester (2018-20) Batch	
BoS 5.6	To consider the Schemes and Syllabi of the	The BoS members noted the progres
	postgraduate program M. Tech in VLSI Design and	of the School and recommended
	Embedded Systems and approve the same.	action items and timeline.
	a) Scheme of I to IV Semester (2019-21)	
	Batch	Action Item No.1: Suggested new co
	b) Syllabus of I/II Semester (2019-21) Batch	courses to strengthen basic concep
	c) Modification of Scheme of III/IV Semester	and programming
	(2018-20)	ATR:
	d) Syllabus of III/IV Semester (2018-20)	A course on Automotive electron
	Batch	and Communication is introduced w
		the focus on industry specific mo
	Discussion: Based on the discussions following action	based design approach and necess
	items as agreed upon by everyone were finalized and	communication protocols for inter a
	the same were circulated to all the members on 13th	intra vehicular communication
	April 2019. Persons responsible for these action items	collaboration with Bosch Bangalore.
	have already initiated the actions, which will be shared	
	in the next BoS meeting.	A standardized interface for softwo
	Anting them bla 1. Commented and the second	components in the application layer
	Action Item No.1: Suggested new core courses to	building applications includ
	the second se	
	strengthen basic concepts and programming	infotainment to support the veh
	1. Automotive electronics and Communication	
		functions is introduced in AUTOS
	 Automotive electronics and Communication AUTOSAR and Infotainment 	functions is introduced in AUTOS
	1. Automotive electronics and Communication	infotainment to support the vehi functions is introduced in AUTOS and Infotainment in collaboration w Bosch, Bangalore.

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BoS 5.7	implementation of action points listed above.a)Scheme of I to IV Semester (2019-21) Batchb)Syllabus of I/II Semester (2019-21) Batchc)Modification of Scheme of III/IV Semester (2018-20) Batchd)Syllabus of III/IV Semester (2018-20) BatchQuestion Paper reviewDiscussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI	QP Pattern is incorporated in all the courses.
BoS 5.8	mapping was presented. Vision, Mission, POs, PSOs of School of ECE and CAM, PAM of 2015-19 Discussion: The Vision, Mission, POs, PSOs and CAM, PAM of 2015-19 of School of ECE were presented.	SoECE staff aligned to Vision, Mission, POs and PSOs .
BoS 5.9	Any other subject with the permission of the Chair Nil.	
April 2019.	6.3 Resolved to confirm the action taken report on the mi The BoS members appreciated the new initiatives taken b the Schemes and Syllabi of the undergraduate program B.E	y SoECE
approve the a) b) c) d) e) f) g) h) i) j) k) l) l) m)	Scheme approval of I to VIII Semester (2020-24) Syllabus approval of I / II Semester, Basic Electronics for (2020-24) Scheme approval of III to VIII Semester (2019-23) Syllabus approval of V to VIII Semester (2018-22) Syllabus approval of V to VIII Semester (2018-22) Syllabus approval of V to VIII Semester (2018-22) Scheme approval of V to VIII Semester (2017-21) Syllabus approval of V and VIII Semester (2017-21) Scheme approval of V and VIII Semester (2017-21) Scheme approval: Scheme 2019-23 in Minor Program Scheme approval: Scheme 2018-22 in Minor Program Syllabus approval: Scheme 2018-22 in Minor Program Scheme approval (2020-24) in Bachelor of Electronics Engi Syllabus approval of I / II /III trimester (2020-24) in Bachelor	neering (Industrial Integrated)
	Integrated)	
same were o	Based on the discussions following action items as agreed u irculated to all the members on 30 th May 2020. Persons re ated the actions, which will be shared in the next BoS meet	sponsible for these action items have
same were of already initian Action Item Action Item	Based on the discussions following action items as agreed us irculated to all the members on 30 th May 2020. Persons re ated the actions, which will be shared in the next BoS meet <i>No.1:</i> New courses added: Senior Design Project <i>No.2:</i> Revised courses : Project Work , AUTOSAR 5.4: Resolved to approve the Schemes and Syllabi of the un cation:	sponsible for these action items have ing.
same were of already initia Action Item Action Item Resolution 6	Based on the discussions following action items as agreed usinculated to all the members on 30 th May 2020. Persons re ated the actions, which will be shared in the next BoS meet No.1: New courses added: Senior Design Project No.2: Revised courses : Project Work , AUTOSAR 5.4: Resolved to approve the Schemes and Syllabi of the u	sponsible for these action items have ing. ndergraduate program B.E in Electronics

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	f) Syllabus approval of V to VIII Semester (2018-22)				
	g) Scheme approval of VII and VIII Semester (2017-21)				
	h) Syllabus approval of V and VIII Semester (2017-21)				
	i) Scheme approval: Scheme 2019-23 in Minor Program				
	(Industrial Integrated)				
	(2020 24) in Pachalor of Electronics Engineering				
	m) Syllabus approval of 1 / 11 /111 trimester (2020-24) in Bachelor of Electronics Engineering (Industrial Integrated)				
3oS 6.5	To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the				
503 0.5	same.				
	a) Scheme approval of I to IV Semester (2020-22)				
	(2020.22)				
	10010 D1)				
	 d) Syllabus approval of III/IV Semester (2019-21) Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and th 				
	Discussion: Based on the discussions following action items as agreed upon by everyone were managed and the				
	same were circulated to all the members on 30 th May 2020. Persons responsible for these action items hav				
	already initiated the actions, which will be shared in the next BoS meeting.				
	Resolution 6.5: Resolved to the Schemes and Syllabi of the postgraduate program M.Tech in Digital				
	Electronics:				
	a) Scheme approval of I to IV Semester (2020-22)				
	b) Syllabus approval of I/II Semester (2020-22)				
	c) Modification of Scheme of III/IV Semester (2019-21)				
	d) Syllabus approval of III/IV Semester (2019-21)				
BoS 6.6	To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design and Embedded System				
	and approve the same.				
	a) Scheme approval of I to IV Semester (2020-22)				
	b) Syllabus approval of I/II Semester (2020-22)				
	c) Modification of Scheme of III/IV Semester (2019-21)				
	d) Syllabus approval of III/IV Semester (2019-21)				
	Resolution 6.6: Resolved to the Schemes and Syllabi of the postgraduate program M.Tech in in VLSI Design				
	and Embedded Systems				
	a) Scheme approval of I to IV Semester (2020-22)				
	b) Syllabus approval of I/II Semester (2020-22)				
	c) Modification of Scheme of III/IV Semester (2019-21)				
	d) Syllabus approval of III/IV Semester (2019-21)				
BoS 6.7	Question Paper review				
	Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI				
	mapping were presented.				
BoS 6.8	Vision, Mission, POs , PSOs, CAM and PAM of School of ECE				
500 0.0	Discussion: The Vision, Mission, POs and PSOs of School of ECE were presented.				
BoS 6.9	Any other subject with the permission of the Chair				
	Nil.				

The Chairperson thanked all the members for the valuable contributions

N Chairperson, Bos, SoECE Dr. Nalini C Iyer

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Annexure 6.1

Discussion Item

Employers Feedback:

- Students should be able to develop engineering solutions in societal and environmental contexts.
- Ability for effective communication, problem solving, conflict resolution and leadership skills.
- Enhance ability to apply design principles in the development of hardware and software systems of varying complexity, using state of art tools for the development of VLSI/Embedded/Communication systems.

Teachers Feedback (Pre-BoS MoM):

- Formulation of application oriented examples.
- Focus on problem solving using programming skills and use of online platform.

Students Feedback:

- To focus on latest technological trends and development.
- Formulation of application oriented examples.

Alumni Feedback:

Recommended for co-delivery by industry experts.



Annexure 6.4

	Discussion Item	Course
	BE (ECE)	
1.	Theme based project with strong emphasis on design aspects in the domain of VLSI, Communication and Embedded with hardware and software integration for the desired functionality is introduced.	Senior Design Project-20EECW401 Added-New course
2.	Industry/Research/Academic project with more emphasis on design aspects and real time constraints in the domain of VLSI, Communication and Embedded with hardware and software integration for the desired functionality is introduced.	Project Work-20EECW402 Revised- Delivery
3.	A standardized interface for software components in the application layer and application software components for building applications to support the vehicle functions is introduced. Course prepares students to be industry ready with hands on using standard tools and industry mentored projects.	Autosar-20EECE406 Revised- Delivery
	M.Tech Digital Electronics	
	NIL	NIL ·
	M.Tech VLSI Design and Embedded Systems	
	NIL	NIL

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Action Taken Report

6thBoard of Studies Meeting

of

School of Electronics and Communication Engineering

Hubballi, Karnataka

30th May 2020

KLE Technological University

(Established under Karnataka Act No.22, 2013)

REGISTRAR KLE Technological University HUBBALLI-580 031 The following are the action items proposed during 6rdBoard of Studies meeting of SoECE, KLE Technological University, Hubballi which was held on 30thMay 2020. The corresponding actions taken are also listed below.

ltem No	Description	Action Taken
BoS 6.1	To welcome the BoS Members and present department achievements & initiatives and discussed about the inputs from all stake holders (Annexure 6.1) Resolution 5.1: The BoS members appreciated the work done towards recognition of KLE Technological University as a State private University effective from 2015.	Noted
BoS 6.2	To read and confirm the minutes of 5 th BoS meeting held on 13 th April 2019 Resolution 5.2: Minutes of the last meeting were read and confirmed by BoS.	Noted
BoS 6.3	To confirm the action taken report on the minutes of the previous meeting held on 13 th April 2019 Resolution 5.3: BoS confirmed the action taken report on the minutes of the previous meeting held on 13 th April 2019 and suggestions were implemented	Noted
	 suggestions were implemented. To consider the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication and approve the same. a) Scheme of I to VIII Semester (2020-24) batch. b) Syllabus of I / II Semester, Basic Electronics course for Mechanical and Electrical streams for (2020-24) batch. c) Scheme of III to VIII Semester (2019-23) batch. d) Syllabus of III to VIII Semester (2019-23) batch. e) Scheme of V to VIII Semester (2018-22) batch. f) Syllabus of V to VIII Semester (2018-22) batch. g) Scheme of VI to VIII Semester (2017-21) batch. h) Syllabus of V and VIII Semester (2017-21) batch. h) Syllabus of V and VIII Semester (2017-21) batch. h) Syllabus of V and VIII Semester (2017-21) batch. i) Scheme 2018-22 in Minor Program j) Scheme 2018-22 in Minor Program k) Scheme 2018-22 in Minor Program l) Scheme approval :2020-24 in Bachelor of Electronics Engineering (Industrial Integrated) m) Syllabus of I / II / III trimester (2020-24) in Bachelor of Electronics Engineering (Industrial Integrated) batch. Discussion: Based on the discussions following action, items as agreed upon by everyone were finalized and the same were circulated to all the members on 30th May 2020. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting. The details of discussion are in <i>Annexure 6.4</i>. Action Item No.1: Suggested new core course with strong focus on design aspects. 1) Project Work 2) AUTOSAR Resolution 6.4: Resolved to approve the Schemes and Syllabi of the undergraduate program B.E in Electronics & Communication subject to implementation of action points listed above: a) Scheme of I to VIII Semester (2020-24) batch. 	The BoS members noted the progress of the School and recommended action items and timeline. Action Item No.1: Suggested new core course focusing on project based learning ATR: Theme based Research/Academic senior design project with strong emphasis on design aspects in the domain of VLSI, Communication and Embedded with hardware and software integration for the desired functionality is introduced. Action Item No.2: Suggested course with Project based learning emphasis ATR: Industry/Research/Academic project with more emphasis on design aspects and real time constraints in the domain of VLSI, Communication and Embedded with hardware and software integration for the desired functionality is introduced. A standardized interface for software components in the application layer and application software components for building applications to support the vehicle functions is introduced. Autosar prepares students to be industry ready with hands on using standard tools and industry mentored projects.

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	 b) Syllabus of I / II Semester, Basic Electronics for Mechanical and Electrical stream course (2020-24) batch. c) Scheme of III to VIII Semester (2019-23) batch. d) Syllabus of III to VIII Semester (2019-23) batch. e) Scheme of V to VIII Semester (2018-22) batch. f) Syllabus of V to VIII Semester (2018-22) batch. g) Scheme of VII and VIII Semester (2017-21) batch. h) Syllabus of V and VIII Semester (2017-21) batch. i) Scheme 2019-23 in Minor Program j) Scheme 2018-22 in Minor Program k) Scheme 2018-22 in Minor Program l) Scheme approval :2020-24 in Bachelor of Electronics Engineering (Industrial Integrated) m) Syllabus of I / II /III trimester (2020-24)in Bachelor of Electronics Engineering (Industrial Integrated) batch. 	
BoS 6.5	 To consider the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics and approve the same. a) Scheme of I to IV Semester (2020-22) batch. b) Syllabus of I/II Semester (2020-22) batch. c) Modification of Scheme of III/IV Semester (2019-21) batch d) Syllabus of III/IV Semester (2019-21) batch. Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 30th May 2020. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting. Resolution 6.5: Resolved to the Schemes and Syllabi of the postgraduate program M.Tech in Digital Electronics subject to implementation of action points listed above: a) Scheme of I to IV Semester (2020-22) batch. b) Syllabus of I/II Semester (2020-22) batch. c) Modification of Scheme of III/IV Semester (2019-21) 	The BoS members noted the progress of the School and recommended action items and timeline. ATR: No major changes in scheme or content for 2020-21 and therefore to continue the same courses under the respective scheme
BoS 6.6	 To consider the Schemes and Syllabi of the postgraduate program M.Tech in VLSI Design and Embedded Systems and approve the same. a) Scheme of I to IV Semester (2020-22) batch. b) Syllabus of I/II Semester (2020-22) batch. c) Modification of Scheme of III/IV Semester (2019-21) batch. d) Syllabus of III/IV Semester (2019-21) batch. Discussion: Based on the discussions following action items as agreed upon by everyone were finalized and the same were circulated to all the members on 30 th May 2020. Persons responsible for these action items have already initiated the actions, which will be shared in the next BoS meeting. Resolution 6.6: Resolved to the Schemes and Syllabi of the postgraduate program M.Tech in in VLSI Design and Embedded	The BoS members noted the progress of the School and recommended action items and timeline. ATR: No major changes in scheme or content for 2020-21 and therefore to continue the same courses under the respective scheme

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	 Systems subject to implementation of action points listed above: a) Scheme of I to IV Semester (2020-22) batch. b) Syllabus of I/II Semester (2020-22) batch. c) Modification of Scheme of III/IV Semester (2019-21) batch. d) Syllabus of III/IV Semester (2019-21) batch. 	
BoS 6.7	Question Paper review Discussion: The Question Paper along with assessment patterns with respect to Bloom's Levels and PO-PSO-PI mapping was presented.	QP Pattern is incorporated in all the courses.
BoS 6.8	Vision, Mission, POs, PSOs of School of ECE and CAM, PAM of 2016-20 Discussion: The Vision, Mission, POs, PSOs and CAM, PAM of 2016-20of School of ECEwas presented.	SoECE staff aligned to Vision, Mission, POs and PSOs .
BoS 6.9	Any other subject with the permission of the Chair Nil.	

NCZ Dr.Nalini C Iyer Chairperson, BoS, SoECE

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