



Percentage of new courses  
introduced of the total number of  
courses across all programmes  
offered during the last five years

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<b>Program:</b> Bachelor of Engineering		
Course Title: <b>Engineering Design Practice [Part B]</b> <b>[Part A – Central Level]</b>		Course Code: <b>17ECSP202</b>
L-T-P: <b>0-0-1.5</b>	Credits: <b>1.5</b>	Contact Hrs: <b>3 hrs/week</b>
ISA Marks: <b>40</b>	ESA Marks: <b>0</b>	Total Marks: <b>40</b>
Teaching Hrs: <b>39 hrs</b>	Exam Duration: <b>3 hrs</b>	

Experiments	Lab assignments/experiment
Phase 1 (Plannig)	Introduction to Eclipse –IDE Requirement modeling : <ul style="list-style-type: none"> <li>• Identifying use cases and actors</li> <li>• Apply UML notations to draw use case diagram</li> </ul>
Phase 2 (Conceptual Design)	Behaviour Modeling using DFD <ul style="list-style-type: none"> <li>• List behavior of system/sub-system</li> <li>• List states, tasks and their dependencies</li> </ul> Illustrate DFD : <ul style="list-style-type: none"> <li>• Identify data flow and processes of a system</li> <li>• Draw data flow diagrams for system/sub-system</li> <li>• Draw system diagram to show interaction of all domain components</li> </ul> (Draw state and sequence diagram for identified tasks)
Phase 3(System Design)	Software Architectures: <ul style="list-style-type: none"> <li>• List components of architecture</li> <li>• List type of architectures</li> </ul> Choose appropriate architecture for given system
Phase 4 (Detail Design)	UI Design using GUI wireframe: <ul style="list-style-type: none"> <li>• Design function prototyping for event diagrams(DFD)</li> <li>• Identify user interface components</li> <li>• Choose appropriate property of component</li> <li>• Use wireframe to design a user interface</li> </ul>
Text books: <ol style="list-style-type: none"> <li>1. Ian Somerville, Software Engineering, 9th, Pearson Ed, 2015</li> <li>2. Clive L Dym and Patrick Little, "Engineering Design: A Project Based Introduction", John Wiley &amp; Sons</li> </ol>	
Reference books: <ol style="list-style-type: none"> <li>1. Roger S. Pressman, Software Engineering: A Practitioners Approach, 7th, McGraw, 2007</li> <li>2. Shari Lawrence Pfleeger and Joanne M. Atlee, Software Engineering Theory and Practice, 3rd, Pearson Ed, 2006</li> <li>3. Jalote, P, An Integrated Approach to Software Engineering, 3rd, Narosa Pub, 2005</li> </ol>	



Program: <b>Bachelor of Engineering</b>		
Course Title: <b>Product Realization</b>		Course Code: <b>17ECSP203</b>
L-T-P: <b>0-0-2</b>	Credits: <b>02</b>	Contact Hrs: <b>03 Hrs</b>
ISA Marks: <b>80</b>	ESA Marks: <b>20</b>	Total Marks: <b>100</b>
Teaching Hrs:	Exam Duration:	

Experiments	Lab assignments/experiment
Week 1 And Week 2	IOT workshop: Introduction to Android studio, Introduction to Arduino programming, PHP
Week 3	Selection of UI and Core Component of Android
Week 4	UI implementation using XML
Week 5	UI implementation and validation
Week 6	Android core component implementation and Unit Testing
Week 7	Android core component implementation and Unit Testing
Week 8	Android core components integration and testing
Week 9	Configuration of IoT Server
Week 10	Integratesubsystems for prototype testing, Analyze the test results, System modification, and System integration.
Week 11	System Testing

## Reference:

1. Beginning Android Programming with Android Studio by J.F. DiMarzio



Program: <b>Bachelor of Engineering</b>		
Course Title: <b>Algorithmic Problem Solving</b>		Course Code: <b>17ECSE309</b>
L-T-P: <b>0-0-6</b>	Credits: <b>6</b>	Contact Hrs: 74
ISA Marks: <b>70</b>	ESA Marks: 30	Total Marks: <b>100</b>
Teaching Hrs: 74	Exam Duration: 2 to 3 days	

### Course Content

Unit – 1	
<b>Chapter 0: Building Blocks</b> Understanding coding platforms and tools, Data Structures and Algorithms revisited	<b>06 hrs</b>
<b>Chapter 1: Strategies and Performance</b> Warm up problems, Parsing and Formatting text, Code performance analysis and tools	<b>06 hrs</b>
<b>Chapter 2: Advanced Data Structures</b> Matrix, Grids, Trees and variants, Lists, Skip lists, Hash, Trie and variants	<b>10 hrs</b>
<b>Chapter 3: Dynamic Programming</b> Memory functions, Optimization problems	<b>08 hrs</b>
Unit – 2	
<b>Chapter 4: Graph algorithms</b> Traversal Algorithms, Shortest Path Algorithms, Spanning Tree Algorithms and variants	<b>25 hrs</b>
<b>Chapter 5: Introduction to Computational Geometry</b> Points, Line Segments, Polygons and Basics of Geometric Problems	<b>05 hrs</b>
Unit – 3	
<b>Chapter 6: Problem Solving</b> Assortment of problems and techniques.	<b>14 hrs</b>

#### Text Book

1. Levitin A., “Introduction to the Design and Analysis of Algorithms”, Third Edition, Pearson Education, 2017.
2. Levitin A, Levitin M, “Algorithmic Puzzles”, First Edition, Oxford University Press, 2011.
3. Online Coding Platforms

#### References

1. Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, Clifford Stein, “Introduction to Algorithms”, Third Edition, MIT Press, 2010.



Program: <b>Bachelor of Engineering</b>		
Course Title: <b>Fuzzy Set Theory</b>		Course Code: <b>19ECSE402</b>
L-T-P: <b>3-0-0</b>	Credits: 3	Contact Hrs: <b>3hrs/week</b>
ISA Marks: 50	ESA Marks: 50	Total Marks: 100
Teaching Hrs: 40	Exam Duration: 3hrs	

**Unit –I**

1	<b>Introduction</b> : Introduction to Fuzzy Logic, Fuzzy Membership Functions, Operations on Fuzzy Sets	8hrs
2	<b>Fuzzy Measures:</b> Fuzzy Relations, Fuzzy Proposition, Fuzzy Implications, Fuzzy Inferences	8hrs

**Unit –II**

3	<b>Fuzzy Relations and Fuzzy Graphs:</b> Fuzzy Relations, Compositions of Fuzzy Relations, Properties of the Min-Max Composition, Defuzzification Techniques, Lambda-cut method, Weighted average method, Maxima methods, Centroid methods, Output of a Fuzzy System	8 hrs
4	<b>Uncertainty Modeling:</b> Application-oriented Modeling of Uncertainty, Causes of Uncertainty, Uncertainty Methods, Possibility Theory	8hrs

**Unit –III**

5	<b>Fuzzy Data Bases and Queries:</b> Introduction, Fuzzy Relational Databases, Fuzzy Queries in Crisp Databases	4 hrs
6	<b>Fuzzy Sets and Expert Systems:</b> Introduction to Expert Systems, Uncertainty Modeling in Expert Systems, Applications	4 hrs

**Text Books:**

1. H. J. Zimmermann ., Fuzzy Set Theory-and Its Applications, Fourth Edition, 4th Ed., Springer Science Business Media, LLC , 2001
2. Chander Mohan, An Introduction to Fuzzy Set Theory and Fuzzy Logic, 2nd ed. Vivo Books pvt ltd , 2015

**Reference Books:**

1. Timothy J. Ross, Fuzzy Logic With Engineering Applications, 3ed., 2010, A John Wiley and Sons, Ltd., Publication
2. Kumar S. Ray, Soft Computing and Its Applications: Fuzzy Reasoning and Fuzzy Control, 1st Edition, Apple Academic Press 2014
3. Ahmed M. Ibrahim, Fuzzy Logic for Embedded Systems Applications, Elsevier Press, 2004.



**Scheme for End Semester Assessment (ESA)**

<b>UNIT</b>	<b>8 Questions to be set of 20 Marks Each</b>	<b>Chapter Numbers</b>	<b>Instructions</b>
I	Q.No.-1, Q.No.-2, Q.No.-3	1, 2	Solve Any <b>2</b>
II	Q.No.-4, Q.No.-5, Q.No.-6	3,4	Solve Any <b>2</b>
III	Q.No.-7	5	Solve Any <b>1</b>
	Q.No.-8	6	



Program: <b>Bachelor of Engineering</b>		
Course Title: <b>Natural Language Processing</b>		Course Code: <b>18ECSE403</b>
L-T-P: <b>2-0-1</b>	Credits: <b>3</b>	Contact Hrs: <b>04 hrs/week</b>
ISA Marks: <b>50</b>	ESA Marks: <b>50</b>	Total Marks: <b>100</b>
Teaching Hrs: <b>30</b>	Exam Duration: <b>3 hrs</b>	

**Unit –I**

<b>1</b>	<b>Introduction to NLP and Deep Learning</b> Introduction to Natural Language Processing, Applications of Natural Language Processing, Word2vec introduction, Word2vec objective function gradients	<b>05 hrs</b>
<b>2</b>	<b>Dependency Parsing, Recurrent Neural Networks</b> Dependency Grammar , Neural dependency parsing, Recurrent Neural Networks and Language Models, Vanishing Gradients, Fancy RNNs	<b>07 hrs</b>

**Unit –II**

<b>3</b>	<b>Machine Translation, Seq2Seq and Attention</b> Machine Translation, Seq2Seq and Attention, Advanced Attention	<b>06 hrs</b>
<b>4</b>	<b>Transformer Networks , Coreference Resolution, Memory Networks</b> Transformer Networks and CNNs, Tree Recursive Neural Networks and Constituency Parsing , Advanced Architectures and Memory Networks	<b>06 hrs</b>

**Unit –III**

<b>5</b>	<b>Reinforcement Learning</b> Reinforcement Learning for NLP, Semi-supervised Learning for NLP, Future of NLP Models, Multi-task Learning and QA Systems	<b>06 hrs</b>
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**Text Books:**

- Yoav Goldberg. A Primer on Neural Network Models for Natural Language Processing , 2016.

**Reference Books:**

- Dan Jurafsky and James H. Martin. Speech and Language Processing (3rd ed. draft).  
 Ian Goodfellow, YoshuaBengio, and Aaron Courville. *Deep Learning*. MIT Press.

**Scheme for End Semester Assessment(ESA)**

UNIT	8 Questions to be set of 20 Marks Each	Chapter Numbers	Instructions
I	Q.No.-1, Q.No.-2, Q.No.-3	1, 2	Solve Any <b>2</b> out of <b>3</b>
II	Q.No.-4, Q.No.-5, Q.No.-6	4,5	Solve Any <b>2</b> out of <b>3</b>
III	Q.No.-7	6	Solve Any <b>1</b> out of <b>2</b>
	Q.No.-8		



Program: <b>Bachelor of Engineering</b>		
Course Title: <b>Advanced Parallel Computing</b>		Course Code: <b>18ECSE408</b>
L-T-P: <b>3-0-0</b>	Credits: <b>3</b>	Contact Hrs: <b>03 hrs/week</b>
CIE Marks: <b>50</b>	SEE Marks: <b>50</b>	Total Marks: <b>100</b>
Teaching Hrs: <b>40</b>	Exam Duration: <b>3 hrs</b>	

<b>Unit –I</b>		
<b>1</b>	<b>Introduction and History</b> GPUs as Parallel Computers; Architecture of a Modern GPU; Parallel Programming Languages and Models; Overarching Goals; Evolution of Graphics Pipelines; The Era of Fixed- Function ; Graphics Pipelines; Evolution of Programmable Real-Time Graphics; Unified Graphics and Computing Processors; GPGPU; An Intermediate Step; GPU Computing; Scalable GPUs Recent Developments; Future Trends.	<b>07 hrs</b>
<b>2</b>	<b>Introduction to CUDA</b> Data Parallelism; CUDA Program Structure; A Matrix-Matrix Multiplication Example; Device Memories and Data Transfer; Kernel Functions and Threading; Function declarations; Kernel launch; Predefined variables; Runtime API.CUDA Thread Organization; Using block Id x and thread Id x ; Synchronization and Transparent Scalability; Thread Assignment ; Thread Scheduling and Latency Tolerance.	<b>09 hrs</b>
<b>Unit –II</b>		
<b>3</b>	<b>CUDA Memories</b> Importance of Memory Access Efficiency; CUDA Device Memory Types; A Strategy for Reducing Global Memory Traffic; Memory as a Limiting Factor to Parallelism; Global Memory Bandwidth; Dynamic Partitioning of SM Resources; Data Prefetching; Instruction Mix; Thread Granularity; Measured Performance.	<b>07 hrs</b>
<b>4</b>	<b>Introduction to OPENCL</b> Introduction to OPENCL; Background; Data Parallelism Model; Device Architecture; Kernel Functions; Device Management and Kernel Launch; Electrostatic Potential Map in OpenCL.	<b>09 hrs</b>
<b>Unit –III</b>		
<b>5.</b>	<b>Case Study</b> Concepts of Game Design, Applications like Matrix multiplication, MRI reconstruction Molecular Visualization and Gaming.	<b>04 hrs</b>
<b>6.</b>	<b>Parallel Programming and Computational Thinking</b> Goals of Parallel Programming, Problem Decomposition, Algorithm Selection, Computational Thinking.	<b>04 hrs</b>





**Text Books:**

1. David B. Kirk, Wen-mei W. Hwu, “Programming Massively Parallel Processors: A Hands on Approach”, Morgan Kaufmann/Elsevier India reprint, 2010.

**Reference Books:**

1. Benedict R Gaster, Lee Howes, David Kaeli, Perhaad Mistry and Dana Schaa, “Heterogeneous Computing with OpenCL”, Morgan Kaufmann/Elsevier reprint, 2012.

**Scheme for End Semester Assessment(ESA)**

UNIT	8 Questions to be set of 20 Marks Each	Chapter Numbers	Instructions
I	Q.No.-1, Q.No.-2, Q.No.-3	1, 2	Solve Any 2 out of 3
II	Q.No.-4, Q.No.-5, Q.No.-6	3,4	Solve Any 2 out of 3
III	Q.No.-7	5	Solve Any 1 out of 2
	Q.No.-8	6	



<b>Program:</b> Bachelor of Engineering		
<b>Course Title:</b> <b>Wireless Ad Hoc and Sensor Networks</b>		<b>Course Code:</b> <b>18ECSE406</b>
<b>L-T-P:</b> <b>3-0-0</b>	<b>Credits:</b> <b>3</b>	<b>Contact Hrs:</b> <b>3hrs/week</b>
<b>CIE Marks:</b> <b>50</b>	<b>SEE Marks:</b> <b>50</b>	<b>Total Marks:</b> <b>100</b>
<b>Teaching Hrs:</b> <b>40 hrs</b>	<b>Exam Duration:</b> <b>3 hrs</b>	

**Unit –I**

<b>1</b>	<b>Introduction:</b> Fundamentals of wireless communication technology, Characteristics of wireless channel, Multiple Access Techniques, IEEE802.11 Standards, Bluetooth, Cellular Concept, Cellular Architecture.	<b>07 hrs</b>
<b>2</b>	<b>Ad hoc Networks:</b> Introduction, Issues in Ad hoc wireless networks, Ad hoc wireless internet.	<b>04 hrs</b>
<b>3</b>	<b>MAC Protocols:</b> Introduction, Issues in Designing MAC protocol, Design goals, Classification, Contention Based Protocols with Reservation Mechanisms. Contention-Based MAC Protocols with Scheduling Mechanism.	<b>05 hrs</b>

**Unit –II**

<b>4</b>	<b>Routing Protocols:</b> Introduction, Issues in designing a routing protocol, classification, Table drive routing protocol, On-demand routing protocol, Hybrid routing protocol, Hierarchical routing protocols, Power aware routing protocols.	<b>06 hrs</b>
<b>5</b>	<b>Energy Management:</b> Introduction, Need for Energy Management, Classification, Battery Management Scheme, Transmission Power Management Schemes, System Management Scheme.	<b>05 hrs</b>
<b>6</b>	<b>Sensor Networks:</b> Introduction, Architecture, Data Dissemination, Data Gathering, MAC Protocols (schedule based protocols).	<b>05 hrs</b>

**Unit –III**

<b>7</b>	<b>Routing Protocols for Sensor Networks:</b> Routing Characteristics, Routing Strategies, LEACH, SPIN.	<b>04 hrs</b>
<b>8</b>	<b>Sensor Network Applications:</b> Case Study: Traffic Control, Health Care, Green House Monitoring.	<b>04 hrs</b>

**Text Books:**

1. C. Siva Ram Murthy and B. S. Manoj, “Ad hoc Wireless Networks”, 2<sup>nd</sup> Edition, Pearson Education, 2006.
2. KazemSohraby, Daniel Minoli, TaiebZnati, “Wireless Sensor Networks: Technology, Protocols, and Applications”, John Wiley and Sons, 2007.

**Reference Books:**

1. Ozan K. Tonguz and Gianguigi Ferrari, “Ad hoc Wireless Networks”, John Wiley, 2006.
2. C.K. Toh, “Adhoc Mobile Wireless Networks”, Protocols and Systems, Prentice-Hall PTR, 2002.



UNIT	8 Questions to be set of 20 Marks Each	Chapter Numbers	Instructions
I	Q.No.-1, Q.No.-2, Q.No.-3	1, 2,3	Solve Any <b>2</b> out of <b>3</b>
II	Q.No.-4, Q.No.-5, Q.No.-6	4,5,6	Solve Any <b>2</b> out of <b>3</b>
III	Q.No.-7	7	Solve Any <b>1</b> out of <b>2</b>
	Q.No.-8	8	

Program: <b>Bachelor of Engineering</b>		
Course Title: <b>Software Architecture and Design Thinking</b>		Course Code: <b>18ECSE410</b>
L-T-P: <b>3-0-0</b>	Credits: <b>3</b>	Contact Hrs: <b>3hrs/week</b>
CIE Marks: <b>50</b>	SEE Marks: <b>50</b>	Total Marks: <b>100</b>
Teaching Hrs: <b>40</b>	Exam Duration: <b>3 hrs</b>	

Unit –I		
1	<b>Chapter No. 1 What Is Software Architecture?</b> What Software Architecture Is and What It Isn't ,Architectural Structures and Views, Architectural Patterns, What Makes a “Good” Architecture?	<b>5 hrs</b>
2	<b>Chapter No. 2 Why Is Software Architecture Important?</b> Inhibiting or Enabling a System’s Quality Attributes, Reasoning About and Managing Change, Predicting System Qualities, Enhancing Communication among Stakeholders, Carrying Early Design Decisions, Defining Constraints on an Implementation, Influencing the Organizational Structure, Enabling Evolutionary Prototyping, Improving Cost and Schedule Estimates, Supplying a Transferable, Reusable Model, Allowing Incorporation of Independently Developed Components, Restricting the Vocabulary of Design Alternatives, Providing a Basis for Training	<b>6hrs</b>
3	<b>Chapter No. 3 The Many Contexts of Software Architecture</b> Architecture in a Technical Context, Architecture in a Project Life-Cycle Context, Architecture in a Business Context, Architecture in a Professional Context, Stakeholders, How Is Architecture Influenced?,What Do Architectures Influence?	<b>5 hrs</b>
Unit –II		
4	<b>Chapter No. 4. Understanding Quality Attributes</b> Architecture and Requirements, Functionality, Quality Attribute Considerations, Specifying Quality Attribute Requirements, Achieving Quality Attributes through Tactics, Guiding Quality Design Decisions	<b>5 hrs</b>
5	<b>Chapter No. 5. Quality Attributes</b> Tactics for Availability, Tactics for Interoperability, Tactics for Modifiability,	<b>6hrs</b>



	Tactics for Performance, Tactics for Security, Tactics for Testability, Tactics for Usability,	
<b>6</b>	<b>Chapter No. 6. Architectural Tactics and Patterns</b> Architectural Patterns, Overview of the Patterns Catalog, Relationships between Tactics and Patterns, Using Tactics Together	<b>5 hrs</b>
<b>Unit –III</b>		
<b>5.</b>	<b>Chapter No. 7 Architecture and Requirements</b> Gathering ASRs from Requirements Documents, Gathering ASRs by Interviewing Stakeholders, Gathering ASRs by Understanding the Business Goals, Capturing ASRs in a Utility Tree, Tying the Methods Together	<b>4 hrs</b>
<b>6.</b>	<b>Chapter No. 8 Designing an Architecture, Implementation, Testing and Evaluation</b> <b>Designing:</b> Design Strategy, The Attribute-Driven Design Method, The Steps of ADD, Implementation, and Testing; <b>Architecture and Implementation,</b> Architecture and Testing, Evaluation: <b>Evaluation Factors,</b> The Architecture Tradeoff Analysis Method, Lightweight Architecture Evaluation	<b>4 hrs</b>
<b>Text Books:</b> 1. Len Bass, Paul Clements, Rick Kazman, Software Architecture in Practice (3rd Edition), Addison-Wesley Professional; 3 edition		
<b>Reference Books:</b>		

**Scheme for End Semester Assessment(ESA)**

UNIT	8 Questions to be set of 20 Marks Each	Chapter Numbers	Instructions
I	Q.No.-1, Q.No.-2, Q.No.-3	1, 2	Solve Any 2 out of 3
II	Q.No.-4, Q.No.-5, Q.No.-6	3,4	Solve Any 2 out of 3
III	Q.No.-7	5	Solve Any 1 out of 2
	Q.No.-8	6	



Program: <b>Bachelor of Engineering</b>		
Course Title: <b>Embedded Intelligent Systems</b>		Course Code: <b>18ECSE302</b>
L-T-P: 0-0-3	Credits: 3	Contact Hrs: <b>6hrs/week</b>
ISA Marks: 80	ESA Marks: 20	Total Marks: 100
Teaching Hrs: 60	Exam Duration: 3 hrs	

<b>1</b>	<b>Basics of embedded systems</b> Linux Application Programming, System V IPC, . Linux Kernel Internals and Architecture , Kernel Core , Linux Device Driver Programming, Interrupts & Timers , Sample shell script, application program, driver source build and execute	<b>10 hrs</b>
<b>2</b>	<b>Heterogeneous computing</b> Basics of heterogeneous computing with various hardware architectures designed for specific type of tasks, Advanced heterogeneous computing with a. Introduction to Parallel programming b.GPU programming ( OpenCL) c. Open standards for heterogeneous computing (Openvx) , Basic OpenCL examples - Coding, compilation and execution	<b>12 hrs</b>
<b>3</b>	<b>ML Frameworks lab with the target device</b> Caffe, tensorflow, TF Lite machine learning frameworks & architecture ,Model parsing, feature support and flexibility ,Supported layers , advantages and disadvantages with each of these frameworks, Android NN architecture overview , Full stack compilation and execution on embedded device	<b>16 hrs</b>
<b>4</b>	<b>Model Development and Optimization</b> Significance of on device AI ,Quantization , pruning, weight sharing, Distillation ,Various pre-trained networks and design considerations to choose a particular pre-trained model ,Federated Learning , Flexible Inferencing	<b>8 hrs</b>
<b>6</b>	<b>Android Anatomy</b> Android Architecture ,Linux Kernel , Binder , HAL Native Libraries , Android Runtime, Dalvik Application framework , Applications, IPC	<b>8 hrs</b>



Course Title: <b>Model Thinking</b>	Course Code: <b>18ECSE411</b>	
L-T-P: <b>3-0-0</b>	Credits: <b>3</b>	Contact Hrs: <b>30</b>
CIA Marks: <b>50</b>	SEE Marks: 50	Total Marks: <b>100</b>
Teaching Hrs: <b>40</b>		Exam Duration: <b>3 hrs</b>

<b>Content</b>	<b>40 Hrs</b>
Unit – 1	
<b>1. Why Model</b> Model Thinking - The need, Advantages and disadvantages, Segregation/Peer Effects, Case study	4 hrs
<b>2. Modeling People, Tipping Points &amp; Economic Growth</b> Rational models, Behavioral models, Rule based models, Percolation Models, Growth and its kinds	6 hrs
<b>3. Special Topics</b> Standing ovation model, Game of Life, Lyapunov Functions: Equilibrium, A cycle, Randomness or complexity, Coordination and culture. Urn models, Polya process, paths and networks, Prisoners' Dilemma, Collective Action & Mechanism Design	6 hrs
Unit – 2	
<b>4. Randomness and Learning Models</b> Luck as randomness, Random Walks & Colonel Blotto, Replicator Dynamics, Fisher's fundamental theorem, Prediction and the Many Model Thinker	8 hrs
<b>5. Model Checking and Modelling Concurrent Systems</b> Model Checking, Characteristics of Model Checking, Transition Systems, Parallelism and Communication, The State Space Explosion	8 hrs
Unit – 3	
<b>6. Linear-Time Properties</b> Linear-Time behavior, Safety Properties and Invariants, Liveness Properties, Fairness	4 hrs
<b>7. Regular Properties</b> Automata on Finite Words, Model-Checking Regular Safety Properties, Automata on Infinite Words, Model Checking with omega-regular properties	4 hrs

**Text Books**

1. Scott E Page, The Model Thinker, Basic Books Publication, 2018
2. Christel Baier and Joost-Pieter Katoen, Principles of Model Checking (Representation and Mind Series), The MIT Press, 2008

**References**

1. Model Thinking Coursera online course from Michigan University.



Program: <b>Bachelor of Engineering</b>		
Course Title <b>Scripting Languages Lab</b>		Course Code: <b>18ECSP201</b>
L-T-P: <b>0-0-2</b>	Credits: <b>2</b>	Contact Hrs: <b>4hrs/week</b>
ISA Marks: <b>80</b>	ESA Marks: <b>20</b>	Total Marks: <b>100</b>
Teaching Hrs: <b>30</b>	Exam Duration: <b>3 hrs</b>	

<b>1</b>	<b>Introduction to UNIX Utilities</b> Architecture, Commands, File Attributes, vi Editor, Process, Simple Filter, File System, Handling Files and Basic File Attributes.	<b>06hrs</b>
<b>2</b>	<b>UNIX shell Scripting</b> Shell Basics, Shell Environment, Shell Script Programming Concepts, Decision Structures, Looping Structures, and Command line arguments, Functions and Arrays, Regular Expression & Filters, Processes.	<b>06hrs</b>
<b>3</b>	<b>Python Scripting</b> Python: Types, Variables, and Simple I/O, Branching and Looping, String Manipulation, Numbers, Lists and Dictionaries, Regular Expressions, Functions, Files and Exceptions, Programming using numpy and scipy libraries.	<b>12hrs</b>
<b>4</b>	<b>System Administration</b> Common administrative tasks, creating and mounting file system, File system management, managing users and group accounts, monitoring system performance, accessing system information, backup and restore files, reconfiguration hardware with kudzu, installing and removing packages.	<b>06 hrs</b>

### Tentative plan of lab implementation

Expt./ Job No.	Lab assignments/experiment	No. of Lab. Slots per batch (estimate)
1-2	Introduction to UNIX Utilities	02
3-4	Shell Script	03
5-10	Python programming	05
11-12	System Administration	02

### Text Books

1. Sumitabha Das, "UNIX Concepts and Applications", 4<sup>th</sup> Edition, McGraw-Hill, 2017.
2. Mark Lutz, "Programming Python", 4<sup>th</sup> Edition, O'Reilly, 2010.

### Reference Books

1. Noah Gift, Jeremy Jones, Python for Unix and Linux System Administration, 2008.
2. RytisSileikam, Pro Python System Administration, 2<sup>nd</sup> Edition, 2014
3. Michael Dawson, Python Programming for the Absolute Beginner, Premier Press, 3<sup>rd</sup> Edition 2010.



<b>Program:</b> Bachelor of Engineering		
Course Title: Object Oriented Programming with C++		Course Code: 18ECSC207
L-T-P: <b>3-0-0</b>	Credits: <b>3</b>	Contact Hrs: <b>3 hrs/week</b>
ISA Marks: <b>50</b>	ESA Marks: <b>50</b>	Total Marks: <b>100</b>
Teaching Hrs: <b>40</b>	Exam Duration: <b>3hrs</b>	

<b>Unit –I</b>		
<b>1</b>	<b>Chapter No. 1: Introduction:</b> Introduction to object oriented programming. Characteristics of object oriented languages, Programming Basics, arrays, Functions in C++ (parameter passing techniques.)	<b>4 hrs</b>
<b>2</b>	<b>Chapter No. 2:Classes and Objects:</b> Introduction to Classes and Objects, encapsulation visibility modifiers, constructor and its types, nested classes, String class. UML diagrams to describe classes and relationships.	<b>6 hrs</b>
<b>3</b>	<b>Chapter No. 3:Inheritance:</b> Introduction, types of Inheritance, constructors, Abstract class,Aggregation: classes within classes	<b>6 hrs</b>
<b>Unit –II</b>		
<b>4</b>	<b>Chapter No. 4:Virtual Functions and Polymorphism:</b> Virtual functions, Friend functions, static functions, The ‘this’ pointer	<b>6 hrs</b>
<b>5</b>	<b>Chapter No. 5:Templates and Exception Handling:</b> Function and class templates.Introduction to exceptions, Throwing an Exception, Try Block, Exception Handler (Catching an Exception), Multiple exceptions. Exceptions with arguments	<b>6hrs</b>
<b>6</b>	<b>Chapter No. 6:Design Patterns:</b> Creational, Structural and Behavioural design patterns.	<b>4 hrs</b>
<b>Unit –III</b>		
<b>7</b>	<b>Chapter No. 7:Streams and Files:</b> Stream classes, File I/O with streams.	<b>4 hrs</b>
<b>8</b>	<b>Chapter No. 8:Standard Template Library:</b> container classes: Sequence and Associative Containers	<b>4 hrs</b>
<b>Textbooks</b>		
1. Robert Lafore, “Object oriented programming in C++”, 4 <sup>th</sup> Edition, Pearson education,		
<b>Reference Books</b>		
1. Lippman S B, Lajorie J, Moo B E, C++ Primer, 5ed, Addison Wesley, 2013.		
2. Herbert Schildt: The Complete Reference C++, 4th Edition, Tata McGraw Hill		





<b>Course Code: 18ECSC206</b>	<b>Course Title: Microcontroller Programming &amp; Interfacing</b>	
<b>L-T-P-SS: 3-0-1</b>	<b>Credits: 4</b>	<b>Contact Hrs: 3+2 hrs</b>
<b>ISA Marks: 50</b>	<b>ESA Marks: 50</b>	<b>Total Marks: 100</b>
<b>Teaching Hrs: 40</b>		<b>Exam Duration: 3 hrs</b>

<b>Content</b>	<b>Hrs</b>
<b>Unit – I</b>	
<b>Chapter No. 1. The 8051 Architecture</b> Introduction, 8051 Microcontroller hardware, input/output pins, ports & circuits, External memory,	04 hrs
<b>Chapter No. 2. Assembly Programming</b> Introduction, addressing modes, External Data Moves, Code Memory Read Only Data Moves / Indexed Addressing mode, PUSH and POP opcodes, Data exchanges, assembler directives, example programs. Byte level logical Operations, Bit level Logical Operations, Rotate and Swap Operations, Example Programs. Arithmetic Operations: Flags, Incrementing and Decrementing, Addition, Subtraction, Multiplication and Division, Decimal Arithmetic, Example Programs. The JUMP and CALL Program range, Jumps, Call and Subroutines, Example programs	12hrs +08 hrs (Lab)
<b>Unit – II</b>	
<b>Chapter No. 3. Timer/Counter &amp; Serial Port Programming.</b> C Data Types and Time delay computation in 8051 Counters and Timers, Programming 8051 Timers/counters in different modes, Basics of Serial Communication, RS232 standards, 8051 connection to RS232, 8051 serial port Programming.	12 hrs
<b>Chapter No. 4. Interrupts Programming</b> 8051 Interrupts, Programming Timer Interrupts, Programming external hardware interrupts, Programming the Serial Communication Interrupts, Interrupt Priority in the 8051, Interrupt programming.	04 hrs
	4 hrs
<b>Unit – III</b>	
<b>Chapter No. 5. Interfacing to Peripheral Devices</b> Interfacing 8051 to LEDs, DIP switches, BCD Decoder display, 7 Segment Display, LCD, Keypad, DAC, ADC, Stepper Motor and DC Motor	08hrs +12 hrs (Lab)



**Text Books (List of books as mentioned in the approved syllabus)**

2. Ayala.K.J, “The 8051 Microcontroller”, 3rd.,CENGAGE Learning, 2007.
3. Mazidi.M.A, Mazidi.J.G and McKinlay.R.D, “The 8051 Microcontroller and Embedded Systems- using Assembly and C”, 2ed, PHI 2006/Pearson, 2006.

**References**

1. Ayala.K.J., Gadre D.V., “The 8051 Microcontroller & Embedded Systems using Assembly and C”, 1ed., CENGAGE Learning, 2010
2. V. Udayashankara, M.S. Mallikarajunaswamy, ”8051 Microcontroller Hardware, Software and Applications”, 1ed., Tata McGraw Hill, 2009.

**Scheme for End Semester Assessment (ESA)**

UNIT	8 Questions to be set of 20 Marks Each	Chapter Numbers	Instructions
I	Q.No.-1, Q.No.-2, Q.No.-3	1 & 2	Solve Any 2 out of 3
II	Q.No.-4, Q.No.-5, Q.No.-6	3 &4	Solve Any 2 out of 3
III	Q.No.-7	5	Solve Any 1 out of 2
	Q.No.-8	5	



<b>Program:</b> Bachelor of Engineering		
<b>Course Title: Object Oriented Programming with C++ Lab</b>		<b>Course Code:</b> <b>18ECSP203</b>
<b>L-T-P: 0-0-1.5</b>	<b>Credits: 1.5</b>	<b>Contact Hrs: 3 hrs/week</b>
<b>ISA Marks: 80</b>	<b>ESA Marks: 20</b>	<b>Total Marks: 100</b>
<b>Teaching Hrs: 39</b>	<b>Exam Duration: 3hrs</b>	

<b>Experiments</b>	<b>Lab assignments/experiment</b>
2-Demonstration	Introduction to Code Blocks IDE (Integrated Development Environment), C++ programming basics.
4-Exercise	Classes and objects, Inheritance, Polymorphism, Templates and Exceptions Handling
2-Structured Enquiry	Classes and objects, Inheritance, Polymorphism, Templates and Exceptions Handling
1-Open Ended	Data types, Classes and Objects, Inheritance polymorphism, Exception Handling. Design patterns

**Text Book:**

1. Robert Lafore, "Object oriented programming in C++", 4<sup>th</sup> Edition, Pearson education.

**Reference Books:**

1. Lippman S B, Lajorie J, Moo B E, C++ Primer, 5ed, Addison Wesley, 2013.
2. Herbert Schildt: The Complete Reference C++, 4th Edition, Tata McGraw Hill
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**Evaluation :****Students Assessment Through CIE (80%) + SEE (20%)**

<b>Continuous Internal Evaluation (80%)</b>	<b>Assessment</b>	<b>Weightage in Marks</b>
	Exercises	40
Structured Enquiry	20	
Open Ended Experiment	20	
<b>Semester End Examination (20%)</b>	Structured Enquiry	<b>20</b>
	<b>Total</b>	<b>100</b>



<b>Program:</b> Bachelor of Engineering		
<b>Course Title:</b> JAVA Programming		<b>Course Code:</b> 19ECSP301
<b>L-T-P:1-0-1.5</b>	<b>Credits: 2.5</b>	<b>Contact Hrs:</b> 4 Hrs/week
<b>ISA Marks: 80</b>	<b>ESA Marks: 20</b>	<b>Total Marks: 100</b>
<b>Teaching Hrs: 52</b>	<b>Exam Duration: 3hrs</b>	
<b>Unit –I</b>		
<b>1</b>	<b>JAVA Language Fundamentals:</b> Java Features, Programming basics, Arrays and Strings, classes and objects	<b>4 Hrs</b>
<b>2</b>	<b>Inheritance:</b> Introduction, types of inheritance, static and dynamic polymorphism.	<b>2 Hrs</b>
<b>Unit –II</b>		
<b>3</b>	<b>Interfaces and Exception Handling:</b> Introduction, Create and implement interfaces, Exception handling,	<b>2 Hrs</b>
<b>4</b>	<b>Generics and Collections Frame work:</b> Introduction to generic programming, Collections: Interfaces: List, Set, Queue Classes: ArrayList, LinkedList and HashSet, Map	<b>2 Hrs</b>
<b>Unit –III</b>		
<b>5</b>	<b>Lambda Expressions:</b> Functional programming, Functional interface, Bulk operations on collections	<b>2hrs</b>
<b>6</b>	<b>Java Database Connectivity (JDBC):</b> Introduction, Drivers, Interfaces and classes to develop data base applications, case study	<b>2 Hrs</b>
<b>Text Books:</b>		
1. JAVA The Complete Reference, Herbert Schildt, 10th Ed, 2017, McGraw-Hill		
<b>Reference Book</b>		
1. Kathy Sierra and Bert Bates, Head First Java: A Brain-Friendly Guide, 2nd Edition, O'Reilly Media		
2. Introduction to Java Programming, Liang Y D, Pearson, 11 <sup>th</sup> Edition		



Program: <b>Bachelor of Engineering</b>		
Course Title: <b>Semantic Web</b>		Course Code: <b>19ECSE303</b>
L-T-P: <b>3-0-0</b>	Credits: <b>3</b>	Contact Hrs: <b>3hrs/week</b>
ISA Marks: <b>50</b>	ESA Marks: <b>50</b>	Total Marks: <b>100</b>
Teaching Hrs: <b>40</b>	Exam Duration: <b>03 hrs</b>	

<b>Unit –I</b>		
<b>1</b>	<b>Introduction to Semantics</b> History of the Web, Limitations, Vision of Semantic Web, Principles, Data Integration Across Web, Data Modeling Methods, Semantic Relationships, Metadata, Perpetual Data	<b>4 hrs</b>
<b>2</b>	<b>Expressing Meaning</b> Triple Store, Merging Graphs, Querying: Case Study	<b>4 hrs</b>
<b>3</b>	<b>Using Semantic Data</b> Query Language, Feed Forward Inference, Searching for Connections, Linked Data, Freebase	<b>8 hrs</b>
<b>Unit –II</b>		
<b>4</b>	<b>Working with Semantics</b> RDF—The Basis of the Semantic Web, OWL, Metadata with RDF, Metadata Taxonomies, Ontology	<b>8 hrs</b>
<b>5</b>	<b>Reasoning and Social Web</b> Reasoning types: Approximate Reasoning and Bounded Reasoning, Social Semantic Web, Semantic Crawlers	<b>8 hrs</b>
<b>Unit –III</b>		
<b>6</b>	<b>Semantic Modeling</b> Semantic Modeling, Semantic Web Applications, Logic for Semantic Web, Case Studies: Dr. Watson, Yahoo! SearchMonkey	<b>8 hrs</b>
<b>Text Books</b>		
1. Grigoris Antoniou, Paul Groth, Frank van Harmelen and Rinke Hoekstra, A Semantic Web Primer, MIT Press; 3rd edition, 2012.		
2. Toby Segaran, Colin Evans, and Jamie Taylor, Programming the Semantic Web: Build Flexible Applications with Graph Data, O'Reilly Media; 2 edition, July 2009.		
<b>Reference Books:</b>		
1. Pascal Hitzler, Markus Krötzsch, Sebastian Rudolph, Foundations of Semantic Web Technologies, Chapman and Hall; 1st edition, 2009.		
2. Dean Allemang, and James Hendler, Semantic Web for the Working Ontologist, Effective Modeling in RDFS and OWL, Morgan Kaufmann; 2nd edition, 2011.		
3. John Hebel, Matthew Fisher, Ryan Blace, Andrew Perez-Lopez, and Mike Dean (Foreword), Semantic Web Programming, Wiley Publishers, 1 edition 2009.		
<b>Scheme for End Semester Assessment (ESA)</b>		

UNIT	8 Questions to be set of 20 Marks Each	Chapter Numbers	Instructions
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I	Q.No.-1, Q.No.-2, Q.No.-3	1, 2,3	Solve Any 2
II	Q.No.-4, Q.No.-5, Q.No.-6	4,5	Solve Any 2
III	Q.No.-7	6	Solve Any 1
	Q.No.-8	6	

Course Title: Block Chain Technology		Course Code:19ECSE301
L-T-P: <b>2-0-1</b>	Credits: <b>3</b>	Contact Hrs: <b>3hrs/week</b>
ISA Marks: <b>50</b>	ESA Marks: <b>50</b>	Total Marks: <b>100</b>
Teaching Hrs: <b>40</b>	Exam Duration: <b>3 hrs</b>	

### Unit –I

<b>1</b>	<b>Introduction</b> Overview of Blockchain, History: Digital Money to Distributed Ledgers, Design Primitives: Protocols, Security, Consensus, Permissions, Privacy	<b>08 hrs</b>
<b>2</b>	<b>Blockchain Architecture and Design</b> Crypto primitives- Hash, Signature, Hashchain to Blockchain, basic consensus mechanisms, Requirements for the consensus protocols, Proof of Work, Proof of State, Scalability issues of consensus protocols	<b>08 hrs</b>

### Unit –II

<b>3</b>	<b>Blockchain Contracts</b> Financial Services, Crowdfunding, Bitcoin Prediction Markets, Smart Property, Smart Contracts, Blockchain Development Platforms and APIs, Blockchain Ecosystem: Decentralized Storage, Communication, and Computation	<b>08 hrs</b>
<b>4</b>	<b>Ethereum</b> Ethereum transactions, accounts, smart contracts, smart contract development, Solidity basics, basic contracts, distributed storage, Ethereum scaling	<b>08 hrs</b>

### Unit –III

<b>5</b>	<b>Blockchain Applications</b> Blockchain in Financial Software and Systems: Settlements, KYC, InsuranceBlockchain for Government: Digital identity, land records and other kinds of record keeping between government entities, public distribution system social welfare systems	<b>08hrs</b>
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#### Text Books:

- Melanie Swan, "Blockchain: Blueprint for New Economy", 1st Edition, O'Reilly Media, 2014.

#### Reference Books:

- ArshdeepBhaga, Vijay Madiseti, "Blockchain Applications: A Hands-On Approach", Paperback– January 31, 2017

#### Scheme for End Semester Assessment (ESA)

UNIT	8 Questions to be set of 20 Marks Each	Chapter Numbers	Instructions
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I	Q.No.-1, Q.No.-2, Q.No.-3	1,2	Solve Any 2
II	Q.No.-4, Q.No.-5, Q.No.-6	3,4	Solve Any 2
III	Q.No.-7, 8	5	Solve Any 1

<b>Course Title: The ARM Architecture</b>		<b>Coursecode:19ECSE302</b>
<b>L-T-P: 2-1-0</b>	<b>Credits: 3</b>	<b>Contact Hrs: 3hrs/week</b>
<b>ISA Marks: 50</b>	<b>ESA Marks: 50</b>	<b>Total Marks: 100</b>
<b>Teaching Hrs: 30</b>	<b>Exam Duration: 3 hrs</b>	

**Unit –I**

<b>1</b>	<b>ARM Embedded Systems and Processor Fundamentals</b> The RISC Design Philosophy , The ARM Design Philosophy, Embedded System Hardware, Embedded System Software, Registers, Current Program Status Register, Pipeline, Exceptions, Interrupts, and the Vector Table, Core Extensions, Architecture Revisions, ARM Processor Families	06 hrs
<b>2</b>	<b>Introduction to the ARM Instruction Set &amp; Assembly Programming</b> Data Processing Instructions, Branch Instructions, Load-Store Instructions, Software Interrupt Instruction, Program Status Register Instructions, Loading Constants, ARMv5E Extensions, Conditional Execution, Thumb instruction set.	06 hrs

**Unit –II**

<b>3</b>	<b>Efficient C Programming</b> Overview of C Compilers and Optimization, Basic C Data Types, C Looping Structures, Register Allocation, Function Calls, Pointer Aliasing, Structure Arrangement, Bit-fields, Unaligned Data and Endianness, Division.	06 hrs
<b>4</b>	<b>Writing and Optimizing ARM Assembly Code</b> Writing Assembly Code, Profiling and Cycle Counting, Instruction Scheduling, Register Allocation, Conditional Execution, Looping Constructs, Bit Manipulation, Efficient Switches, Handling Unaligned Data.	06 hrs

**Unit –III**

<b>5</b>	<b>Introduction to LPC-2148 controller</b> Input output Ports, Pin select registers, Input output select registers, direction control and control registers, Introduction to interfacing standards	03 hrs
<b>6</b>	<b>ARM Interfacing</b> ARM interfacing to peripherals like LED, LCD, Seven segments, Motors, Converters, Keypad.	03 hrs

**Text Books**

1. Andrew N.Sloss et al, ARM System Developer's Guide- Designing and Optimizing System Software

**Reference Books:**

1. Marilyn Wolf, Computers as Components: Principles of embedded computing system design, Morgan Ka, 2012
2. Steve Furber, ARM System-on-chip Architecture, 2, Pearson, 2000

**Tutorial Plan**

Expt./ Job No.	assignments/experiment	No. of Lab. Slots per batch (estimate)
1	ALP on arithmetic instructions set	01
2	ALP on logical instructions set	01
3	ALP on loop and branch instructions	01
4	Interface LED and Seven segments to ARM for displaying message.	01
5	Interface LCD to ARM for displaying message.	01
6	Interface Keypad to read the characters	01
7	Rotate DC and stepper motor for variable speed and direction	01
8	Interface DAC to ARM controller	01

**Scheme for End Semester Assessment (ESA)**

UNIT	8 Questions to be set of 20 Marks Each	Chapter Numbers	Instructions
I	Q.No.-1, Q.No.-2, Q.No.-3	1,2	Solve Any 2 out of 3
II	Q.No.-4, Q.No.-5, Q.No.-6	3,4	Solve Any 2 out of 3
III	Q.No.-7, 8	5	Solve Any 1 out of 2





Department of Computer Science & Engineering