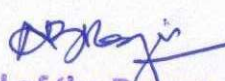

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**Curriculum Structure for the year 2016-20 batch
Semester: III**

Sl No	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P	ISA	ESA	Sub total			
1	15EMAB203	Integral Transforms and Statistics	4	0	0	50	50	100	4	4	3 hours
2	15EEEC201	Circuit Analysis	4	0	0	50	50	100	4	4	3 hours
3	15EEEC202	Analog Electronic Circuits	4	0	0	50	50	100	4	4	3 hours
4	17EEEC203	Digital Circuits	3	0	0	50	50	100	3	3	3 hours
5	17EEEF201	Engineering Design	0	0	3	80	20	100	3	6	3 hours
6	15EEEP201	Microcontroller Architecture & Programming	0	0	2	80	20	100	2	4	2 hours
7	15EEEP202	Analog Electronics Laboratory	0	0	1	80	20	100	1	2	2 hours
8	15EEEP203	Digital Electronics Laboratory	0	0	1	80	20	100	1	2	2 hours
TOTAL			15	0	7				22	29	

ISA: In-semester Assessment ESA: End Semester Assessment L: Lecture T: Tutorials P: Practical


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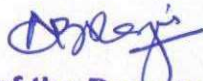

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Semester: IV

Sl No	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P	ISA	ESA	Sub total			
1	17EMAB208	Linear Algebra and Partial differential equations	4	0	0	50	50	100	4	4	3 hours
2	17EEEC201	Electrical Machines	3	0	0	80	20	100	3	3	3 hours
3	17EEEC204	Linear Control Systems	3	0	0	50	50	100	3	3	3 hours
4	15EEEC207	ARM Processor & Applications	3	0	0	50	50	100	3	3	3 hours
5	15EEEC208	Electrical Power Generation, Transmission & Distribution	3	0	0	50	50	100	3	3	3 hours
6	17EEEF202	Product Realization	0	0	2	80	20	100	2	4	2 hours
7	15EEEP204	Machines Lab	0	0	1	80	20	100	1	2	2 hours
8	15EEEP205	ARM Microcontroller Lab	0	0	1	80	20	100	1	2	2 hours
TOTAL			16	0	4				20	24	

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Curriculum Structure with Content- Course wise			


Semester: V

Sl. No	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P	ISA	ESA	Sub tota			
1	17EEEC301	Power Electronics	3	0	0	50	50	100	3	3	3 hours
2	17EEEC302	Power System Analysis	3	0	0	50	50	100	3	3	3 hours
3	17EEEC303	OS & Embedded Systems	3	0	0	50	50	100	3	3	3 hours
4	17EEEC304	Digital Signal Processing	3	0	0	50	50	100	3	3	3 hours
5	18EEEC301	Linear Integrated Circuits	3	0	0	50	50	100	2	4	2 hours
6	18EEEP301	Data Structure using C Lab	0	0	3	80	20	100	2	4	2 hours
7	18EEEP302	Control System Lab	0	0	1	80	20	100	1	4	2 hours
8	17EEEP306	RTOS Lab	0	0	1	80	20	100	1	4	2 hours
9	17EEEW301	Mini project	0	0	3	80	20	100	3	6	3 hours
TOTAL			15	0	8				23	31	

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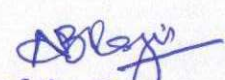

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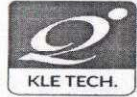
Semester: VI

Sr.N	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P	ISA	ESA	Sub total			
1	18EEEC302	Electric Drives & Control	2	0	1	50	50	100	3	3	3 hours
2	17EEEC307	Automotive Electronics	3	0	0	50	50	100	3	3	3 hours
3	17EEEC308	Machine Learning	2	0	1	50	50	100	3	3	3 hours
4	18EEEE301	Program Elective 1 C++	3	0	0	50	50	100	3	4	3 hours
5	17EEEE301	Program Elective 2 Digital Control Systems	3	0	0	50	50	100	3	3	3 hours
	18EEEE302	Modeling and Analysis of Hybrid Electrical Energy Systems									
6	HSC	HSC 1 (PALR)	3	0	0	50	50	100	3	3	3 hours
7	18EEEP303	Digital System Design using Verilog	0	0	2	80	20	100	2	4	2 hours
8	17EEEP305	Automotive Electronics Lab	0	0	1	80	20	100	1	2	2 hours
9	17EEEW302	Minor Project	0	0	6	80	20	100	6	12	3 hours
			16	0	11				27	37	

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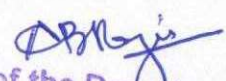

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**Curriculum Structure for the year 2016-20 batch
Semester: VII**

Sl No	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P	ISA	ESA	Sub Total			
1	17EEEC401	Switched Mode Power Converters	3	0	0	50	50	100	3	3	3 hours
2	19EEEC401	Power System Modeling, Operation & Control	3	0	0	50	50	100	3	3	3 hours
3	17EEEE401	AUTOSAR & Infotainment Systems	3	0	0	50	50	100	3	3	3 hours
4	17EEEE402	Electric Drive Vehicles	3	0	0	50	50	100	3	3	3 hours
5	17EEEE407	Model Based Real Time Control Systems									
6	17EEEE405	Smart Grid Technologies	3	0	0	50	50	100	3	3	3 hours
7	19EEEE401	FACTS									
8	17EEEE0401	Artificial Neural Networks	3	0	0	50	50	100	3	3	3 hours
9	19EEEE0401	Wind and PV Electrical Energy Systems									
10	15EHSC402	HSC 4(CIPE/EVS)	0	0	0	0	0	0	0	0	3 hours
11	19EEEP401	Power System Simulation Lab	0	0	1	80	20	100	1	2	2 hours
12	19EEEP402	Relay and High Voltage Engineering lab	0	0	1	80	20	100	1	2	2 hours
			18	0	2				20	22	

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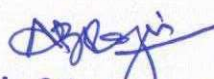

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Semester: VIII

Sr.N	Course code	Course Title		Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
				L	T	P	ISA	ESA	Sub tota			
1	19EEEP403	Embedded Linux	Industry Internship (0-0-6)	3	0	0	50	50	100	3	3	3 hours
3	17EEEO402	Artificial Intelligence		3	0	0	50	50	100	3	3	3 hours
4	18EEEW401	Capstone Project		0	0	14	50	50	100	14	28	3 hours
				6	0	14				20	34	

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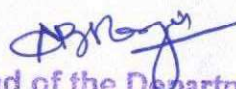
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Curriculum Structure for the year 2017-21 batch


Semester: III

Sr.No	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P			Sub total			
1	15EMAB203	Integral Transforms and Statistics	4	0	0	50	50	100	4	4	3 hours
2	15EEEC201	Circuit Analysis	4	0	0	50	50	100	4	4	3 hours
3	15EEEC202	Analog Electronic Circuits	4	0	0	50	50	100	4	4	3 hours
4	17EEEC203	Digital Circuits	3	0	0	50	50	100	3	3	3 hours
5	18EEEP201	Data structure using C	0	0	3	80	20	100	3	6	2 hours
6	15EEEP201	Microcontroller Architecture & Programming	0	0	2	80	20	100	2	4	2 hours
7	15EEEP202	Analog Electronics Laboratory	0	0	1	80	20	100	1	2	2 hours
8	15EEEP203	Digital Electronics Laboratory	0	0	1	80	20	100	1	2	2 hours
TOTAL			15	0	7				22	29	

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Semester: IV

Sr.No	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P			Sub total			
1	17EMAB208	Linear Algebra and Partial differential equations	4	0	0	50	50	100	4	4	3 hours
2	17EEEC201	Electrical Machines	3	0	0	80	20	100	3	3	3 hours
3	17EEEC204	Linear Control Systems	3	0	0	50	50	100	3	3	3 hours
4	15EEEC207	ARM Processor & Applications	3	0	0	50	50	100	3	3	3 hours
5	15EEEC208	Electrical Power Generation, Transmission & Distribution	3	0	0	50	50	100	3	3	3 hours
6	15EEEP205	ARM Microcontroller Lab	0	0	1	80	20	100	1	2	2 hours
7	18EEEP202	Control System Lab	0	0	1	80	20	100	1	2	2 hours
8	18EEEP203	Digital System Design using Verilog	0	0	2	80	20	100	2	4	2 hours
TOTAL			16	0	4				20	24	

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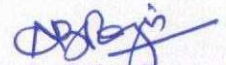
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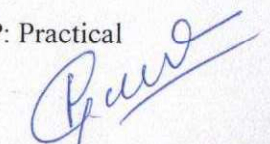
Curriculum Structure for the year 2017-21 batch


Semester: V

Sl. No	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P	ISA	ESA	Sub total			
1	17EEEC301	Power Electronics	3	0	0	50	50	100	3	3	3 hours
2	17EEEC302	Power System Analysis & Stability	3	0	0	50	50	100	3	3	3 hours
3	17EEEC303	OS & Embedded Systems	3	0	0	50	50	100	3	3	3 hours
4	17EEEC304	Digital Signal Processing	3	0	0	50	50	100	3	3	3 hours
5	18EEEC301	Linear Integrated Circuits	3	0	0	50	50	100	2	3	3 hours
6	19EEEC301	Machine Learning	2	0	1	50	50	100	3	3	3 hours
7	17EEEP306	RTOS Lab	0	0	1	80	20	100	1	2	2 hours
8	19EEEP301	Machines Lab	0	0	1	80	20	100	1	2	2 hours
9	17EEEW301	Mini project	0	0	3	80	20	100	3	6	3 hours
TOTAL			17	0	6				23	28	

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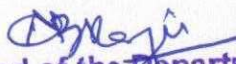

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Semester: VI

Sr.No	Course code	Course Title	Period			Evaluation scheme			Credit (L+T+P)	Contact Hours	Exam Duration
			L	T	P	ISA	ESA	Sub total			
1	19EEEC302	Electric Drives and Control	3	0	0	50	50	100	3	3	3 hours
2	17EEEC307	Automotive Electronics	3	0	0	50	50	100	3	3	3 hours
3	19EEEC303	Object Oriented Programming using C++	2	0	1	50	50	100	3	3	3 hours
4	19EEEE301	Program Elective 1 CMOS VLSI Circuits	3	0	0	50	50	100	3	4	3 hours
5	19EEEE302 18EEEE302	Program Elective 2 Battery Management Systems Modelling and Analysis of Hybrid Electrical Energy Systems	3	0	0	50	50	100	3	3	3 hours
6	16EHSC301	HSC 1 (PALR)	3	0	0	50	50	100	3	3	3 hours
7	19EEEP302	Electric Drives and Control Lab	0	0	1	80	20	100	1	2	2 hours
8	17EEEP305	Automotive Electronics Lab	0	0	1	80	20	100	1	2	2 hours
9	17EEEW302	Minor Project	0	0	6	80	20	100	6	12	3 hours
			17	0	9				26	35	

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