111.CMOS ASIC design

One Week Workshop
On
CMOS ASIC Design
for Faculty and UG students

A three Day intense workshop for reinforcing learning and practice.

A thorough understanding of the Complete ASIC Design Flow – GDSII, using Cadence Innovus tool and Calibre tool for DRC and LVS, which equips the faculty and students with the foundation, techniques, methodologies and skillsets, along with relevant hands–on.
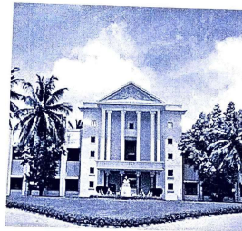
Overview of the Workshop:
The workshop is intended to give hands on experience of

1. Complete ASIC design flow using Cadence
2. P&R for ASIC Design
3. Physical Design for ASIC

About the Institution BVBCET

Established in 1947, BVBCET has achieved an enviable status due to a strong emphasis on academic and technical excellence. Spread over a luxurious 50 acres, the picture sque campus comprises of various buildings with striking architecture. A constant endeavour to keep abreast with technology has resulted in excellent state-of-the-art infrastructure that supplements every engineering discipline.

Currently college offers 12 UG and 8 PG programs affiliated Visvesvaraya Technological University, Belgaum and is recognized by AICTE, New Delhi and accredited by NBA.

About ECE Department

Department was established in 1979 & is the pride of Karnataka. The graduates from this department are playing a vital role in the IT revolution and are instrumental in placing Karnataka on the global IT landscape.

The department offers Post Graduate programs in "Digital Electronics" and "VLSI Design and Embedded System". Active engagement of faculty in research has led to recognition of department as a research center by the University.

E&C students are placed in major industries such as IBM, Motorola, Siemens, Alcatel, Intel, Microsoft, Bosch, KPIT etc.

Sankalp semiconductors Pvt. Ltd. Is housed in our campus which mentors the staff and students in the area of Analog and Mixed signal design.

The department is working with following industries which have given considerable input in the design of the curriculum in Automotive Electronics, VLSI, Communication and Networking; Robert Bosch, KPIT, Sankalp Semiconductor, ARM India Ltd, Juniper Network, CISCO.

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Resource persons
1. Mr. Marjunath T
   Senior Team Lead,
   Broadcom,
   Bangalore.

Chief Patron:
Dr. Ashok Shettar
Vice Chancellor, KLE Technological University, Hubli
Prof. B.L.Desai
Registrar, KLE Technological University, Hubli
Dr. P. G. Tewari,
Principal, BVBCET, Hubli

Organizing Committee
Prof. Shashidhar S N

Convener:
Dr. Nalini C Iyer
HOD, ECE- Department, BVBCET, Hubli.
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Co coordinator:
Prof. Suhas Shirol,
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School of Electronics & Communication Engineering

TEQIP III Sponsored

Faculty Development Program on CMOS ASIC Design

From 11th Sept to 16th Sept, 2017

Attendance List

Sl.No.	Name of the Participants	Affiliations	Signatures					
			11/09/2017	12/09/2017	13/09/2017	14/09/2017	15/09/2017	16/09/2017
1.								
2.	Satish. Chikkamath	KLE Tech.						
3.	Sanjay Elizar	KLE Tech						
4.	Preeti S.P	KLE Tech						
5.	Jyoti Patil Devaji	KLETech						
6.	Shamshuddin. K	KLE Tech						
7.	V. R. Mane	KLE tech						
8.	R. M. Shet	KLE Tech						
9.	Vishal. P	KLE Tech						
10.	Suhas B.S	KLE Tech						
11.	Dr. Sujata. S. Kotabagi	"						
12.	Shenshail P	KLE Tech						

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School of Electronics & Communication Engineering

	Name	Institution						
13.	Bhagyashree	KLE-TU						
14.	Rajeshwari. M	"						
15.	Nagaratna Shenbhag	KLE Tech						
16.	Nikita Patil	KLE Tech						
17.	V. S. Eliyal	KLE Tech						
18.	P. C. Nissimagudi	KLE Tech						
19.	Rohini H	BVBCET						
20.	Shivashankar A. Hunddar	KLE Tech						
21.	Shraddha. B. M	KLE TECH						
22.	R. V. Harya	"						
23.								
24.								
25.								
26.								
27.								
28.								
29.								
30.								