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# TRAINING PROGRAM On al & Analog VLSI Design flow

Digital & Analog VLSI Design flow using Cadence for Faculty and PG students

A three Day intense training program reinforcing learning and practice.

Equips the faculty and students with the foundation, techniques, methodologies and skillsets, along with relevant hands-on. A thorough understanding of the Complete VLSI Design flow, using Cadence tool.

#### RELEVANCE OF THE COURSE:

The knowledge of VLSI design is very essential in all facets of engineering applications in industries as well as in academia. Aim of the workshop is to give hands on experience using CADENCE tool to meet VLSI lab requirements. The knowledge of cadence tool used for VLSI design can be easily extended to design any other digital, analog and mixed signal applications.

#### About CADENCE

Cadence is an Electronic Design Automation (EDA) environment that allows integrating in a single framework different applications and tools (both proprietary and from other vendors), allowing to support all the stages of IC design and verification from a single environment. These tools are completely general, supporting different fabrication technologies. When a particular technology is selected, a set of configuration and technology-related files are employed for customizing the Cadence environment. This set of files is commonly referred to as a design kit.

#### About the Institution BVBCET

Established in 1947, BVBCET has achieved an enviable status due to a strong emphasis on academic and technical excellence. Spread over a luxurious 50 acres, the picture sque campus comprises of various buildings with striking architecture. A constant endeavour to keep abreast with technology has resulted in excellent state-of-the-art infrastructure that supplements every engineering discipline.



Currently college offers 12 UG and 8 PG programs affiliated Visvesvaraya Technological University, Belgaum and is recognised by AICTE, New Delhi and accredited by NBA.

"To know how to suggest is the art of teaching."

Henri-Frédéric Amiel

#### About ECE Department

Department was established in 1979 & is the pride of Karnataka. The graduates from this department are playing a vital role in the IT revolution and are instrumental in placing Karnataka on the global IT landscape.

The department offers Post Graduate programs in "Digital Electronics" and "VLSI Design and Embedded System". Active engagement of faculty in research has led to recognition of department as a research center by the University.

E&C students are placed in major industries such as IBM, Motorola, Siemens, Alcatel, Intel, Microsoft, Bosch, KPIT etc.

Sankalp semiconductors Pvt. Ltd. housed in our campus which mentors the staff and students in the area of Analog and Mixed signal design. Cadence full suite design tool is available at the department.

Robert Bosch is a partner for the curriculum in Automotive Electronics. ARM India Ltd. supports in embedded system design and development.



### "DIGITAL AND ANALOG VLSI DESIGN FLOW USING CADENCE", TEQIP SPONSPRED SEP 26-28, 2016 Attendance Sheet

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39.	TI	oti. R. H		AR-	JOH	Sec	DOP	OPE	april
40.	6	tain. K. Bhat	[VDES]	ekslaf	cocobe	exclose	okelo	ekslor	endo
41.		i. S. Hotkar	[VDES]	Potkou	Potted	Retter		4 Pottos	
42.	Shi	yu. Maned	[1058]	that	ma	do f	04	and	4
43.		Di B. MIXXELI	Titwi	2	(V)	12	Da .	The state of	
44.	Note	eta Bill	0,000	<i>Y</i>	- <b>XX</b> /	10,	10	10	1
45.		Katesh B		R-	(118)	TA	(KN)	1	1 2
46.	Par	rth. B.			10	N N		The state of the s	A
	ran	(10) . 1-3.		2	7			-41)	文

