

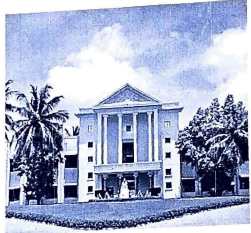


# 138. Technical Report writing in LaTeX and digital and analog VLSI design using Cadence

<p style="text-align: center;"><b>Registration Form</b> <b>2 Days Workshop for Faculty &amp; PG Students</b> On <b>"Technical report writing in LaTeX"</b></p> <p>Name of the Participant: .....</p> <p>Designation: .....</p> <p>Department: .....</p> <p>College Name: .....</p> <p>Address: .....</p> <p>Contact Number: .....</p> <p>Email id: .....</p> <p>DD No. with Date: .....</p> <p>Bank: .....</p> <p>Signature with seal Head of the institute</p> <p>Signature of the Applicant</p> <p><b>Payment Details:</b> DD (Rs 200/-) in the favor of "Principal, B.V.B College of Engineering &amp; Technology, Vidyanagar, Hubli, payable at Hubli - 580 031 to be submitted at the time of registration.</p> <p><b>Note:</b> Photocopies of this registration form may also be used. Registration Fee: Rs 200/-</p>	<p><b>Resource persons</b></p> <ol style="list-style-type: none"><li><b>Dr. Priyatamkumar</b> Professor and Head of ECE Dept. BVCET, Hubli.</li><li><b>Dr. Saroja V. Siddamal</b> Professor, ECE Dept. BVCET, Hubli.</li><li><b>Prof. Rohini Hongal</b> Asst. Professor, ECE Dept. BVCET, Hubli.</li><li><b>Prof. Sanjay Eligar</b> Asst. Professor, ECE Dept. BVCET, Hubli.</li><li><b>Prof. Suhas Shirol</b> Asst. Professor, ECE Dept. BVCET, Hubli.</li></ol> <p><b>Chief Patron:</b> <b>Dr. Ashok Shettar</b> Vice Chancellor, KLE Technological University, Hubli <b>Prof. B.L.Desai</b> Registrar, KLE Technological University, Hubli <b>Dr. P. G. Tewari,</b> Principa, BVCET, Hubli</p> <p><b>Organizing Committee</b> <b>Dr. Rajashekar B Shettar</b> <b>Prof. Arun L K</b> <b>Prof. Ramakrishna S. Joshi</b> <b>Prof. Shrishail M P</b> <b>Prof. Vasanth R.</b> <b>Prof. Anil K.</b> <b>Prof. Supriya K.</b></p> <p><b>Convener:</b> <b>Dr. Priyatamkumar</b> HOD, ECE- Department, BVCET, Hubli. Landline: 0836-2378250. Email: hod_ece@bvb.edu</p> <p><b>Co coordinator:</b> <b>Prof. Suhas Shirol,</b> Asst. Professor, ECE Department, BVCET Hubli. +91-9035203578. suhasshirol@bvb.edu</p>	<p style="text-align: center;">K. L. E Society's</p> <div style="text-align: center;"><p><b>B. V. Bhoomaraddi</b> College of Engineering &amp; Technology, Vidyanagar, Hubli - 580 031. <a href="http://www.bvb.edu">www.bvb.edu</a></p><div style="text-align: center;"><p><b>2 Days Training Program</b> On <b>"Technical report writing in LaTeX"</b> for Faculty &amp; PG Students Under TEQIP 23<sup>rd</sup> and 24<sup>th</sup> September 2016 Organized by Department of Electronics &amp; Communication Engineering Coordinator <b>Prof. Sanjay Eligar</b> Asst. Professor, ECE Department BVCET, Hubli.</p></div></div>
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# 138. Technical Report writing in LaTeX and digital and analog VLSI design using Cadence

<p style="text-align: center;"><b>Workshop On Technical report writing in LaTeX for Faculty and PG students</b></p> <p style="text-align: center;"><i>A three Day intense workshop for reinforcing learning and practice.</i></p> <p style="text-align: center;"><i>A thorough understanding of the Complete report writing, using LaTeX tool which equips the faculty and students with the foundation, techniques, methodologies and skillsets, along with relevant hands-on.</i></p> <p><b>Overview of the Workshop:</b> The workshop is intended to give hands on experience of report writing and explore the capabilities of LaTeX tool. LaTeX is a document preparation system for high-quality typesetting. It is most often used for medium-to-large technical or scientific documents but it can be used for almost any form of publishing. The goal of workshop is to meet following objectives.</p> <ul style="list-style-type: none"><li>• To train PG students to give insight of technical report writing.</li><li>• To motivate faculty and students to involve in research report and paper writing.</li></ul>	<p style="text-align: center;"><b>About the Institution BVBCET</b></p> <p>Established in 1947, BVBCET has achieved an enviable status due to a strong emphasis on academic and technical excellence. Spread over a luxurious 50 acres, the picturesque campus comprises of various buildings with striking architecture. A constant endeavour to keep abreast with technology has resulted in excellent state-of-the-art infrastructure that supplements every engineering discipline.</p>  <p>Currently college offers 12 UG and 8 PG programs affiliated Vivesvaraya Technological University, Belgaum and is recognized by AICTE, New Delhi and accredited by NBA.</p>	<p style="text-align: center;"><b>About ECE Department</b></p> <p>Department was established in 1979 &amp; is the pride of Karnataka. The graduates from this department are playing a vital role in the IT revolution and are instrumental in placing Karnataka on the global IT landscape.</p> <p>The department offers Post Graduate programs in "Digital Electronics" and "VLSI Design and Embedded System". Active engagement of faculty in research has led to recognition of department as a research center by the University.</p> <p>E&amp;C students are placed in major industries such as IBM, Motorola, Siemens, Alcatel, Intel, Microsoft, Bosch, KPIT etc.</p> <p>Sankalp semiconductors Pvt. Ltd. is housed in our campus which mentors the staff and students in the area of Analog and Mixed signal design.</p> <p>The department is working with following industries which have given considerable input in the design of the curriculum in Automotive Electronics, VLSI, Communication and Networking; Robert Bosch, KPIT, Sankalp Semiconductor, ARM India Ltd., Juniper Network, CISCO.</p>
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HOD, ECE- Department, BVBCET, HOD, Registration. Email: hod\_ece@bvb.edu

Asst. Professor, ECE Department

# 138. Technical Report writing in LaTeX and digital and analog VLSI design using Cadence

"TECHNICAL REPORT WRITING USING LATEX", TEQIP SPONSORED  
SEP 23-24, 2016  
Attendance Sheet

Sl.No.	Name of the participant	23-09-2016		24-09-2016	
		M	E	M	E
1.	PRIYADARSHINI.S (CDE)	Present	Present	Present	Present
2.	VAISHNAVI.M (DE)	Present	Present	Present	Present
3.	Nikhita Matti (CDE)	Present	Present	Present	Present
4.	Pooja.B.T (VLSI)	Present	Present	Present	Present
5.	Lavanya Nupak (VLSI)	Present	Present	Present	Present
6.	Ashwini P. Gadage (VLSI)	Present	Present	Present	Present
7.	Mangala N.R. (VLSI)	Present	Present	Present	Present
8.	Pratima Ghattaski (DE)	Present	Present	Present	Present
9.	Pavitra Kulkarni (DE)	Present	Present	Present	Present
10.	Roopa. Bhatnagar (DE)	Present	Present	Present	Present
11.	Sneha. Chakoti (DE)	Present	Present	Present	Present
12.	Suma Shinde (DE)	Present	Present	Present	Present
13.	Keerti Channaraddi (DE)	Present	Present	Present	Present
14.	Ravli. S. Hotkar (VDES)	Present	Present	Present	Present
15.	Goutam. K. Bhat (VDES)	Present	Present	Present	Present
16.	Shivalingappa. mared (VDES)	Present	Present	Present	Present
17.	Meghana. Padayatti (VDES)	Present	Present	Present	Present
18.	Shweta. Anjali (VDES)	Present	Present	Present	Present
19.	Jyoti. R. Hirekumbi (VDES)	Present	Present	Present	Present
20.	Shilpa. Ambiger (VDES)	Present	Present	Present	Present
21.	Supartha. M. Shetty (DE)	Present	Present	Present	Present

## 138. Technical Report writing in LaTeX and digital and analog VLSI design using Cadence

S.NO.	Name of the participant	23-09-2016		24-09-2016	
		M	E	M	E
22.	Tejashwini C.R	<del>Tejashwini</del>	<del>Tejashwini</del>	<del>Tejashwini</del>	<del>Tejashwini</del>
23.	Ratnak J. Hasmak (VDES)	<del>Ratnak</del>	<del>Ratnak</del>	<del>Ratnak</del>	<del>Ratnak</del>
24.	Jayalaxmi N. D (VDES)	<del>Jayalaxmi</del>	<del>Jayalaxmi</del>	<del>Jayalaxmi</del>	<del>Jayalaxmi</del>
25.	Vidyalakshmi M. Muralakshmi (DE)	<del>Vidyalakshmi</del>	<del>Vidyalakshmi</del>	<del>Vidyalakshmi</del>	<del>Vidyalakshmi</del>
26.	Pooja K. Gadli (VDES)	<del>Pooja</del>	<del>Pooja</del>	<del>Pooja</del>	<del>Pooja</del>
27.	Ranya H.S. (CDE)	<del>Ranya</del>	<del>Ranya</del>	<del>Ranya</del>	<del>Ranya</del>
28.	Atshatha Mahajan (CDE)	<del>Atshatha</del>	<del>Atshatha</del>	<del>Atshatha</del>	<del>Atshatha</del>
29.	Aniulya Lakshmi (D.E)	<del>Aniulya</del>	<del>Aniulya</del>	<del>Aniulya</del>	<del>Aniulya</del>
30.	Sneha Bendre (D.E)	<del>Sneha</del>	<del>Sneha</del>	<del>Sneha</del>	<del>Sneha</del>
31.	Ashwini Timmaragouda (DE)	<del>Ashwini</del>	<del>Ashwini</del>	<del>Ashwini</del>	<del>Ashwini</del>
32.	Pooja Appanavar (VDES)	<del>Pooja</del>	<del>Pooja</del>	<del>Pooja</del>	<del>Pooja</del>
33.	Chaitra A. Senatti (D.E)	<del>Chaitra</del>	<del>Chaitra</del>	<del>Chaitra</del>	<del>Chaitra</del>
34.	Ravi S. Hatkar (VDES)	<del>Ravi</del>	<del>Ravi</del>	<del>Ravi</del>	<del>Ravi</del>
35.	Abhishek Motaji (VDES)	<del>Abhishek</del>	<del>Abhishek</del>	<del>Abhishek</del>	<del>Abhishek</del>
36.	Arundhati R. Sankaragouda	<del>Arundhati</del>	<del>Arundhati</del>	<del>Arundhati</del>	<del>Arundhati</del>
37.	Keerthi V. Kulkarni	<del>Keerthi</del>	<del>Keerthi</del>	<del>Keerthi</del>	<del>Keerthi</del>
38.	Priya Kasngal	<del>Priya</del>	<del>Priya</del>	<del>Priya</del>	<del>Priya</del>
39.	Shreeta J. Hiranath	<del>Shreeta</del>	<del>Shreeta</del>	<del>Shreeta</del>	<del>Shreeta</del>
40.	Asma Begum Kustogi	<del>Asma</del>	<del>Asma</del>	<del>Asma</del>	<del>Asma</del>
41.	Bhagyashree R	<del>Bhagyashree</del>	<del>Bhagyashree</del>	<del>Bhagyashree</del>	<del>Bhagyashree</del>
42.	Shradha B.H	<del>Shradha</del>	<del>Shradha</del>	<del>Shradha</del>	<del>Shradha</del>
43.	PREETI JITURI	<del>Preeti</del>	<del>Preeti</del>	<del>Preeti</del>	<del>Preeti</del>
44.	Ranjith B	<del>Ranjith</del>	<del>Ranjith</del>	<del>Ranjith</del>	<del>Ranjith</del>
45.	Venkatesh B	<del>Venkatesh</del>	<del>Venkatesh</del>	<del>Venkatesh</del>	<del>Venkatesh</del>
46.	Niketa Patel	<del>Niketa</del>	<del>Niketa</del>	<del>Niketa</del>	<del>Niketa</del>