139.Real time System development on ARM platform using RTX Kernel and custom IC design using Cadence Virtuso IC 6

Registration Form

One Week Faculty Development Programme
On
"Real Time System Development on ARM Platform using RTX Kernel" & "Custom IC Design using Cadence Virtuoso IC6"

Name of the Participant: ....

Designation: ....

Department: ....

College Name: ....

Address: ....

Contact Number: ....

Email id: ....

DD No. with Date...

Bank: ....

Signature with seal   Signature of the Applicant
Head of the institute

Payment Details:
DD (Rs 200/-) in the favor of "Principal, B.V.B College of Engineering & Technology, Vidyanagar, Hubli, payable at Hubli - 580 031 to be submitted at the time of registration.

te:
tocopies of this registration form may also be used.
istration Fee: Rs 200/-

Resource persons
1. Dr. PriyatamKumar
   Professor and Head of ECE Dept.
   BVB CET, Hubli.
2. Dr. Rajashekar B. Shettar
   Professor, ECE Dept.
   BVB CET, Hubli.
3. Prof. Rohini S. H
   Asst. Professor, ECE Dept.
   BVB CET, Hubli.
4. Prof. Ramakrishna. S
   Asst. Professor, ECE Dept.
   BVB CET, Hubli.
5. Prof. Sanjay Eligar
   Asst. Professor, ECE Dept.
   BVB CET, Hubli.
6. Prof. Suhas B. Shirol
   Asst. Professor, ECE Dept.
   BVB CET, Hubli.
7. Prof. Shrishail P
   Asst. Professor, ECE Dept.
   BVB CET, Hubli.
8. Prof. Supriya K, Lecturer, ECE Dept., BVBCET.

Chief Patron:
Dr. Ashok Shettar
Vice Chancellor, KLE Technological University, Hubli
Prof. B.L.Desai
Registrar, KLE Technological University, Hubli.
Dr. P. G. Tewari,
Principal, BVBCET, Hubli

Organizing Committee
Prof. Sujata K   Prof. Kiran M R
Prof. Arun L K   Prof. Prashant V A
Prof. Shivraj H   Prof. Vasanth R
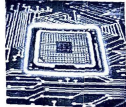Prof. Hemanth K   Prof. Supriya K

Convener:
Dr. PriyatamKumar
HOD, ECE- Department, BVBCET, Hubli.
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Co coordinator:
Prof. Shrishail Pattanashetti, Asst. Professor, ECE Department, BVBCET Hubli. +91-9880982395.
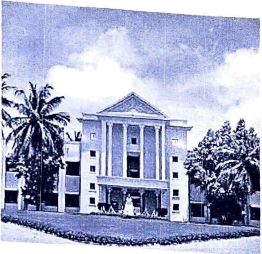shrishail@bvb.edu

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K. L. E Society's

B. V. Bhoomaraddi
College of Engineering & Technology,
Vidyanagar, Hubli - 580 031.
www.bvb.edu

One Week Faculty Development Programme
On
"Real Time System Development on ARM Platform using RTX Kernel" & "Custom IC Design using Cadence Virtuoso IC6"

Under TEQIP
20th to 25th October 2016

Organized by
Department of
Electronics & Communication Engineering

Co-ordinator
Prof. Rohini S. H
Asst. Professor, ECE Department
BVBCET, Hubli.

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Faculty Development Programee
On
Real Time System Development on ARM Platform using RTX Kernel" & "Custom IC Design using Cadence Virtuoso IC6

One week FDP for reinforcing learning and practice. A thorough understanding of the Complete Real Time System Development on ARM Platform using RTX Kernel & VLSI Design flow, using Cadence Virtuoso IC6 tool which equips the faculty with the foundation, techniques, methodologies and skillsets, along with relevant hands–on in respective areas.

Overview of the Workshop:
The workshop is intended to give summary of Real Time System Development on ARM Platform using RTX Kernel & VLSI design flow and explore the capabilities of Cadence Virtuoso IC6 tool. Cadence is an Electronic Design Automation (EDA) environment that allows integrating in a single framework different applications and tools (both proprietary and from other vendors), allowing to support all the stages of IC design and verification from a single environment. The program is designed in such a way that, to give the flavor of front end design to back end design through hands on experience. The workshop is focuses on understanding digital and analog VLSI design flow through set of experiments. The goal of workshop is to meet following objectives,

- To train Faculty to give insight of RTOS concepts & VLSI design flow using Virtuoso IC6
- To motivate faculty to involve in research activities.

About the Institution BVBCET

Established in 1947, BVBCET has achieved an enviable status due to a strong emphasis on academic and technical excellence. Spread over a luxurious 50 acres, the picture sque campus comprises of various buildings with striking architecture. A constant endeavor to keep abreast with technology has resulted in excellent state-of-the-art infrastructure that supplements every engineering discipline.

Currently college offers 12 UG and 8 PG programs affiliated Visvesvaraya Technological University, Belgaum and is recognized by AICTE, New Delhi and accredited by NBA.

About ECE Department

Department was established in 19. the pride of Karnataka. The graduates from department are playing a vital role in the revolution and are instrumental in plac Karnataka on the global IT landscape.

The department offers Post Graduate programs in "Digital Electronics" and "VLSI Design and Embedded System". Active engagement of faculty in research has led to recognition of department as a research center by the University.

E&C students are placed in major industries such as IBM, Motorola, Siemens, Alcatel, Intel, Microsoft, Bosch, KPIT etc.

Sankalp semiconductors Pvt. Ltd. Is housed in our campus which mentors the staff and students in the area of Analog and Mixed signal design.

The department is working with following industries which have given considerable input in the design of the curriculum in Automotive Electronics, VLSI, Communication and Networking; Robert Bosch, KPIT, Sankalp Semiconductor, ARM India Ltd., Juniper Network, CISCO.

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FDP on "Real Time System Development on ARM platform using RTX Kernel"
20th -22th Oct 2016

Attendance Sheet

Sl.No.	Name of the participant	20/10/16		21/10/16		22/10/16	
		M	E	M	E	M	E
1.	SONIA S. KUWELKAR	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*
2.	Asmabegum. kostagi	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*
3.	Keerthi. V. Kulkarni	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*
4.	Arundhati. R. Sankanagoudae	A.R.Sa	A.R.Sa	A.R.Sa	A.R.Sa	A.R.Sa	A.K.Sa
5	Preeti Tituri	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*
6.	bhagyashree k	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*
7.	Basavaraj-Rabakari	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*
8	PRIYA. KARUGAL	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*
9	Nikita Patil	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*
10	Sheetal Hiremath	*sign*	*sign*	*sign*	*sign*	*sign*	*sign*

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28. M.S. Patil

29. Shashidhar S N

30. Anuradha B

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FDP on "*Custom IC Design using Cadence Virtuoso IC6* "
24th -25th Oct 2016

Attendance Sheet

Sl.No.	Name of the participant	24/10/16		25/10/16	
		M	E	M	E
1	Anil.M.Kashue				
2	Goutam. K. Bhat	GKBhat	GKBhat	GKBhat	GKBhat
3	RAVI. S. HOTKAR	Hotkar	Hotkar	Hotkar	Hotkar
4	Abhishek Motagi				
5	Prateek. J. Hiremath				
6	Basavaraj Rabakavi	BB	BB	BB	BB
7	Shivalingappa .Maved				
8	Mangala N.R	Mangala NR	Mangala NR	Mangala NR	Mangala NR
9	Asmabegum. Kostagi				
10	Arundhati. Sankanagoudar	A.R.Su..	A.R.Su..	A.R.Su..	A.R.Su..

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11	Priya. Karagal				
12	Shital Hiremath.				
13	Bhagyashree K				
14	Nikita Patel				
15	Pooja. B. Tippareddy				
16	Pooja. K. Gulle				
17	Ashwini. P. Badiger				
18	Lavanya. Nayak				
19	Jyoti. Hisekumbi				
20	Tejashwini. C.R				
21	Jayalaxmi. N. Dhanyal				
22	Pooja. Y. Appanavar				
23	Shilpa. Ambiger				
24	Shweta. Angadi				

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No.	Name				
25.	Sonia S. Kuveltor				
26	Preeti Tiluri				
27	Keerthi. Kulkarni				
28	Supriya Kabur				
29	Anuradha Kushtal				
30	Vasanth Reddy				
31	Anusha.m.S				
32	Ravi Rayann				
33.	Ranjith B				
34	Venkatesh B				
35	Kiran.m.k				
36	Hemavandraj M.kelagundi				
37	Soumya S pati				
38	NAGARATA B.G				
39	Ramakrishna S				
40	V.S. Eljan				
41	Shashidhar S.N				