


142.CMOS VLSI Design – Flipped Class

<p style="text-align: center;">Registration Form</p> <p style="text-align: center;">5 days Faculty Development Programme On "CMOS VLSI Design – Flipped Class"</p> <p>Name of the Participant:</p> <p>Designation:</p> <p>Department:</p> <p>College Name:</p> <p>Address:</p> <p>Contact Number:</p> <p>Email id:</p> <p>DD No. with Date:</p> <p>Bank:</p> <p>Signature with seal Head of the institute</p> <p>Signature of the Applicant</p> <p>Last date for registration: 25-06-2017</p> <p>Note: Photocopies of this registration form may also be used. No fees for registration.</p>	<p>Resource persons Vivek Pawar CEO, Sankalp Semiconductor PVT LTD Shivakumar, Team Lead, Sankalp Semiconductor PVT LTD P.K. Sundarajan Sankalp Semiconductor PVT LTD</p> <p>Chief Patron: Dr. Ashok Shettar Vice-Chancellor, KLE Technological University, Hubli Prof. B.L. Desai Registrar, KLE Technological University, Hubli. Dr. P. G. Tewari, Principal, BVBCET, Hubli</p> <p>Organizing Committee Prof. Suhas Shirol Asst. Professor, KLE technological university, Hubli</p> <p>Convener: Dr. Nalini C. Iyer HOS, School of Electronics and Communication Engineering, KLE technological university, Hubli</p> <p>Co coordinator: Suhas Shirol Asst. Professor, School of Electronics and Communication Engineering, KLE technological university, Hubli</p>	<p style="text-align: center;">K. L. E Society's</p> <div style="text-align: center;">  <p>KLE Technological University Creating Value Leveraging Knowledge</p> </div> <p style="text-align: center;">Earlier known as B. V. Ehaomaraddi College of Engineering & Technology, Vidyaragar, Hubli - 580 031. www.bvb.edu</p> <p style="text-align: center;">5 days Faculty Development Programme On "CMOS VLSI Design – Flipped Class" Under TEQIP</p> <p style="text-align: center;">26th June to 8th July 2017</p> <p style="text-align: center;">Organized by Department of Electronics & Communication Engineering</p> <p style="text-align: center;">Co-ordinator Suhas Shirol Asst. Professor, School of Electronics and Communication Engineering, KLE technological university, Hubli</p>
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Faculty Development Programme On "CMOS VLSI Design – Flipped Class"

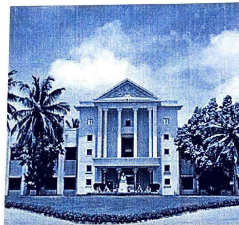
Overview of the Workshop:

The goal of workshop is to meet following objectives,

- Self-regulating learners have a clear set of criteria against which to judge their learning progress.
- write down all the things you'd like students to be able to do.
- Ready-made topic list for timed assessments like tests and the final exam.
- Use of Bloom's Taxonomy to measure the student level of learning.

About the Institution BVBCET

Established in 1947, BVBCET has achieved an enviable status due to a strong emphasis on academic and technical excellence. Spread over a luxurious 50 acres, the picturesque campus comprises of various buildings with striking architecture. A constant endeavor to keep abreast with technology has resulted in excellent state-of-the-art infrastructure that supplements every engineering discipline.



Currently college offers 12 UG and 8 PG programs affiliated Visvesvaraya Technological University, Belgaum and is recognized by AICTE, New Delhi and accredited by NBA.

About ECE Department

Department was established in 1979 & is the pride of Karnataka. The graduates from this department are playing a vital role in the IT revolution and are instrumental in placing Karnataka on the global IT landscape.


The department offers Post Graduate programs in "Digital Electronics" and "VLSI Design and Embedded System". Active engagement of faculty in research has led to recognition of department as a research center by the University.

E&C students are placed in major industries such as IBM, Motorola, Siemens, Alcatel, Intel, Microsoft, Bosch, KPIT etc.

Sankalp semiconductors Pvt. Ltd. is housed in our campus which mentors the staff and students in the area of Analog and Mixed signal design.

The department is working with following industries which have given considerable input in the design of the curriculum in Automotive Electronics, VLSI, Communication and Networking; Robert Bosch, KPIT, Sankalp Semiconductor, ARM India Ltd., Juniper Network, CISCO.

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<p align="center">Registration Form</p> <p align="center">Training On "Internet of Things"</p> <p>Name of the Participant:</p> <p>Designation:</p> <p>Department:</p> <p>College Name:</p> <p>Address:</p> <p>Contact Number:</p> <p>Email id:</p> <p>DD No. with Date:</p> <p>Bank:</p> <p>Signature with seal Head of the Institute</p> <p>Signature of the Applicant</p> <p>Last date for registration: 15-07-2017</p> <p>Note: • Photocopies of this registration form may also be used. • No fees for registration.</p>	<p>Resource persons Ramraj Srinivasan, Preva Systems LTD</p> <p>Chief Patron: Dr. Ashok Shettar Vice Chancellor, KLE Technological University, Hubli</p> <p>Prof. B.L.Desai Registrar, KLE Technological University, Hubli.</p> <p>Dr. P. G. Tewari, Principa, BVBCET, Hubli</p> <p>Organising Committee Prof. Saroja V.Siddamal Associate Professor, KLE technological university, Hubli</p> <p>Convener: Dr. Nalini C. Iyer HOS, School of Electronics and Communication Engineering, KLE technological university, Hubli</p>	<p align="center">K. L. E Society's</p> <p align="center"> KLE Technological University <small>Creating Value Leveraging Knowledge</small></p> <p align="center">Earlier known as B. V. Bhoomaraddi College of Engineering & Technology, Vidyanagar, Hubli - 580 031. www.avb.edu</p> <p align="center">Training On "Internet of Things" Under TEQIP 17th to 18th July and 24th to 26th July 2017 Organized by Department of Electronics & Communication Engineering</p> <p align="center">Co-ordinator Dr. Nalini C. Iyer HOS, School of Electronics and Communication Engineering, KLE technological university, Hubli</p>
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KLE Technological University
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School of Electronics & Communication Engineering

TEQIP III Sponsored

Faculty Development Programme on CMOS VLSI Flipped Class

26th June to 08th July, 2017

Attendance List

Sl. No.	Name of the Participants	Affiliations	Signatures						
			Days						
			26-06	27-06	28-06	29-06	30-06	01-07	
1.	Dr. R. B. Shettar	KLE	RS	RS	RS	RS	RS	RS	RS
2.	Saroja V.S	KLE	SS	SS	SS	SS	SS	SS	SS
3.	Sanjay E	KLE	SE	SE	SE	SE	SE	SE	SE
4.	Shashidhar S.N	KLE	SSN	SSN	SSN	SSN	SSN	SSN	SSN
5.	Sulha B. Shihol	KLE	SS	SS	SS	SS	SS	SS	SS
6.	Soumya Bennadi	KLE	SB	SB	SB	SB	SB	SB	SB
7.	Nagarathna S	KLE	NS	NS	NS	NS	NS	NS	NS
8.	Sharadha . H	KLE	SH	SH	SH	SH	SH	SH	SH
9.	Anand C	KLE	AC	AC	AC	AC	AC	AC	AC
10.	Rohini S.H	KLE	RS	RS	RS	RS	RS	RS	RS
11.	Rohini S.H	KLE	RS	RS	RS	RS	RS	RS	RS
12.	Arvi M. Kabbur	KLE	AK	AK	AK	AK	AK	AK	AK
13.									
14.									
15.									
16.									
17.									
18.									
19.									
20.									

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Attendance List

02-07-17	03-07-17	04-07-17	05-07-17	06-07-17	07-07-17	08-07-17
P	P	P	P	P	P	P
S	S	S	S	S	S	S
S	S	S	S	S	S	S
S	S	S	S	S	S	S
S	S	S	S	S	S	S
S	S	S	S	S	S	S
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
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A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A
A	A	A	A	A	A	A