


28.Chisel Programming

<p style="text-align: center;">Registration Form 3 Days Workshop on "Chisel Programming"</p> <p>Name of the Participant:</p> <p>Designation:</p> <p>Department:</p> <p>College Name:</p> <p>Address:</p> <p>Contact Number:</p> <p>Email id:</p> <p>Signature with seal Head of the institute</p>	<p>Resource persons</p> <ol style="list-style-type: none">Rajesh Vardharajan, Director of Engineering SIFive, Daly City, CaliforniaJohn Andrew Koenig, Staff Engineer SIFive, Berkeley, CaliforniaKritik Bhimnan, Staff Engineer SIFive, Bangalore India <p>Chief Patron: Dr. Ashok Shettar Vice Chancellor, KLE Technological University, Hubli Dr. P. G. Tewari, Principal, BVBCET, Hubli</p> <p>Convener: Dr. Nalini C Iyer Head , SoECE , KLE Technological University, Hubli. Landline: 0836-2378250. Email: hod_ece@kletech.ac.in</p> <p>Co coordinators: Prof. Suhas Shirai Asst. Professor, SoECE KLE Technological University, Hubli. +91-9035203578, suhasshirai@kletech.ac.in Prof. Shraddha Hiremath Asst. Professor, SoECE KLE Technological University, Hubli. +91-9620618835, shraddha_h@kletech.ac.in</p>	<p style="text-align: center;">K. L. E Society's</p> <div style="text-align: center;"><p>KLE TECH</p></div> <p style="text-align: center;">KLE Technological University, Vidyanagar, Hubli - 580 031. www.kletech.ac.in</p> <p style="text-align: center;">3 Days Workshop on On "Chisel Programming"</p> <p style="text-align: center;">Under TEQIP 8th to 10th August 2019</p> <p style="text-align: center;">Organized by School of Electronics & Communication Engineering</p> <p style="text-align: center;">Coordinator Dr. Saroja V Siddamal Professor, SoECE KLE Technological University, Hubli.</p>
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3 Days Workshop On Chisel Programming

Chisel is a hardware construction language developed at UC Berkeley that supports advanced hardware design using highly parameterized generators and layered domain-specific hardware languages.

- Embedded in the Scala programming language
- Algebraic construction and wiring
- Hierarchical + object oriented + functional construction
- Highly parameterizable using metaprogramming in Scala
- Supports layering of domain specific languages
- Sizeable standard library including floating-point units
- Multiple dock domains
- Generates low-level Verilog designed to pass on to standard ASIC or FPGA tools

Overview of the Workshop:

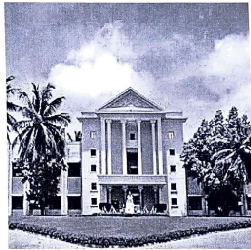
- Design circuit generators ---programs that automatically generate designs from a high-level set of design parameters and constraints.
- Through circuit generators, leverage the hard work of design experts and raise the level of design abstraction for everyone.

Prerequisite

Knowledge of Digital Circuits and Scala Programming

About the University

Established in 1947, BVBCET has achieved an enviable status due to a strong emphasis on academic and technical excellence. Spread over a luxurious 50 acres, the picturesque campus comprises of various buildings with striking architecture. A constant endeavour to keep abreast with technology has resulted in excellent state-of-the-art infrastructure that supplements every engineering discipline.



Currently college offers 12 UG and 8 PG programs affiliated Visvesvaraya Technological University, Belgaum and is recognized by AICTE, New Delhi and accredited by NBA.

About SoECE

Department was established in 1979 & is the pride of Karnataka. The graduates from this department are playing a vital role in the IT revolution and are instrumental in placing Karnataka on the global IT landscape.

The department offers Post Graduate programs in "Digital Electronics" and "VLSI Design and Embedded System". Active engagement of faculty in research has led to recognition of department as a research center by the University.

E&C students are placed in major industries such as IBM, Motorola, Siemens, Alcatel, Intel, Microsoft, Bosch, KPIT etc.

Sankalp semiconductors Pvt. Ltd. is housed in our campus which mentors the staff and students in the area of Analog and Mixed signal design.

The department is working with following industries which have given considerable input in the design of the curriculum in Automotive Electronics, VLSI, Communication and Networking; Robert Bosch, KPIT, Sankalp Semiconductor, ARM India Ltd., Juniper Network, CISCO.

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Workshop on "Chisel Programming"
8th -10th August, 2019

Sl.No.	Name of the participant	08/08/19	09/08/19	10/08/19
1.	Ashwarya M. K	<i>AM</i>	<i>AM</i>	<i>AM</i>
2.	Mandakini M.K	<i>AM</i>	<i>AM</i>	<i>AM</i>
3.	Kaathik .Raoak	<i>Kaathik</i>	<i>Kaathik</i>	<i>Kaathik</i>
4.	Ashwarya H.R	<i>AH</i>	<i>AH</i>	<i>AH</i>
5.	Prateeksha M. R	<i>PR</i>	<i>PR</i>	<i>PR</i>
6.	Vinayak. Vargada	<i>VV</i>	<i>VV</i>	<i>VV</i>
7.	Yogesh .Hodurkar	<i>YH</i>	<i>YH</i>	<i>YH</i>
8.	Pavan. B. Mulimani	<i>PM</i>	<i>PM</i>	<i>PM</i>
9.	Sultan Bepas	<i>Sultan</i>	<i>Sultan</i>	<i>Sultan</i>
10.	Dashan. Guled	<i>DG</i>	<i>DG</i>	<i>DG</i>
11.	Nithin. Angadi	<i>NA</i>	<i>NA</i>	<i>NA</i>
12.	Vineeth. S	<i>VS</i>	<i>VS</i>	<i>VS</i>
13.	Raksh. G. S	<i>RS</i>	<i>RS</i>	<i>RS</i>
14.	Santosh Laxmi	<i>SL</i>	<i>SL</i>	<i>SL</i>
15.	Pooja Bilokai	<i>PB</i>	<i>PB</i>	<i>PB</i>
16.	Jayashree. s. k	<i>JSK</i>	<i>JSK</i>	<i>JSK</i>
17.	AMITA KARAMADE	<i>AK</i>	<i>AK</i>	<i>AK</i>
18.	Tyotino. V. Bhagwat	<i>TVB</i>	<i>TVB</i>	<i>TVB</i>
19.	Amey. J. Joshi	<i>AJ</i>	<i>AJ</i>	<i>AJ</i>

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20.	SP. Sheld			
21.	Siddhika Revankar	SR	SR	SR
22.	SYED AHTEHAM	AS	AS	AS
23.	DARSHAN.N	DL	DL	DL
24.	B.VISHWESHWARA	EL	EL	EL
25.	IS. DEEPAK	IK	IK	IK
26.	Pranav.S.	PS	PS	PS
27.	Sachin Avind	SA	SA	SA
28.	ABEL JOSEPH JOHN	AJ	AJ	AJ
29.	Rashmi Kubad	RK	RK	RK
30.	Priyanka Kulkarni	PK	PK	PK
31.	Ashwin Ray	AR	AR	AR
32.	PRIVA- SULE	PS	PS	PS
33.	Babu Mallappa Rao	BR	BR	BR
34.	GOUTAM NAIK	GN	GN	GN
35.	CHANDAN.V.SHANBHAG	CS	CS	CS
36.	HARDEK S. PATEL	HS	HS	HS
37.	Hansh. Eirodeli	HE	HE	HE
38.	Pradeep Aragi	PA	PA	PA
39.	Ganghi M.G	GM	GM	GM
40.	Vinayak Vengadam	VV	VV	VV
41.	Pratiksha M.R	PM	PM	PM
42.	Pratiksha M.R	PM	PM	PM
43.	Syed Amranchandrabha			
44.	Sanyal Anjum.GP	SA	SA	SA
45.	VANESH.S	VS	VS	VS

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46.	Suhad B.S	Sh	Sh	Sh
47.	Saroja V.S	SB	SB	SB
48.	Shraddha.H	SH	SH	SH
49.	Shashidhar.N	Sheti	Sheti	Sheti
50.	Nagamatma.S	Naga	Naga	Naga
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