

# 51.A Seminar on Entrepreneurial & Job Opportunities in VLSI Design by Mr. Shripad Annigeri

**Title:** A Seminar on Entrepreneurial & Job Opportunities in VLSI Design by Mr. Shripad Annigeri

**Date:** 22<sup>nd</sup> February 2020

**Banner/Brochure:**



## A seminar on Entrepreneurial and Job Opportunities in VLSI Design

This seminar brings awareness about VLSI Design and its subfields, skills to be acquired for job as well as starting your company. It will cover various opportunities available and means to tap them.

**Date:** 22 February 2020  
**Time:** 10.30am  
**Venue:** Mechanical Seminar Hall,  
School of Mechanical Engineering,  
KLE Technological University,  
SVS College Campus, Hubballi



Mr. Shripad Annigeri, has over 20 years of experience in VLSI Industry. He was Director, Analog Mixed Signal IP Development at MegaChips. He is currently engaging students and faculty members of Engineering Colleges in designing Chip following industry like practices. Shripad regularly conducts workshops and training sessions in VLSI Field.

For clarifications connect – 9886707196 / 9742211120

**Photos:**



**Details of the Program/Report:**

Mr. Shripad Annigeri has over 20 years of experience in VLSI Industry. He was Director, Analog Mixed Signal IP Development at MegaChips. He is currently engaging students and faculty members of Engineering Colleges in designing Chip following industry like practices. Shripad regularly conducts workshops and training sessions in VLSI Field. It was an honour to have him interact with students and engage them with his session.

**Number of Participants:** 10

**More about Resource Person:**

Executive with focus on Aligning Execution towards Strategy and Business Direction of the organisation; maintaining a healthy traction between R & D, Product Development and

A Seminar on Entrepreneurial & Job Opportunities in VLSI Design by Mr. Shripad Annigeri

Page 1

DIRECTOR  
CTIE

---

## 51.A Seminar on Entrepreneurial & Job Opportunities in VLSI Design by Mr. Shripad Annigeri

Marketing Teams for greater ROI.

Interaction with EDA Tool Vendors, IP and Service providers in creating win-win conditions for all.

Experience in building teams from scratch to deliver niche products and be critical part of organisation growth.

Defining and continuously upgrading Work Flow and Quality Processes to meet overall quality requirement of the product and customer.

Teaching, mentoring, guiding students.

Specialties : Analog Subsystem/IP Development -- Concept to Productisation.

Technologies :

IP: IO Libraries, PLLs, SERDES, Memory Interface, Custom Analog Cells.

Domain : Consumer and Networking

Integration of Analog Macros in SOCs.

Analog Flow and Model Development.

System Level Design of High Reliability/High Precision Systems.

20+ Years of overall Industry Experience.